

CURRICULUM VITAE

SAIFULLAH KHALID

M.TECH (POWER ELECTRONICS)

C/O M.A.Lais
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1. EDUCATION

2007 **Post Doctorate (D.Sc.)**

Got acceptance for admission in **Institute for International Education, Tomsk Polytechnic University, Russia**

2006 **Ph.D. (Pursuing) (Power Electronics) U.P.T.U, Lucknow**

Supervisor: Prof. Bharti Dwivedi
Department of Electrical Engineering
I.E.T, Lucknow, U.P

Topic: Development of Advanced controllers for APF and their Critical Performance-Based Comparison

2001 – 2003 **M.TECH (Power Electronics) U.P.T.U, Lucknow**

(Second Position in University)
Department of Electrical Engineering
M.M.M. Engg college, Gorakhpur, U.P.
DIVISION: FIRST

1997 – 2001 **B.E. (E&TC) N.M.U, Jalgaon**

Department of Electronics & Telecommunication Engg.
S.E.S College of Engg., Navalnagar, M.S.
DIVISION: FIRST

2. EMPLOYMENT HISTORY

Total Teaching and Research Experience: Approx. 5 years

2006 - Present

Sr. Lecturer
Department of Electronics & Communication Engineering
B.B.D.N.I.T.M, Lucknow

RESEARCH WORK

2004 **Research Assistant**
INSTITUTE OF TECHNOLOGY, BHU
Department of Electrical Engineering
Lanka, Varanasi, U.P.

Title **"Power Quality improvement using Active Power Filter"**

3. PUBLICATIONS

Published:

1. "Analysis and Simulation of UPFC Based on d-q Axis Model" in National Conference Proceedings of CCIS-04 on Jan 24-25, 2004 in Goa, India.
2. "A Review of Future Trends in the Field of Wireless Communication" in National Conference on Next Generation Network Evolution, Application & limitation on Feb 24-25, 2007 in I.T.I., Mankapur, India.
3. "A Growing Approach of IT in Technology Transfer and Business" in National Conference on Information Technology in Governance, Medicine, Industry and Environment on Feb 10-11, 2007 in Dr. M.C.S.I.T.M, Lucknow, India

Papers Accepted:

- 1 Paper accepted entitled "A Review of Active Power Filter and Reactive Power Compensation Techniques" in CIRED 2007, International Conference on Electricity Distribution to be held in Vienna, U.S.A.

Papers Communicated:

- 1 "A Review of United States Patent Apparatuses for Active Power Filters", in IEEE Trans. on Power Electronics.
- 2 "Power Quality Issues, Problems and Related Standards" in International Journal of Emerging Power System.
- 3 "A Review of Networking Evolution in the Field of WDM Optical Communication" in International Conference on Optoelectronics and Communications (OECC/IOOC2007) to be held on July 9-10, 2007 in Japan.
- 4 "A Directional Overview of Latest Researches in Optical Communication" in International Conference on Optoelectronics and Communications (OECC/IOOC2007) to be held on July 9-10, 2007 in Japan.

Research Papers under Process:

- Ø Comparative Critical Analysis of Advanced Controllers used for Active Power Filter

- Ø Global Power System Conditioners: Simulation and Performance Measurement of a Single Phase Active Power Filter.

Books: (In Press)

1. Microprocessor System
2. Basic Electronics
3. Introduction to Radar

WEB:

- Ø Website Named www.geocities.com/microprocessor_system of Microprocessor Subject.
- Ø Website Named www.geocities.com/signals401 of Signals & System Subject.
- Ø Website Named www.geocities.com/ecbbdnitm of EC deptt of BBDNITM..

4. ACHIEVEMENTS:

- 1 First in the job assignment who has given best results i.e. 98% and 92% as a Fresher in teaching assignment for subjects DIC, SSDC etc.
- 2 Continuous giving good results including last two semester's results for subjects Communication System and Microprocessor System-1 100% and 98% respectively.

5. THESIS & PROJECTS

- Ø S.Khalid, Analysis and Simulation of Unified Power Flow Controller (UPFC) based on d-q axis model, M.Tech Thesis, U.P.T.U, Lucknow, U.P, 2003.
- Ø S.Khalid, Study of Flexible AC Transmission System (FACTS) Devices, M.Tech Project, U.P.T.U, Lucknow, U.P, 2002.
- Ø S.Khalid, Alarm Annunciator (Device Controller), B.E. Project, N.M.U, Jalgaon, M.S, 2001.

6. UNIVERSITY TEACHING CONTRIBUTIONS

- Ø Power Electronics
- Ø Basic Electronics & Instrumentation
- Ø Microprocessor
- Ø Communication Systems

7. UNIVERSITY COMMITTEES AND ADMINISTRATIVE RESPONSIBILITIES

- 1 University Evaluator for papers: (i) Digital Integrated Circuits
(ii) Microprocessor
(iii) Basic Electronics
- 2 University practical examiner for final year project (B.TECH)

- 3 Communication Systems laboratory Coordinator
- 4 Power Electronics laboratory Coordinator
- 5 Basic Electronics laboratory Coordinator
- 6 Microprocessor laboratory Coordinator
- 7 Practical Examination Coordinator

8. SEMINAR & TRAINING UNDERGONE:

- Ø Attended a Technical Teacher training program entitled **Blackboard Teaching Methodology** held on Jan 19 -22, 2007 in B.B.D.N.I.TM Lucknow, India.
- Ø Seminar on Comparative Evaluation of UPFC with other FACTS Controllers.
- Ø Four weeks training of Signaling & Exchange in N. E. Railway, Varanasi.

9. SKILLS:

- Ø Modeling and simulation of dynamical systems using mathematical tools such as MATLAB/SIMULINK,
- Ø P-SPICE
- Ø MAGNETIC DESIGNER
- Ø C++

10. EXTRA CURRICULAR ACTIVITIES:

- Ø Worked as a **Head of Portrait Selection Committee** of an **Art Gallery** composed by M.M.M.E.C. Academy of Literary Activities held on March 02, 2003.
- Ø Poem published in magazine "Sun: The Voice of Yuth" in March, 2000, p.no. 108.
- Ø Poem published in magazine "Sun: The Voice of Yuth" in July, 2000, p.no. 60.
- Ø Gotten Consolation Prize in Fevicryl Dip-n-paint contest held at Govt. Queens Inter College, Varanasi on Jan 18, 1991.

Date:

Place:

SAIFULLAH KHALID