

Fermat Research Lab,
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OBJECTIVE Seeking an internship, in a challenging research environment to enhance my knowledge and skills in the area of Low Power VLSI design.

WORK EXPERIENCE

- Sept 2004-July 2006 : **Sequence Design**, Noida, India.
Position: FAE/TM (Field Application/ Technical Marketing Engineer).
- (May – July) {2002, 2003} : **ST Microelectronics** and UBO, Brest, France.
Position: Summer Intern.

EDUCATIONAL QUALIFICATION

- August 2006 – Present : PhD, Computer Engineering, (Expected - May' 2009), Virginia Polytechnic Institute and State University, VA, USA (GPA **4.0/4.0**).
- Sept 2003 – July2004: Master of Engineering in Embedded Systems, ALaRI, USI, Lugano, Switzerland (Total **72/72** Credits (ECTS)).
- July 1999 – July 2003: Bachelor of Technology, Institute of Technology, BHU, Varanasi, India with *honors* (DGPA **8.72/10.00**).

SKILLS

- **Programming skills** – C, C++, TCL.
- **HDLs** : Verilog, VHDL, SystemC, Gezel.
- **EDA tools** – Power Theater (Power Estimation), Physical Studio (STA & SI), Synopsys DC, View Logic, Xilinx Platform studio (XPS), Modelsim, Altera Quartus, NCVerilog, VCS, Cadence–Encounter (P&R).

PUBLICATIONS AND PRESENTATIONS

- *Field Programmable Gate Arrays Based Overcurrent Relays*, journal **Electric Power Components and Systems** (Taylor and Francis), vol. 32, no. 3, March 2004.
- *Implementation of Bi-polar Adder on FPGA*, presented and published in **Chandigarh Symposium on Microelectronics (CSME-2002)** organized by **Indian Microelectronics society**.
- **DRUM: A Reconfigurable Unit Integrated on Heterogeneous Multiprocessing Platform**, 9th symposium on architecture and algorithm/architecture adequation, France, 14-17 October 2003.

RELEVANT COURSEWORK

- **Hardware Architecture Principles and Design**: Low power design, Reprogrammable systems, Design technologies, Computer architecture, Hardware software co-design.
- **Software Systems and Technologies**: RTOS and scheduling, Low power software, Information systems, Formal Languages and Automata theory.
- **System Oriented Courses**: Dependable systems, Networks on chip, Computer systems, Digital circuits and systems, Advance VLSI design.
- **Application Oriented Courses**: Cryptography, Digital signal processing, Control theory.
- **Management Related**: High tech startup, Industrial management, Communication skills.

AWARDS AND HONORS

- Received “**Team Player & Excellent customer orientation**” award at Sequence Worldwide annual meeting 2006.
- Received **First Prize** as member (**VP Marketing Americas**) for innovative idea of “**RE-SHELL**” at Entrepreneurship for High-tech start-ups held at University of Lugano, Lugano, Switzerland.
- Received **Second Prize** for innovative idea of **D-DIVE** at ALaRI, University of Lugano.
- Received **Best paper** award for the Paper “FPGA based protective relays” in All India paper presentation contest by **Institution of Electrical Engineers (IEE, UK)**.