

Developing Embedded Software in C Using ICC11/ICC12/Hiware

Jonathan W. Valvano

[Chapter 1: Program Structure](#)

A sample program introduces C

C is a free field language

Precedence of the operator determines the order of operation

Comments are used to document the software

Preprocessor directives are special operations that occur first

Global declarations provide modular building blocks

Declarations are the basic operations

Function declarations allow for one routine to call another

Compound statements are the more complex operations

Global variables are permanent and can be shared

Local variables are temporary and are private

Source files make it easier to maintain large projects

[Chapter 2: Tokens](#)

ASCII characters

Literals include numbers characters and strings

Keywords are predefined

Names are user

defined

Punctuation marks

Operators

[Chapter 3: Literals include numbers characters and strings](#)

How are numbers represented on the computer

8-bit unsigned numbers

8-bit signed numbers

16-bit unsigned numbers

16-bit signed numbers

Big and little endian

Boolean (true/false)

Decimal numbers

Hexadecimal numbers

Octal numbers

Characters

Strings

Escape sequences

[Chapter 4: Variables](#)

A static variable exists permanently

A static global can be accessed only from within the same file

A static local can be accessed only in the function

We specify volatile variables when using interrupts and I/O ports

Automatic variables are allocated on the stack

We can understand automatics by looking at the assembly code

A constant local can not be changed

External variables are defined elsewhere

The scope of a variable defines where it can be accessed

Variables declarations

8-bit variables are defined with char

Discussion of when to use static versus automatic variables

Initialization of variables and constants

We can understand initialization by looking at the assembly code

[Chapter 5: Expressions](#)

Precedence and associativity

Unary operators

Binary operators

Assignment operators

Expression type and explicit casting

Selection operator

Arithmetic overflow and underflow

[Chapter 6: Flow of Control](#)

Simple statements

Compound statements

if and if-else statements

switch statements

while statements

for statements

do statements

return statements

goto statements

Null statements

[Chapter 7: Pointers](#)

Definitions of address and pointer

Declarations of pointers define the type and allocate space in memory

How do we use pointers

Memory architecture of the 6811 and 6812

Pointer math

Pointer comparisons

FIFO queue implemented with pointers

I/O port access

[Chapter 8: Arrays and Strings](#)

Array Subscripts

Array Declarations

Array References

Pointers and Array Names

Negative Subscripts

Address Arithmetic

String Functions defined in string.h

Fifo Queue Example

[Chapter 9: Structures](#)

Structure Declarations

Accessing elements of a structure

Initialization of structure data

Using pointers to access structures

Passing structures as parameters to functions

Example of MC68HC812A4 extended addressing

Example of a Linear Linked List

Example of a Huffman Code

[Chapter 10: Functions](#)

Function Declarations

Function Definitions

Function Calls

Parameter Passing

Making our C programs "look like" C++

Stack frame created by ICC11 and ICC12

Animation of ICC12 function call

Finite State Machine using Function Pointers

Linked list interpreter

[Chapter 11: Preprocessor Directives](#)

Using #define to create macros

Using #ifdef to implement conditional compilation

Using #include to load other software modules

Using #pragma to write interrupt software

[Chapter 12: Assembly Language](#)

How to insert single assembly instructions

How to compile with a mixture of assembly and C files

Assembler Directives

How to use assembly to optimize a C function

[Appendix 1: Kevin Ross BDM - Adapt812 Interface](#)

Kevin Ross's Background Debug Module

ICC12 options menu for developing software for the Adapt812

Adapt 812 Board Jumpers

What you need to get started

Development Procedure

Web sites for more information

Chapter 0 The Preface

Zero is an appropriate place for a book on C to start. Zero has many special meanings to the C programmer. On the 6812, zero is the address of Port A. On the 6811 zero is the first address in RAM. The compiler will initialize all global variables to zero on start-up. We use a zero to signify the end of a string. A pointer with a zero value is considered a null-pointer (doesn't point to anything). We use a zero value to signify the boolean false, and true is any nonzero value. The array subscripts in C start with zero.

This document serves as an introduction to C programming on the Motorola 6811 and 6812 microcomputers. Its purpose is to provide a short introduction to C programming using the Hiware and ImageCraft ICC11 or ICC12 compilers (contact ImageCraft, 706 Colorado Ave. Suite 10-88, Palo Alto, CA 94303 or see <http://www.imagecraft.com>) (contact Hiware Inc., 5808 Brown Rock Trail, Austin, TX 78749, phone: (512) 301 4500, fax: (512) 301 0957, or see <http://www.hiware.com>).

While the ICC11/ICC12/Hiware manuals explain how to use their compilers, this document explains how to program in C for the Motorola 6811 and 6812. This document was written with a special emphasis on embedded systems. My philosophy about C compilers for the 6811/6812 is that there are three groups of engineers

a) I define a beginner as one who is developing very small programs (less than 1K bytes) with no budget (like a student). In this situation a free compiler is a requirement. Thus, the demo version of Hiware, or the freeware ICC11 version 1 could be used.

b) Next in the progression is the intermediate programmer. This engineer has a budget, but it is very small. The object code size is larger than 1K bytes. Because these projects tend to involve a small number of engineers, the learning curve needs to be short. Because of the limited scope of the project, more time is spent on original coding/debugging and less time on software maintenance. In this situation, the low cost ICC11/ICC12 can be used.

c) For the professional developer, the software projects are large and have a high percentage of time allocated to maintenance, rather than original coding/debugging. For this environment, either ICC11/ICC12 or the full version of Hiware is appropriate.

This document differs from classical C programming books in its emphasis on embedded systems. While reviewing the existing literature on C programming I was stuck by the high percentage of programming examples in these books that rely on the functions **scanf** and **printf** to perform input/output. While I/O is extremely important for embedded systems, rarely is serial I/O with **scanf** and **printf** an important aspect of an embedded system. This HTML document is clearly not comprehensive rather it serves as a short refresher for those C programmers whose skills are a little rusty. This document also assists the experienced programmer trained in another language like Pascal or C++, that now wishes to program in C for an embedded 6811 or 6812 system. If the reader is interested in a more classical approach to C programming I suggest:

[A Book on C: Programming in C](#), by Kelley and Pohl, Addison-Wesley

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The style and structure of this HTML document was derived from [A Small C Compiler: Language, Usage, Theory, and Design](#), by James E. Hendrix.



This book was created on a Macintosh.

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Chapter 1: Program Structure

What's in Chapter 1?

[A sample program introduces C](#)

[C is a free field language](#)

[Precedence of the operator determines the order of operation](#)

[Comments are used to document the software](#)

[Preprocessor directives are special operations that occur first](#)

[Global declarations provide modular building blocks](#)

[Declarations are the basic operations](#)

[Function declarations allow for one routine to call another](#)

[Compound statements are the more complex operations](#)

[Global variables are permanent and can be shared](#)

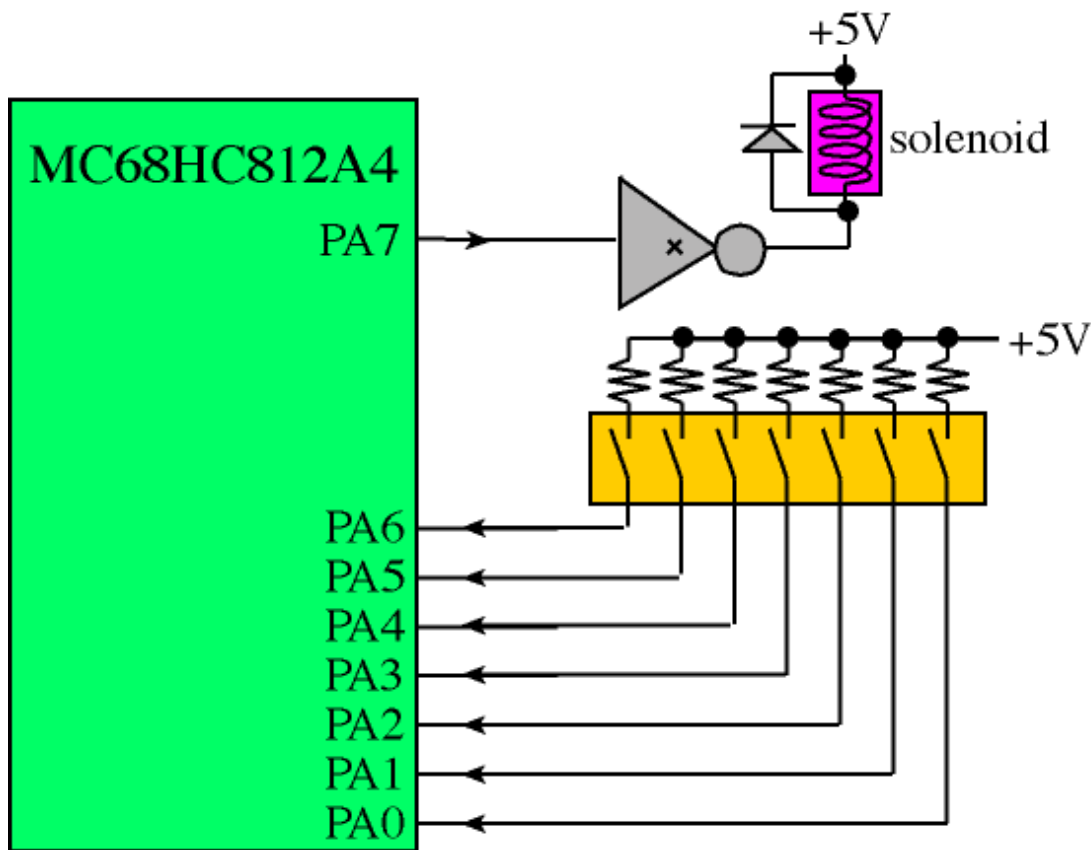
[Local variables are temporary and are private](#)

[Source files make it easier to maintain large projects](#)

This chapter gives a basic overview of programming in C for an embedded system. We will introduce some basic terms so that you get a basic feel for the language. Since this is just the first of many chapters it is not important yet that you understand fully the example programs. The examples are included to illustrate particular features of the language.

Case Study 1: Microcomputer-Based Lock

To illustrate the software development process, we will implement a simple digital lock. The lock system has 7 toggle switches and a solenoid as shown in the following figure. If the 7-bit binary pattern on Port A bits 6-0 becomes 0100011 for at least 10 ms, then the solenoid will activate. The 10 ms delay will compensate for the switch bounce. For information on switches and solenoids see Chapter 8 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano. For now what we need to understand is that Port A bits 6-0 are input signals to the computer and Port A bit 7 is an output signal.



Before we write C code, we need to develop a software plan. Software development is an iterative process. Even though we list steps the development process in a 1,2,3... order, in reality we iterative these steps over and over.

1) We begin with a list of the inputs and outputs. We specify the range of values and their significance. In this example we will use PORTA. Bits 6-0 will be inputs. The 7 input signals represent an unsigned integer from 0 to 127. Port A bit 7 will be an output. If PA7 is 1 then the solenoid will activate and the door will be unlocked. In assembly language, we use #define MACROS to assign a symbolic names, **PORTA DDRA**, to the corresponding addresses of the ports, **\$0000 \$0002**.

```
#define PORTA *(unsigned char volatile*)(0x0000)
#define DDRA *(unsigned char volatile*)(0x0002)
```

2) Next, we make a list of the required data structures. Data structures are used to save information. If the data needs to be permanent, then it is allocates in global space. If the software will change its value then it will be allocated in RAM. In this example we need a 16-bit unsigned counter.

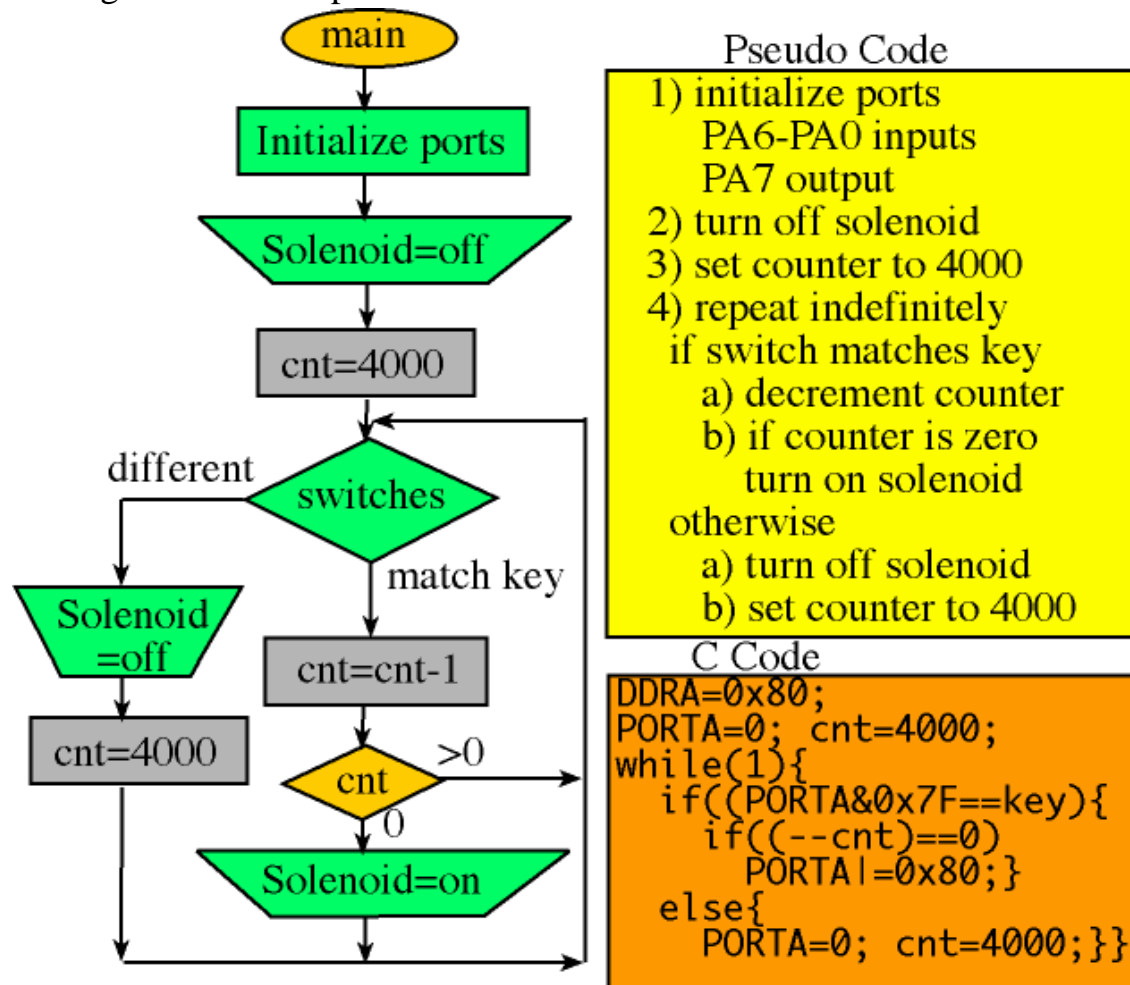
```
unsigned int cnt;
```

If data structure can be defined at compile time and will remain fixed, then it can be allocated in EEPROM. In this example we will define an 8 bit fixed constant to hold the key code, which the operator needs to set to unlock the door. The compiler will place these lines with the program so that they will be defined in ROM or EEPROM memory.

```
const unsigned char key=0x23; // key code
```

It is not real clear at this point exactly where in EEPROM this constant will be, but luckily for us, the compiler will calculate the exact address automatically. After the program is compiled, we can look in the listing file or in the map file to see where in memory each structure is allocated.

3) Next we develop the software algorithm, which is a sequence of operations we wish to execute. There are many approaches to describing the plan. Experienced programmers can develop the algorithm directly in C language. On the other hand, most of us need an abstractive method to document the desired sequence of actions. Flowcharts and pseudo code are two common descriptive formats. There are no formal rules regarding pseudo code, rather it is a shorthand for describing what to do and when to do it. We can place our pseudo code as documentation into the comment fields of our program. The following shows a flowchart on the left and pseudo code and C code on the right for our digital lock example.



Normally we place the programs in ROM or EEPROM. Typically, the compiler will initialize the stack pointer to the last location of RAM. On the 6812, the stack is initialized to 0x0C00. Next we write C code to implement the algorithm as illustrated in the above flowchart and pseudo code.

4) The last stage is debugging. For information on debugging see Chapter 2 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano.

Case Study 2: A Serial Port 6811 Program

Let's begin with a small program. This simple program is typical of the operations we perform in an embedded system. This program will read 8 bit data from parallel port C and transmit the information in serial fashion using the SCI, serial communication interface. The numbers in the first column are

not part of the software, but added to simplify our discussion.

```
1  /* Translates parallel input data to serial outputs */
2  #define PORTC *(unsigned char volatile*)(0x1003)
3  #define DDRC *(unsigned char volatile*)(0x1007)
4  #define BAUD *(unsigned char volatile*)(0x102B)
5  #define SCCR2 *(unsigned char volatile*)(0x102D)
6  #define SCSR *(unsigned char volatile*)(0x102E)
7  #define SCDR *(unsigned char volatile*)(0x102F)
8  void OpenSCI(void) {
9      BAUD=0x30; /* 9600 baud */
10     SCCR2=0x0C;} /* enable SCI, no interrupts */
11     #define TDRE 0x80
12     /* Data is 8 bit value to send out serial port */
13     void OutSCI(unsigned char Data){
14         while ((SCSR & TDRE) == 0); /* Wait for TDRE to be set
15         */
16         SCDR=Data; } /* then output */
17     void main(void){ unsigned char Info;
18         OpenSCI(); /* turn on SCI serial port */
19         DDRC=0x00; /* specify Port C as input */
20         while(1){
21             Info=PORTC; /* input 8 bits from parallel port C */
22             OutSCI(Info);} /* output 8 bits to serial port */
23     extern void _start(); /* entry point in crt11.s */
24     #pragma abs_address:0xffffe
25     void (*reset_vector[])() ={_start};
26     #pragma end_abs_address
```

Listing 1-1: Sample ICC11 Program

The first line of the program is a **comment** giving a brief description of its function. Lines 2 through 7 define **macros** that provide programming access to I/O ports of the 6811. These macros specify the format (unsigned 8 bit) and address (the Motorola microcomputers employ memory mapped I/O). The **#define** invokes the preprocessor that replaces each instance of **PORTC** with ***(unsigned char volatile*)(0x1003)**. For more information see [the section on macros in the preprocessor chapter](#).

Lines 8,9,10 define a **function** or procedure that when executed will initialize the SCI port. The assignment statement is of the form `address=data;` In particular line 9 (`BAUD=0x30;`) will output a hexadecimal \$30 to I/O configuration register at location \$102B. Similarly line 10 will output a hexadecimal \$0C to I/O configuration register at location \$102D. Notice that comments can be added virtually anywhere in order to clarify the software function. **OpenSCI** is an example of a function that is executed only once at the beginning of the program. Another name for an initialization function is **ritual**.

Line 11 is another **#define** that specifies the transmit data ready empty (TDRE) bit as bit 7. This **#define** illustrates the usage of macros that make the software more readable. Line 12 is a comment. Lines 13,14,15 define another function, **OutSCI**, having an 8 bit input parameter that when executed will output the data to the SCI port. In particular line 14 will read the SCI status register at \$102E over and over again until bit 7 (TDRE) is set. Once TDRE is set, it is safe to start another serial output transmission. This is an example of Gadfly or I/O polling. Line 15 copies the input parameter, Data, to the serial port starting a serial transition. Line 15 is an example of an I/O output operation.

Lines 16 through 21 define the main program. After some brief initialization this is where the software will start after a reset or after being powered up. The sequence **unsigned char Info** in line 16 will define a local variable. Notice that the size (**char** means 8 bit), type (**unsigned**) and name (**Info**) are specified. Line 17 calls the ritual function **OpenSCI**. Line 8 writes a 0 to the I/O configuration register at \$1007, specifying all 8 bits of PORTC will be inputs (writing ones to a direction register specifies the current bits as outputs.) The sequence **while(1){ }** defines a control structure that executes forever and never finishes. In particular lines 20 and 21 are repeated over and over without end. Most software on embedded systems will run forever (or until the power is removed.) Line 20 will read the input port C and copy the voltage levels into the variable Info. This is an example of an I/O input operation. Each of the 8 lines that compose PORTC corresponds to one of the 8 bits of the variable Info. A digital logic high, voltage above +2V, is translated into a 1. A digital logic low, voltage less than 0.7V) is translated into a 0. Line 21 will execute the function **OutSCI** that will transmit the 8 bit data via the SCI serial port.

With ICC11/ICC12 lines 22 through 25 define the reset vector so that execution begins at the **_start** location. With Hiware, we would delete lines 22-25, and specify the reset vector in the linker file, *.prm. With both the Hiware and Imagecraft compilers, the system will initialize then jump to the main program.

Free field language

In most programming languages the column position and line number affect the meaning. On the contrary, C is a free field language. Except for preprocessor lines (that begin with #, see [Chapter 11](#)), spaces, tabs and line breaks have the same meaning. The other situation where spaces, tabs and line breaks matter is string constants. We can not type tabs or line breaks within a string constant. For more information see [the section on strings in the constants chapter](#). This means we can place more than one statement on a single line, or place a single statement across multiple lines. For example the function **OpenSCI** could have been written without any line breaks

```
void OpenSCI(void) {BAUD=0x30;SCCR2=0x0C;}
```

"Since we rarely make hardcopy printouts of our software, it is not necessary to minimize the number of line breaks."

Similarly we could have added extra line breaks

```
void OpenSCI(void)
```

```

{
BAUD=
    0x30;
SCCR2=
    0x0C;
}

```

At this point I will warn the reader, just because C allows such syntax, it does not mean it is desirable. After much experience you will develop a programming style that is easy to understand. Although spaces, tabs, and line breaks are syntactically equivalent, their proper usage will have a profound impact on the readability of your software. For more information on programming style see chapter 2 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano, Brooks/Cole Publishing Co., 1999.

A *token* in C can be a user defined name (e.g., the variable **Info** and function **OpenSCI**) or a predefined operation (e.g., *** unsigned while**). Each token must be contained on a single line. We see in the above example that tokens can be separated by white spaces (space, tab, line break) or by the special characters, which we can subdivide into punctuation marks (Table 1-1) and operations (Table 1-2). Punctuation marks ([semicolons](#), [colons](#), [commas](#), [apostrophes](#), [quotation marks](#), [braces](#), [brackets](#), and [parentheses](#)) are very important in C. It is one of the most frequent sources of errors for both the beginning and experienced programmers.

punctuation	Meaning
;	End of statement
:	Defines a label
,	Separates elements of a list
()	Start and end of a parameter list
{ }	Start and stop of a compound statement
[]	Start and stop of a array index
" "	Start and stop of a string
' '	Start and stop of a character constant

Table 1-1: Special characters can be punctuation marks

The next table shows the single character operators. For a description of these operations, see [Chapter 5](#).

operation	Meaning
=	assignment statement
@	address of
?	selection
<	less than

>	greater than
!	logical not (true to false, false to true)
~	1's complement
+	addition
-	subtraction
*	multiply or pointer reference
/	divide
%	modulo, division remainder
	logical or
&	logical and, or address of
^	logical exclusive or
.	used to access parts of a structure

Table 1-2: Special characters can be operators

The next table shows the operators formed with multiple characters. For a description of these operations, see [Chapter 5](#).

operation	Meaning
==	equal to comparison
<=	less than or equal to
>=	greater than or equal to
!=	not equal to
<<	shift left
>>	shift right
++	increment
--	decrement
&&	boolean and
	boolean or
+=	add value to
-=	subtract value to
*=	multiply value to
/=	divide value to
=	or value to
&=	and value to
^=	exclusive or value to
<<=	shift value left
>>=	shift value right
%=	modulo divide value to
->	pointer to a structure

Table 1-3: Multiple special characters also can be operators

Although the operators will be covered in detail in Chapter 9, the following section illustrates some of the common operators. We begin with the assignment operator. Notice that in the line `x=1;` `x` is on the left hand side of the `=`. This specifies the address of `x` is the destination of assignment. On the other hand, in the line `z=x;` `x` is on the right hand side of the `=`. This specifies the value of `x` will be assigned into the variable `z`. Also remember that the line `z=x;` creates two copies of the data. The original value remains in `x`, while `z` also contains this value.

```
int x,y,z; /* Three variables */
void Example(void){
    x=1;      /* set the value of x to 1 */
    y=2;      /* set the value of y to 2 */
    z=x;      /* set the value of z to the value of x (both are 1) */

    x=y=z=0; /* all all three to zero */
}
```

Listing 1-2: Simple program illustrating C arithmetic operators

Next we will introduce the arithmetic operations addition, subtraction, multiplication and division. The standard arithmetic precedence apply. For a detailed description of these operations, see [Chapter 5](#).

```
int x,y,z; /* Three variables */
void Example(void){
    x=1; y=2; /* set the values of x and y */
    z=x+4*y; /* arithmetic operation */
    x++;     /* same as x=x+1; */
    y--;     /* same as y=y-1; */
    x=y<<2; /* left shift same as x=4*y; */
    z=y>>2; /* right shift same as x=y/4; */
    y+=2;   /* same as y=y+2; */
}
```

Listing 1-3: Simple program illustrating C arithmetic operators

Next we will introduce a simple conditional control structure. `PORTB` is an output port, and `PORTE` is an input port on the 6811. For more information on input/output ports see chapter 3 of [Embedded Microcomputer Systems: Real Time Interfacing](#) by Jonathan W. Valvano, Brooks/Cole Publishing Co., 1999. The expression `PORTE&0x04` will return 0 if `PORTE` bit 2 is 0 and will return a 4 if `PORTE` bit 2 is 1. The expression `(PORTE&0x04)==0` will return `TRUE` if `PORTE` bit 2 is 0 and will return a `FALSE` if `PORTE` bit 2 is 1. The statement immediately following the `if` will be executed if the condition is `TRUE`. The `else` statement is optional.

```

#define PORTB *(unsigned char volatile *)(0x1004)
#define PORTE *(unsigned char volatile *)(0x100A)
void Example(void){
    if((PORTE&0x04)==0){ /* test bit 2 of PORTE */
        PORTB=0;} /* if PORTE bit 2 is 0, then make PORTB=0 */
    else{
        PORTB=100;} /* if PORTE bit 0 is not 0, then make PORTB=100
*/
}

```

Listing 1.4: Simple program illustrating the C if else control structure

PORTA bit 3 is another output pin on the 6811. Like the if statement, the while statement has a conditional test (i.e., returns a TRUE/FALSE). The statement immediately following the while will be executed over and over until the conditional test becomes FALSE.

```

#define PORTA *(unsigned char volatile *)(0x1000)
#define PORTB *(unsigned char volatile *)(0x1004)
void Example(void){ /* loop until PORTB equals 200 */
    PORTB=0;
    while(PORTB!=200){
        PORTA = PORTA^0x08;} /* toggle PORTA bit 3 output */
    PORTB++;} /* increment PORTB output */
}

```

Listing 1.5: Simple program illustrating the C while control structure

The for control structure has three parts and a body. `for(part1;part2;part3){body;}`
The first part `PORTB=0` is executed once at the beginning. Then the body `PORTA = PORTA^0x08;` is executed, followed by the third part `PORTB++`. The second part `PORTB!=200` is a conditional. The body and third part are repeated until the conditional is FALSE. For a more detailed description of the control structures, see [Chapter 6](#).

```

#define PORTB *(unsigned char volatile *)(0x1004)
void Example(void){ /* loop until PORTB equals 200 */
    for(PORTB=0;PORTB!=200;PORTB++){
        PORTA = PORTA^0x08;} /* toggle PORTA bit 3 output */
    }
}

```

Listing 1.6: Simple program illustrating the C for loop control structure

Precedence

As with all programming languages the order of the tokens is important. There are two issues to consider when evaluating complex statements. The **precedence** of the operator determines which operations are performed first. In the following example, the $2*x$ is performed first because $*$ has higher precedence than $+$ and $=$. The addition is performed second because $+$ has higher precedence than $=$. The assignment $=$ is performed last. Sometimes we use parentheses to clarify the meaning of the expression, even when they are not needed. Therefore, the line $z=y+2*x;$ could also have been written $z=2*x+y;$ or $z=y+(2*x);$ or $z=(2*x)+y;$.

```
int example(int x, int y){ int z;
    z=y+2*x;
    return(z);
}
```

The second issue is the **associativity**. Associativity determines the left to right or right to left order of evaluation when multiple operations of the precedence are combined. For example $+$ and $-$ have the same precedence, so how do we evaluate the following?

```
z=y-2+x;
```

We know that $+$ and $-$ associate the left to right, this function is the same as $z=(y-2)+x;$. Meaning the subtraction is performed first because it is more to the left than the addition. Most operations associate left to right, but the following table illustrates that some operators associate right to left.

Precedence	Operators	Associativity
highest	() [] . -> ++(postfix) --(postfix)	left to right
	++(prefix) --(prefix) !~ sizeof(type) +(unary) -	right to left
	(unary) &(address) *(dereference)	
	* / %	left to right
	+ -	left to right
	<< >>	left to right
	< <= > >=	left to right
	== !=	left to right
	&	left to right
	^	left to right
		left to right
	&&	left to right
		left to right
	? :	right to left
	= += -= *= /= %= <<= >>= = &=	right to left
	^=	
lowest	,	left to right

Table 1-4: Precedence and associativity determine the order of operation

"When confused about precedence (and aren't we all) add parentheses to clarify the expression."

Comments

There are two types of comments. The first type explains how to use the software. These comments are usually placed at the top of the file, within the header file, or at the start of a function. The reader of these comments will be writing software that uses or calls these routines. Lines 1 and 12 in the above [listing](#) are examples of this type of comment. The second type of comments assists a future programmer (ourselves included) in changing, debugging or extending these routines. We usually place these comments within the body of the functions. The comments on the right of each line in the above [listing](#) are examples of the second type. For more information on writing good comments see chapter 2 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano, Brooks/Cole Publishing Co., 1999.

Comments begin with the `/*` sequence and end with the `*/` sequence. They may extend over multiple lines as well as exist in the middle of statements. The following is the same as `BAUD=0x30;`

```
BAUD /*specifies transmission rate*/=0x30/*9600 bits/sec*/;
```

ICC11 and ICC12 do allow for the use of C++ style comments (see [compiler option dialog](#)). The start comment sequence is `//` and the comment ends at the next line break or end of file. Thus, the following two lines are equivalent:

```
OpenSCI(); /* turn on SCI serial port */
OpenSCI(); // turn on SCI serial port
```

C does allow the comment start and stop sequences within character constants and string constants. For example the following string contains all 7 characters, not just the `ac`:

```
str="a/*b*/c";
```

ICC11 and ICC12 unfortunately do not support comment nesting. This makes it difficult to comment out sections of logic that are themselves commented. For example, the following attempt to comment-out the call to `OpenSCI` will result in a compiler error.

```
void main(void){ unsigned char Info;
/*
  OpenSCI();      /* turn on SCI serial port */
*/
  DDRC=0x00;     /* specify Port C as input */
  while(1){
    Info=PORTC;   /* input 8 bits from parallel port C */
    OutSCI(Info); /* output 8 bits to serial port */
  }
```

The [conditional compilation](#) feature can be used to temporarily remove and restore blocks of code.

Preprocessor Directives

Preprocessor directives begin with # in the first column. As the name implies preprocessor commands are processed first. I.e., the compiler passes through the program handling the preprocessor directives. Although there are many possibilities (assembly language, conditional compilation, interrupt service routines), I thought I'd mention the two most important ones early in this document. We have already seen the macro definition (**#define**) used to define I/O ports and bit fields. A second important directive is the `#include`, which allows you to include another entire file at that position within the program. The following directive will define all the 6811 I/O port names.

```
#include "HC11.h"
```

Examples of `#include` are shown [below](#), and more in [Chapter 11](#).

Global Declarations

An object may be a data structure or a function. Objects that are not defined within functions are global. Objects that may be declared in ICC11/ICC12/Hiware include:

- integer variables (16 bit signed or unsigned)
- character variables (8 bit signed or unsigned)
- arrays of integers or characters
- pointers to integers or characters
- arrays of pointers
- structure (grouping of other objects)
- unions (redefinitions of storage)
- functions

Both Hiware and ICC12 support 32 bit long integers and floating point. In this document we will focus on 8 and 16 bit objects. Oddly the object code generated with the these compilers is often more efficient using 16 bit parameters rather than 8 bit ones.

Declarations and Definitions

It is important for the C programmer to distinguish the two terms *declaration* and *definition*. A function declaration specifies its name, its input parameters and its output parameter. Another name for a function declaration is *prototype*. A data structure declaration specifies its type and format. On the other hand, a function definition specifies the exact sequence of operations to execute when it is called. A function definition will generate object code (machine instructions to be loaded into memory that perform the intended operations). A data structure definition will reserve space in memory for it. The confusing part is that the definition will repeat the declaration specifications. We can declare something without defining it, but we cannot define it without declaring it. For example the declaration for the function `OutSCI` could be written as

```
void OutSCI(unsigned char);
```

We can see that the declaration shows us how to use the function, not how the function works. Because the C compilation is a one-pass process, an object must be declared or defined before it can be used in a statement. (Actually the preprocess performs a pass through the program that handles the preprocessor directives.) Notice that the function `OutSCI` was defined before it was used in the above [listing](#). The following alternative approach first declares the functions, uses them, and lastly defines the functions:

```
/* Translates parallel input data to serial outputs */
#define PORTC *(unsigned char volatile*)(0x1003)
#define DDRC *(unsigned char volatile*)(0x1007)
#define BAUD *(unsigned char volatile*)(0x102B)
#define SCCR2 *(unsigned char volatile*)(0x102D)
#define SCSR *(unsigned char volatile*)(0x102E)
#define SCDR *(unsigned char volatile*)(0x102F)
void OpenSCI(void);
void OutSCI(unsigned char);
void main(void){ unsigned char Info;
  OpenSCI(); /* turn on SCI serial port */
  DDRC=0x00; /* specify Port C as input */
  while(1){
    Info=PORTC; /* input 8 bits from parallel port C */
    OutSCI(Info);} /* output 8 bits to serial port */
void OpenSCI(void) {
  BAUD=0x30; /* 9600 baud */
  SCCR2=0x0C;} /* enable SCI, no interrupts */
/* Data is 8 bit value to send out serial port */
#define TDRE 0x80
void OutSCI(unsigned char Data){
  while ((SCSR & TDRE) == 0); /* Wait for TDRE to be set */
  SCDR=Data; } /* then output */
```

Listing 1-7: Alternate ICC11 Program

An object may be said to exist in the file in which it is defined, since compiling the file yields a module containing the object. On the other hand, an object may be declared within a file in which it does not exist. Declarations of data structures are preceded by the keyword **extern**. Thus,

```
short RunFlag;
```

defines a 16 bit signed integer called `RunFlag`; whereas,

```
extern short RunFlag;
```

only declares the **RunFlag** to exist in another, separately compiled, module. We will use external function declarations in the ICC11/ICC12 VECTOR.C file when we create the reset/interrupt vector table. Thus the line

```
extern void TOFhandler();
```

declares the function name and type just like a regular function declaration. The **extern** tells the compiler that the actual function exists in another module and the linker will combine the modules so that the proper action occurs at run time. The compiler knows everything about extern objects except where they are. The linker is responsible for resolving that discrepancy. The compiler simply tells the assembler that the objects are in fact external. And the assembler, in turn, makes this known to the linker.

Functions

A *function* is a sequence of operations that can be invoked from other places within the software. We can pass 0 or more parameters into a function. The code generated by the ICC11 and ICC12 compilers pass the first input parameter in Register D and the remaining parameters are passed on the stack. A function can have 0 or 1 output parameter. The code generated by the ICC11 and ICC12 compilers pass the return parameter in Register D (8 bit return parameters are promoted to 16 bits.) The add function below has two 16 bit signed input parameters, and one 16 bit output parameter. Again the numbers in the first column are not part of the software, but added to simplify our discussion.

```
1  short add(short x, short y){ short z;
2      z=x+y;
3      if((x>0)&&(y>0)&&(z<0))z=32767;
4      if((x<0)&&(y<0)&&(z>0))z=-32768;
5      return(z);}
6  void main(void){ short a,b;
7      a=add(2000,2000)
8      b=0
9      while(1){
10         b=add(b,1);
11     }
```

Listing 1-8: Example of a function call

The interesting part is that after the operations within the function are performed control returns to the place right after where the function was called. In C, execution begins with the **main** program. The execution sequence is shown below:

```
6  void main(void){ short a,b;
7      a=add(2000,2000);          /* call to add*/
1  short add(short x, short y){ short z;
```

```

2     z=x+y;                               /* z=4000*/
3     if((x>0)&&(y>0)&&(z<0))z=32767;
4     if((x<0)&&(y<0)&&(z>0))z=-32768;
5     return(z);}                          /* return 4000 from call*/
8     b=0
9     while(1){
10      b=add(b,1); }                       /* call to add*/
1 short add(short x, short y){ short z;
2     z=x+y;                               /* z=1*/
3     if((x>0)&&(y>0)&&(z<0))z=32767;
4     if((x<0)&&(y<0)&&(z>0))z=-32768;
5     return(z);}                          /* return 1 from call*/
11  }
9     while(1){
10      b=add(b,1); }                       /* call to add*/
1 short add(short x, short y){ short z;
2     z=x+y;                               /* z=2*/
3     if((x>0)&&(y>0)&&(z<0))z=32767;
4     if((x<0)&&(y<0)&&(z>0))z=-32768;
5     return(z);}                          /* return 2 from call*/
11  }

```

Notice that the return from the first call goes to line 8, while all the other returns go to line 11. The execution sequence repeats lines 9,10,1,2,3,4,5,11 indefinitely.

The programming language Pascal distinguishes between functions and procedures. In Pascal a function returns a parameter while a procedure does not. C eliminates the distinction by accepting a bare or `void` expression as its return parameter.

C does not allow for the nesting of procedural declarations. In other words you can not define a function within another function. In particular all function declarations must occur at the global level.

A function declaration consists of two parts: a *declarator* and a *body*. The declarator states the name of the function and the names of arguments passed to it. The names of the argument are only used inside the function. In the `add` function above, the declarator is **(short x, short y)** meaning it has two 16 bit input parameters. ICC11 and ICC12 accept both approaches for defining the input parameter list. The following three statements are equivalent:

```

short add(short x, short y){ return (x+y);}
short add(x,y)short x; short y;{ return (x+y);}
short add(x,y)short x,y;{ return (x+y);}

```

The parentheses are required even when there are no arguments. When there are no parameters a `void` or nothing can be specified. The following four statements are equivalent:

```

void OpenSCI(void){BAUD=0x30;SCCR2=0x0C;}
OpenSCI(void){BAUD=0x30;SCCR2=0x0C;}
void OpenSCI(){BAUD=0x30;SCCR2=0x0C;}
OpenSCI(){BAUD=0x30;SCCR2=0x0C;}

```

I prefer to include the `void` because it is a positive statement that there are no parameters. For more information on functions see [Chapter 10](#).

The body of a function consists of a statement that performs the work. Normally the body is a compound statement between a `{}` pair. If the function has a return parameter, then all exit points must specify what to return. In the following median filter function shown in Listing 1-4, there are six possible exit paths that all specify a return parameter.

The programs created by ICC11 and ICC12 actually begin execution at a place called **`_start`**. After a power on or hardware reset, the embedded system will initialize the stack, initialize the heap, and clear all RAM-based global variables. After this brief initialization sequence the function named **`main()`** is called. Consequently, there must be a **`main()`** function somewhere in the program. If you are curious about what really happens, look in the assembly file `crt11.s` or `crt12.s`. For programs not in an embedded environment (e.g., running on your PC) a return from **`main()`** transfers control back to the operating system. As we saw earlier, software for an embedded system usually does not quit. Software systems developed with Hiware also perform initialization before calling **`main()`**.

Compound Statements

A *compound statement* (or *block*) is a sequence of statements, enclosed by braces, that stands in place of a single statement. Simple and compound statements are completely interchangeable as far as the syntax of the C language is concerned. Therefore, the statements that comprise a compound statement may themselves be compound; that is, blocks can be nested. Thus, it is legal to write

```

// 3 wide 16 bit signed median filter
short median(short n1,short n2,short n3){
    if(n1>n2){
        if(n2>n3)
            return(n2);    // n1>n2,n2>n3    n1>n2>n3
        else{
            if(n1>n3)
                return(n3);    // n1>n2,n3>n2,n1>n3    n1>n3>n2
            else
                return(n1);    // n1>n2,n3>n2,n3>n1    n3>n1>n2
        }
    }
    else{
        if(n3>n2)
            return(n2);    // n2>n1,n3>n2    n3>n2>n1
        else{

```

```

        if(n1>n3)
            return(n3); // n2>n1,n2>n3,n1>n3 n2>n1>n3
        else
            return(n1); // n2>n1,n2>n3,n3>n1 n2>n3>n1
    }
}

```

Listing 1-9: Example of nested compound statements.

Although C is a free-field language, notice how the indenting has been added to the above example. The purpose of this indenting is to make the program easier to read. On the other hand since C is a free-field language, the following two statements are quite different

```

if(n1>100) n2=100; n3=0;
if(n1>100) {n2=100; n3=0;}

```

In both cases `n2=100;` is executed if `n1>100`. In the first case the statement `n3=0;` is always executed, while in the second case `n3=0;` is executed only if `n1>100`.

Global Variables

Variables declared outside of a function, like `Count` in the following example, are properly called *external* variables because they are defined outside of any function. While this is the standard term for these variables, it is confusing because there is another class of external variable, one that exists in a separately compiled source file. In this document we will refer to variables in the present source file as *globals*, and we will refer to variables defined in another file as *externals*.

There are two reasons to employ global variables. The first reason is data permanence. The other reason is information sharing. Normally we pass information from one module to another explicitly using input and output parameters, but there are applications like interrupt programming where this method is unavailable. For these situations, one module can store data into a global while another module can view it. For more information on accessing shared globals see chapters 4 and 5 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano, Brooks/Cole Publishing Co., 1999.

In the following example, we wish to maintain a counter of the number of times `OutSCI` is called. This data must exist for the entire life of the program. This example also illustrates that with an embedded system it is important to initialize RAM-based globals at run time. Some C compilers like ICC11 and ICC12 will automatically initialize globals to zero at startup.

```

unsigned short Count; /* number of characters transmitted*/
void OpenSCI(void) {
    Count=0;          /* initialize global counter */
    BAUD=0x30;       /* 9600 baud */
}

```

```

    SCCR2=0x0C;} /* enable SCI, no interrupts */
#define TDRE 0x80
void OutSCI(unsigned char Data){
    Count=Count+1;          /* incremented each time */
    while ((SCSR & TDRE) == 0); /* Wait for TDRE to be set */
    SCDR=Data; }           /* then output */

```

Listing 1-10: A global variable contains permanent information

Although the following two examples are equivalent, I like the second case because its operation is more self-evident. In both cases the global is allocated in RAM, and initialized at the start of the program to 1.

```

short Flag=1;
void main(void) {
/* main body goes here */
}

```

Listing 1-11: A global variable initialized at run time by the compiler

```

short Flag;
void main(void) { Flag=1;
/* main body goes here */
}

```

Listing 1-12: A global variable initialized at run time by the compiler

From a programmer's point of view, we usually treat the I/O ports in the same category as global variables because they exist permanently and support shared access.

Local Variables

Local variables are very important in C programming. They contain temporary information that is accessible only within a narrow scope. We can define local variables at the start of a compound statement. We call these *local variables* since they are known only to the block in which they appear, and to subordinate blocks. The following statement adjusts *x* and *y* such that *x* contains the smaller number and *y* contains the larger one. If a swap is required then the local variable *z* is used.

```

if(x>y){ short z;      /* create a temporary variable */
    z=x; x=y; y=z;    /* swap x and y */
}                    /* then destroy z */

```

Notice that the local variable *z* is declared within the compound statement. Unlike globals, which are

said to be *static*, locals are created dynamically when their block is entered, and they cease to exist when control leaves the block. Furthermore, local names supersede the names of globals and other locals declared at higher levels of nesting. Therefore, locals may be used freely without regard to the names of other variables. Although two global variables can not use the same name, a local variable of one block can use the same name as a local variable in another block. Programming errors and confusion can be avoided by understanding these conventions.

Source Files

Our programs may consist of source code located in more than one file. The simplest method of combining the parts together is to use the `#include` preprocessor directive. Another method is to compile the source files separately, then combine the separate object files as the program is being linked with library modules. The linker/library method should be used when the programs are large, and only small pieces are changed at a time. On the other hand, most embedded system applications are small enough to use the simple method. In this way we will compile the entire system whenever changes are made. Remember that a function or variable must be defined or declared before it can be used. The following example is one method of dividing our simple example into multiple files.

```
/* ****file HC11.H **** */
#define PORTC *(unsigned char volatile*)(0x1003)
#define DDRC *(unsigned char volatile*)(0x1007)
#define BAUD *(unsigned char volatile*)(0x102B)
#define SCCR2 *(unsigned char volatile*)(0x102D)
#define SCSR *(unsigned char volatile*)(0x102E)
#define SCDR *(unsigned char volatile*)(0x102F)
```

Listing 1-13: Header file for 6811 I/O ports

```
/* ****file SCI11.H **** */
void OpenSCI(void);
void OutSCI(unsigned char);
```

Listing 1-14: Header file for the SCI interface

```
/* ****file SCI11.C **** */
void OpenSCI(void) {
    BAUD=0x30; /* 9600 baud */
    SCCR2=0x0C; /* enable SCI, no interrupts */
/* Data is 8 bit value to send out serial port */
#define TDRE 0x80
void OutSCI(unsigned char Data){
    while ((SCSR & TDRE) == 0); /* Wait for TDRE to be set */
    SCDR=Data; } /* then output */
```

Listing 1-15: Implementation file for the SCI interface

```

/* ****file VECTOR.C **** */
extern void _start(); /* entry point in crt11.s */
#pragma abs_address:0xffff
void (*reset_vector[])() ={_start};
#pragma end_abs_address

```

Listing 1-16: Reset vector

```

/* ****file MY.C **** */
/* Translates parallel input data to serial outputs */
#include "HC11.H"
#include "SCI11.H"
void main(void){ unsigned char Info;
    OpenSCI(); /* turn on SCI serial port */
    DDRC=0x00; /* specify Port C as input */
    while(1){
        Info=PORTC; /* input 8 bits from parallel port C */
        OutSCI(Info);} /* output 8 bits to serial port */
#include "SCI11.C"
#include "VECTOR.C"

```

Listing 1-17: Main program file for this system

With Hiware, we do not need the VECTOR.C file or the line `#include "VECTOR.C"`. This division is a clearly a matter of style. I make the following general statement about good programming style.

"If the software is easy to understand, debug, and change, then it is written with good style"

While the main focus of this document is on C syntax, it would be improper to neglect all style issues. This system was divided using the following principles:

- Define the I/O ports in a HC11.H or HC12.H header file
- For each module place the user-callable prototypes in a *.H header file
- For each module place the implementations in a *.C program file
- In the main program file, include the header files first
- In the main program file, include the implementation files last

Breaking a software system into files has a lot of advantages. The first reason is code reuse. Consider the code in this example. If a SCI output function is needed in another application, then it would be a simple matter to reuse the SCI11.H and SCI11.C files. The next advantage is clarity. Compare the main program in Listing 1-11 with the entire software system in Listing 1-1. Because the details have been removed, the overall approach is easier to understand. The next reason to break software into files is parallel development. As the software system grows it will be easier to divide up a software project into subtasks, and to recombine the modules into a complete system if the subtasks have

separate files. The last reason is upgrades. Consider an upgrade in our simple example where the 9600 bits/sec serial port is replaced with a high-speed Universal Serial Bus (USB). For this kind of upgrade we implement the USB functions then replace the SCI11.C file with the new version. If we plan appropriately, we should be able to make this upgrade without changes to the files SCI11.H and MY.C.

Go to [Chapter 2 on Tokens](#) Return to [Table of Contents](#)

Chapter 11: Preprocessor Directives

What's in Chapter 11?

[Using #define to create macros](#)

[Using #ifdef to implement conditional compilation](#)

[Using #include to load other software modules](#)

[Using #pragma to write interrupt software](#)

C compilers incorporate a preprocessing phase that alters the source code in various ways before passing it on for compiling. Four capabilities are provided by this facility in C. They are:

- macro processing
- inclusion of text from other files
- conditional compiling
- in-line assembly language

The preprocessor is controlled by directives which are not part of the C language proper. Each directive begins with a #character and is written on a line by itself. Only the preprocessor sees these directive lines since it deletes them from the code stream after processing them.

Depending on the compiler, the preprocessor may be a separate program or it may be integrated into the compiler itself. C has an integrated preprocessor that operates at the front end of its single pass algorithm.

Macro Processing

We use macros for three reasons. 1) To save time we can define a macro for long sequences that we will need to repeat many times. 2) To clarify the meaning of the software we can define a macro giving a symbolic name to a hard-to-understand sequence. The I/O port #define macros are good examples of this reason. 3) To make the software easy to change, we can define a macro such that changing the macro definition, automatically updates the entire software.

```
#define Name CharacterString?...
```

define names which stand for arbitrary strings of text. After such a definition, the preprocessor replaces each occurrence of *Name* (except in string constants and character constants) in the source text with *CharacterString?...* As C implements this facility, the term macro is misleading, since parameterized substitutions are not supported. That is, *CharacterString?...* does not change from one substitution to another according to parameters provided with *Name* in the source text.

C accepts macro definitions only at the global level.

The *Name* part of a macro definition must conform to the standard C naming conventions as described

in [Chapter 2](#). `CharacterString?...` begins with the first printable character following *Name* and continues through the last printable character of the line or until a comment is reached.

If `CharacterString?...` is missing, occurrences of *Name* are simply squeezed out of the text. Name matching is based on the whole name (up to 8 characters); part of a name will not match. Thus the directive

```
#define size 10
```

will change

```
short data[size];
```

into

```
short data[10];
```

but it will have no effect on

```
short data[size1];
```

Replacement is also performed on subsequent **#define** directives, so that new symbols may be defined in terms of preceding ones.

The most common use of **#define** directives is to give meaningful names to constants; i.e., to define so called *manifest constants*. However, we may replace a name with anything at all, a commonly occurring expression or sequence of statements for instance. To disable interrupt during a critical section we could implement.

```
#define START_CRITICAL asm(" tpa\n staa %SaveSP\n sei")
#define END_CRITICAL asm(" ldaa %SaveSP\n tap")
void function(void) {unsigned char SaveSP;
    START_CRITICAL; /* make atomic, entering critical section
*/
    /* we have exclusive access to global variables */
    END_CRITICAL; /* end critical section */
}
```

Listing 11.1: Example of #define

Conditional Compiling

This preprocessing feature lets us designate parts of a program which may or may not be compiled depending on whether or not certain symbols have been defined. In this way it is possible to write into a program optional features which are chosen for inclusion or exclusion by simply adding or removing **#define** directives at the beginning of the program.

When the preprocessor encounters

```
#ifdef Name
```

it looks to see if the designated name has been defined. If not, it throws away the following source lines until it finds a matching

```
#else
```

or

```
#endif
```

directive. The **#endif** directive delimits the section of text controlled by **#ifdef**, and the **#else** directive permits us to split conditional text into true and false parts. The first part (**#ifdef...#else**) is compiled only if the designated name is defined, and the second (**#else...#endif**) only if it is not defined.

The converse of **#ifdef** is the

```
#ifndef Name
```

directive. This directive also takes matching **#else** and **#endif** directives. In this case, however, if the designated name is not defined, then the first (**#ifndef...#else**) or only (**#ifndef...#endif**) section of text is compiled; otherwise, the second (**#else...#endif**), if present, is compiled.

Nesting of these directives is allowed; and there is no limit on the depth of nesting. It is possible, for instance, to write something like

```
#ifdef ABC
... /* ABC */
#ifndef DEF
... /* ABC and not DEF */
#else
... /* ABC and DEF */
#endif
... /* ABC */
#else
```

```

... /* not ABC */
#ifdef HIJ
... /* not ABC but HIJ */
#endif
... /* not ABC */
#endif

```

Listing 11.2: Examples on conditional compilation

where the ellipses represent conditionally compiled code, and the comments indicate the conditions under which the various sections of code are compiled.

A good application of conditional compilation is inserting debugging instruments. In this example the only purpose of writing to PORTC is assist in performance debugging. Once the system is debugged, we can remove all the debugging code, simply by deleting the `#define Debug 1` line.

```

#define Debug 1
int Sub(int j){ int i;
#ifdef Debug
    PORTC|=0x01; /* PC0 set when Sub is entered */
#endif
    i=j+1;
#ifdef Debug
    PORTC&=~0x01; /* PC0 cleared when Sub is exited */
#endif
    return(i);}
void Program(){ int i;
#ifdef Debug
    PORTC|=0x02; /* PC1 set when Program is entered */
#endif
    i=Sub(5);
    while(1) { PORTC=2; i=Sub(i);}
void ProgB(){ int i;
    i=6;
#ifdef Debug
    PORTC&=~0x02; /* PC1 cleared when Sub is exited */
#endif
}

```

Listing 11.3: Conditional compilation can help in removing all debugging code

For more information about debugging see Chapter 2 of Valvano's [Embedded Microcomputer Systems: Real Time Interfacing](#).

Including Other Source Files

The preprocessor also recognizes directives to include source code from other files. The two directives

```
#include "Filename"
```

```
#include <Filename>
```

cause a designated file to be read as input to the compiler. The difference between these two directives is where the compiler looks for the file. The <filename> version will search for the file in the standard include directory, while the "filename" version will search for the file in the same directory as the original source file. The preprocessor replaces these directives with the contents of the designated files. When the files are exhausted, normal processing resumes.

Filename follows the normal MS-DOS file specification format, including drive, path, filename, and extension.

In [Chapter 10](#), an example using #include was presented that implemented a feature similar to encapsulated objects of C++, including private and public functions.

Interrupt software

The ICC11/ICC12 preprocessor also recognizes three pragma directives that we will use to develop interrupt software. We use the `interrupt_handler` pragma to specify a function as an interrupt handler. The compiler will then use the `rti` instruction rather than the `rts` instruction to return from `ExtHan`.

```
#pragma interrupt_handler ExtHan()  
void ExtHan(void){  
    KWIFJ=0x80; // clear flag  
    PutFifo(PORTJ&0x7F);}
```

Listing 11.4: Interrupt service routines are specified using a pragma in ICC11/ICC12.

We use the `abs_address` and `end_abs_address` pragmas to define the interrupt vector.

```
#pragma abs_address:0xffd0  
void (*KeyWakeupJ_interrupt_vector[])() = {  
    ExtHan}; /* 812 KeyWakeUpJ */
```

```
#pragma end_abs_address
```

Listing 11.5: Pragas allow us to define interrupt vectors in ICC11/ICC12.

We also set the reset vector using the `abs_address` and `end_abs_address` pragmas.

```
extern void _start(); /* entry point in crt12.s */
#pragma abs_address:0xffff
void (*Reset_interrupt_vectors[])() = {
    _start }; /* fffe RESET, entry point into ICC12 */
#pragma end_abs_address
```

Listing 11.6: Pragas allow us to define the reset vector in ICC11/ICC12.

We will not use pragmas to develop interrupt software with the Hiware compiler. We use the `interrupt` key word to specify a function as an interrupt handler. The Hiware compiler will then use the `rti` instruction rather than the `rts` instruction to return from `ExtHan`. We start counting the interrupt number from reset. Some of the interrupt numbers used by Hiware for the MC68HC812A4 are shown in the following table.

number	source
24	Key wakeup H
23	Key wakeup J
20	SCIO
16	timer overflow
15	timer channel 7
8	timer channel 0
6	Key wakeup D
4	SWI software interrupt
0	reset

Table 11-1: Interrupt numbers for the MC68HC812A4 used by Hiware

Hiware will automatically set the interrupt vector for KeyWakeup J to point to the `ExtHan` routine.

```
void interrupt 23 ExtHan(void){
    KWIFJ=0x80; // clear flag
    PutFifo(PORTJ&0x7F); }
```

Listing 11.7: Interrupt service routines are specified in Hiware.

We use the `prm` linker file to define the reset vector.

```
LINK keywake.abs
NAMES keywake.o start12s.o ansis.lib END
SECTIONS
MY_RAM = READ_WRITE 0x0800 TO 0x0AFF;
MY_ROM = READ_ONLY 0xF000 TO 0xFF00;
MY_STK = READ_WRITE 0x0B00 TO 0x0BFF;
PLACEMENT
DEFAULT_ROM INTO MY_ROM;
DEFAULT_RAM INTO MY_RAM;
SSTACK INTO MY_STK;
END
/* set reset vector to function _Startup defined in startup code
start12.c */
VECTOR ADDRESS 0xFFFFE _Startup
```

Listing 11.8: The last line of the PRM linker file defines the reset vector in Hiware.

For more information about interrupts see Chapter 4 of Valvano's [Embedded Microcomputer Systems: Real Time Interfacing](#).

Go to [Chapter 12 on Assembly Language](#) Return to [Table of Contents](#)

Chapter 2: Tokens

What's in Chapter 2?

[ASCII characters](#)

[Literals include numbers characters and strings](#)

[Keywords are predefined](#)

[Names are user-defined](#)

[Punctuation marks](#)

[Operators](#)

This chapter defines the basic building blocks of a C program. Understanding the concepts in this chapter will help eliminate the syntax bugs that confuse even the veteran C programmer. A simple syntax error can generate 100's of obscure compiler errors. In this chapter we will introduce some of the syntax of the language.

To understand the syntax of a C program, we divide it into *tokens* separated by *white spaces* and *punctuation*. Remember the white spaces include space, tab, carriage returns and line feeds. A token may be a single character or a sequence of characters that form a single item. The first step of a compiler is to process the program into a list of tokens and punctuation marks. The following example includes punctuation marks of () { } ; The compiler then checks for proper syntax. And, finally, it creates object code that performs the intended operations. In the following example:

```
void main(void){ short z;  
    z=0;  
    while(1){  
        z=z+1;  
    }  
}
```

Listing 2-1: Example of a function call

The following sequence shows the tokens and punctuation marks from the above listing:

```
void main ( void ) { short z ; z = 0 ; while ( 1 ) { z = z + 1 ; } }
```

Since tokens are the building blocks of programs, we begin our study of C language by defining its tokens.

ASCII Character Set

Like most programming languages C uses the standard ASCII character set. The following table shows the 128 standard ASCII code. One or more *white space* can be used to separate tokens and or

punctuation marks. The white space characters in C include horizontal tab (9=\$09), the carriage return (13=\$0D), the line feed (10=\$0A), space (32=\$20).

BITS 4 to 6

	0	1	2	3	4	5	6	7	
	0	NUL	DLE	SP	0	@	P	`	p
B	1	SOH	DC1	!	1	A	Q	a	q
I	2	STX	DC2	"	2	B	R	b	r
T	3	ETX	DC3	#	3	C	S	c	s
S	4	EOT	DC4	\$	4	D	T	d	t
	5	ENQ	NAK	%	5	E	U	e	u
0	6	ACK	SYN	&	6	F	V	f	v
	7	BEL	ETB	'	7	G	W	g	w
T	8	BS	CAN	(8	H	X	h	x
O	9	HT	EM)	9	I	Y	i	y
	A	LF	SUB	*	:	J	Z	j	z
3	B	VT	ESC	+	;	K	[k	{
	C	FF	FS	,	<	L	\	l	
	D	CR	GS	-	=	M]	m	}
	E	SO	RS	.	>	N	^	n	~
	F	S1	US	/	?	O	_	o	DEL

Table 2-1. ASCII Character codes.

The first 32 (values 0 to 31 or \$00 to \$1F) and the last one (127=\$7F) are classified as *control characters*. Codes 32 to 126 (or \$20 to \$7E) include the "normal" characters. Normal characters are divided into

- the space character (32=\$20),
- the numeric digits 0 to 9 (48 to 57 or \$30 to \$39),
- the uppercase alphabet A to Z (65 to 90 or \$41 to \$5A),
- the lowercase alphabet a to z (97 to 122 or \$61 to \$7A), and
- the special characters (all the rest).

Literals

Numeric literals consist of an uninterrupted sequence of digits delimited by white spaces or special characters (operators or punctuation). Although ICC12 and Hiware do support floating point, this document will not cover it. The use of floating point requires a substantial amount of program memory and execution time, therefore most applications should be implemented using integer math. Consequently the period will not appear in numbers as described in this document. For more information about numbers see the sections on [decimals](#), [octals](#), or [hexadecimals](#) in Chapter 3.

Character literals are written by enclosing an ASCII character in apostrophes (single quotes). We would write 'a' for a character with the ASCII value of the lowercase a (97). The control characters can also be defined as constants. For example '\t' is the tab character. For more information about character literals see the section on [characters](#) in Chapter 3.

String literals are written as a sequence of ASCII characters bounded by quotation marks (double quotes). Thus, "ABC" describes a string of characters containing the first three letters of the alphabet in uppercase. For more information about string literals see the section on [strings](#) in Chapter 3.

Keywords

There are some predefined tokens, called *keywords*, that have specific meaning in C programs. The reserved words we will cover in this document are:

keyword	meaning
asm	Insert assembly code
auto	Specifies a variable as automatic (created on the stack)
break	Causes the program control structure to finish
case	One possibility within a switch statement
char	8 bit integer
const	Defines parameter as constant in ROM
continue	Causes the program to go to beginning of loop
default	Used in switch statement for all other cases
do	Used for creating program loops
double	Specifies variable as double precision floating point
else	Alternative part of a conditional
extern	Defined in another module
float	Specifies variable as single precision floating point
for	Used for creating program loops
goto	Causes program to jump to specified location
if	Conditional control structure
int	16 bit integer (same as short on the 6811 and 6812)
long	32 bit integer

register	Specifies how to implement a local
return	Leave function
short	16 bit integer
signed	Specifies variable as signed (default)
sizeof	Built-in function returns the size of an object
static	Stored permanently in memory, accessed locally
struct	Used for creating data structures
switch	Complex conditional control structure
typedef	Used to create new data types
unsigned	Always greater than or equal to zero
void	Used in parameter list to mean no parameter
volatile	Can change implicitly
while	Used for creating program loops

Table 2-2. Keywords have predefined meanings.

Did you notice that all of the keywords in C are lowercase? Notice also that as a matter of style, I used a mixture of upper and lowercase for the names I created, and all uppercase for the I/O ports. It is a good programming practice not to use these keywords for your variable or function names.

Names

We use *names* to identify our variables, functions, and macros. ICC11/ICC12 names may be up to 31 characters long. Hiware names may be up to xxx characters long. Names must begin with a letter or underscore and the remaining characters must be either letters or digits. We can use a mixture of upper and lower case or the underscore character to create self-explaining symbols. E.g.,

time_of_day go_left_then_stop

TimeOfDay GoLeftThenStop

The careful selection of names goes a long way to making our programs more readable. Names may be written with both upper and lowercase letters. The names are case sensitive. Therefore the following names are different:

thetemperature
 THETEMPERATURE
 TheTemperature

The practice of naming macros in uppercase calls attention to the fact that they are not variable names

but defined symbols. Remember the [I/O port names](#) are implemented as macros in the header files HC11.h and HC12.h.

Every global name defined with the ICC11/ICC12 compiler generates an assembly language label of the same name, but preceded by an underscore. The purpose of the underscore is to avoid clashes with the assembler's reserved words. So, as a matter of practice, we should not ordinarily name globals with leading underscores. Hiware labels will not include the underscore. For examples of this naming convention, observe the assembly generated by the compiler (either the assembly itself in the *.s file or the listing file *.lst file.) These assembly names are important during the debugging stages. We can use the map file to get the absolute addresses for these labels, then use the debugger to observe and modify their contents.

Since the Imagecraft compiler adds its own underscore, names written with a leading underscore appear in the assembly file with two leading underscores.

Developing a naming convention will avoid confusion. Possible ideas to consider include:

1. Start every variable name with its type. E.g.,

- b means boolean true/false
- n means 8 bit signed integer
- u means 8 bit unsigned integer
- m means 16 bit signed integer
- v means 16 bit unsigned integer
- c means 8 bit ASCII character
- s means null terminated ASCII string

2. Start every local variable with "the" or "my"

3. Start every global variable and function with associated file or module name. In the following example the names all begin with Bit_. Notice how similar this naming convention recreates the look and feel of the modularity achieved by classes in C++. E.g.,

```
/* *****file=Bit.c*****  
Pointer implementation of the a Bit_Fifo  
These routines can be used to save (Bit_Put) and  
recall (Bit_Get) binary data 1 bit at a time (bit streams)  
Information is saved/recalled in a first in first out manner  
Bit_FifoSize is the number of 16 bit words in the Bit_Fifo  
The Bit_Fifo is full when it has 16*Bit_FifoSize-1 bits */  
#define Bit_FifoSize4  
// 16*4-1=31 bits of storage  
unsigned short Bit_Fifo[Bit_FifoSize]; // storage for Bit  
Stream  
struct Bit_Pointer{
```

```

    unsigned short Mask; // 0x8000, 0x4000, ..., 2, 1
    unsigned short *WPt;}; // Pointer to word containing bit
typedef struct Bit_Pointer Bit_PointerType;
Bit_PointerType Bit_PutPt; // Pointer of where to put next
Bit_PointerType Bit_GetPt; // Pointer of where to get next
/* Bit_FIFO is empty if Bit_PutPt==Bit_GetPt */
/* Bit_FIFO is full if Bit_PutPt+1==Bit_GetPt */
short Bit_Same(Bit_PointerType p1, Bit_PointerType p2){
    if((p1.WPt==p2.WPt)&&(p1.Mask==p2.Mask))
        return(1); //yes
    return(0);} // no
void Bit_Init(void) {
    Bit_PutPt.Mask=Bit_GetPt.Mask=0x8000;
    Bit_PutPt.WPt=Bit_GetPt.WPt=&Bit_Fifo[0]; /* Empty */
}
// returns TRUE=1 if successful,
// FALSE=0 if full and data not saved
// input is boolean FALSE if data==0
short Bit_Put (short data) { Bit_PointerType myPutPt;
    myPutPt=Bit_PutPt;
    myPutPt.Mask=myPutPt.Mask>>1;
    if(myPutPt.Mask==0) {
        myPutPt.Mask=0x8000;
        if(++myPutPt.WPt==&Bit_Fifo[Bit_FifoSize])
            myPutPt.WPt=&Bit_Fifo[0]; // wrap
    }
    if (Bit_Same(myPutPt, Bit_GetPt))
        return(0); /* Failed, Bit_Fifo was full */
    else {
        if(data)
            (*Bit_PutPt.WPt) |= Bit_PutPt.Mask; // set bit
        else
            (*Bit_PutPt.WPt) &= ~Bit_PutPt.Mask; // clear bit
        Bit_PutPt=myPutPt;
        return(1);
    }
}
// returns TRUE=1 if successful,
// FALSE=0 if empty and data not removed
// output is boolean 0 means FALSE, nonzero is true
short Bit_Get (unsigned short *datap) {
    if (Bit_Same(Bit_PutPt, Bit_GetPt))
        return(0); /* Failed, Bit_Fifo was empty */
    else {
        *datap=(*Bit_GetPt.WPt)&Bit_GetPt.Mask;
        Bit_GetPt.Mask=Bit_GetPt.Mask>>1;

```

```

        if(Bit_GetPt.Mask==0) {
            Bit_GetPt.Mask=0x8000;
            if(++Bit_GetPt.WPt)==&Bit_Fifo[Bit_FifoSize])
                Bit_GetPt.WPt=&Bit_Fifo[0]; // wrap
        }
        return(1);
    }
}

```

Listing 2-2: This naming convention can create modularity similar to classes in C++.

Punctuation

Punctuation marks ([semicolons](#), [colons](#), [commas](#), [apostrophes](#), [quotation marks](#), [braces](#), [brackets](#), and [parentheses](#)) are very important in C. It is one of the most frequent sources of errors for both the beginning and experienced programmers.

Semicolons

Semicolons are used as statement terminators. Strange and confusing syntax errors may be generated when you forget a semicolon, so this is one of the first things to check when trying to remove syntax errors. Notice that one semicolon is placed at the end of every simple statement in the following example

```

#define PORTB *(unsigned char volatile *)(0x1004)
void Step(void){
    PORTB = 10;
    PORTB = 9;
    PORTB = 5;
    PORTB = 6;}

```

Listing 2-3: Semicolons are used to separate one statement from the next.

Preprocessor directives do not end with a semicolon since they are not actually part of the C language proper. Preprocessor directives begin in the first column with the # and conclude at the end of the line. The following example will fill the array `DataBuffer` with data read from the input port (PORTC). We assume in this example that Port C has been initialized as an input. Semicolons are also used in the `for` loop statement (see also [Chapter 6](#)), as illustrated by

```

void Fill(void){ short j;
    for(j=0;j<100;j++){
        DataBuffer[j]=PORTC;}
}

```

```
}
```

Listing 2-4: Semicolons are used to separate three fields of the for statement.

Colons

We can define a label using the colon. Although C has a `goto` statement, I discourage its use. I believe the software is easier to understand using the block-structured control statements (`if`, `if else`, `for`, `while`, `do while`, and `switch case`.) The following example will return after the Port C input reads the same value 100 times in a row. Again we assume Port C has been initialized as an input. Notice that every time the current value on Port C is different from the previous value the counter is reinitialized.

```
char Debounce(void){ short Cnt; unsigned char LastData;
Start:    Cnt=0;          /* number of times Port C is the same
*/
        LastData=PORTC;
Loop:    if(++Cnt==100) goto Done;      /* same thing 100 times
*/
        if(LastData!=PORTC) goto Start; /* changed */
        goto Loop;
Done:    return(LastData); }
```

Listing 2-4: Colons are used to define labels (places we can jump to)

Colons also terminate `case`, and `default` prefixes that appear in `switch` statements. For more information see the section on [switch](#) in Chapter 6. In the following example, the next stepper motor output is found (the proper sequence is 10,9,5,6). The default case is used to restart the pattern.

```
unsigned char NextStep(unsigned char step){ unsigned char
theNext;
    switch(step){
        case 10: theNext=9; break;
        case 9: theNext=5; break;
        case 5: theNext=6; break;
        case 6: theNext=10; break;
        default: theNext=10;
    }
    return(theNext); }
```

Listing 2-5: Colons are also used to with the `switch` statement

For both applications of the colon (`goto` and `switch`), we see that a label is created that is a

potential target for a transfer of control.

Commas

Commas separate items that appear in lists. We can create multiple variables of the same type. E.g.,

```
unsigned short beginTime, endTime, elapsedTime;
```

Lists are also used with functions having multiple parameters (both when the function is defined and called):

```
short add(short x, short y){ short z;
    z=x+y;
    if((x>0)&&(y>0)&&(z<0))z=32767;
    if((x<0)&&(y<0)&&(z>0))z=-32768;
    return(z);}
void main(void){ short a,b;
    a=add(2000,2000)
    b=0
    while(1){
        b=add(b,1);
    }
```

Listing 2-6: Commas separate the parameters of a function

Lists can also be used in general expressions. Sometimes it adds clarity to a program if related variables are modified at the same place. The value of a list of expressions is always the value of the last expression in the list. In the following example, first `thetime` is incremented, `thedata` is decremented, then `x` is set to `k+2`.

```
x=(thetime++,--thedata,k+2);
```

Apostrophes

Apostrophes are used to specify character literals. For more information about character literals see the section on [characters](#) in Chapter 3. Assuming the function `OutChar` will print a single ASCII character, the following example will print the lower case alphabet:

```
void Alphabet(void){ unsigned char mych;
    for(mych='a';mych<='z';mych++){
        OutChar(mych);} /* Print next letter */
}
```

Listing 2-7: Apostrophes are used to specify characters

Quotation marks

Quotation marks are used to specify string literals. For more information about string literals see the section on [strings](#) in Chapter 3. Example

```
unsigned char Name[12]; /* Place for 11 characters and
termination*/
void InitName(void){
    Name="Hello World";
}
```

Listing 2-8: Quotation marks are used to specify strings

The command `Letter='A' ;` places the ASCII code (65) into the variable `Letter`. The command `pt="A" ;` creates an ASCII string and places a pointer to it into the variable `pt`.

Braces

Braces `{ }` are used throughout C programs. The most common application is for creating a compound statement. Each open brace `{` must be matched with a closing brace `}`. One approach that helps to match up braces is to use indenting. Each time an open brace is used, the source code is tabbed over. In this way, it is easy to see at a glance the brace pairs. Examples of this approach to tabbing are the [Bit_Put](#) function within Listing 2-2 and the median function in [Listing 1-4](#).

Brackets

Square brackets enclose array *dimensions* (in declarations) and *subscripts* (in expressions). Thus,

```
short Fifo[100];
```

declares an integer array named `Fifo` consisting of 100 words numbered from 0 through 99, and

```
PutPt = &Fifo[0];
```

assigns the variable `PutPt` to the address of the first entry of the array.

Parentheses

Parentheses enclose argument lists that are associated with function declarations and calls. They are required even if there are no arguments.

As with all programming languages, C uses parentheses to control the order in which expressions are evaluated. Thus, $(11+3)/2$ yields 7, whereas $11+3/2$ yields 12. Parentheses are very important when

writing expressions.

Operators

The special characters used as *expression operators* are covered in the [operator section](#) in chapter 5.

There are many operators, some of which are single characters

~ ! @ % ^ & * - + = | / : ? < > ,

while others require two characters

++ -- << >> <= += -
= *= /= == |= %= &= ^= || && !=

and some even require three characters

<<= >>=

The multiple-character operators can not have white spaces or comments between the characters.

The C syntax can be confusing to the beginning programmer. For example

```
z=x+y;    /* sets z equal to the sum of x and y */  
z=x_y;    /* sets z equal to the value of x_y */
```

Go to [Chapter 3 on Literals](#) Return to [Table of Contents](#)

Chapter 3: Numbers, Characters and Strings

What's in Chapter 3?

[How are numbers represented on the computer](#)

[8-bit unsigned numbers](#)

[8-bit signed numbers](#)

[16-bit unsigned numbers](#)

[16-bit signed numbers](#)

[Big and little endian](#)

[Boolean \(true/false\)](#)

[Decimal numbers](#)

[Hexadecimal numbers](#)

[Octal numbers](#)

[Characters](#)

[Strings](#)

[Escape sequences](#)

This chapter defines the various data types supported by the compiler. Since the objective of most computer systems is to process data, it is important to understand how data is stored and interpreted by the software. We define a *literal* as the direct specification of the number, character, or string. E.g.,

```
100 'a' "Hello World"
```

are examples of a number literal, a character literal and a string literal respectively. We will discuss the way data are stored on the computer as well as the C syntax for creating the literals. The Imagecraft and Hiware compilers recognize three types of literals (*numeric*, *character*, *string*). Numbers can be written in three bases (*decimal*, *octal*, and *hexadecimal*). Although the programmer can choose to specify numbers in these three bases, once loaded into the computer, the all numbers are stored and processed as unsigned or signed binary. Although C does not support the binary literals, if you wanted to specify a binary number, you should have no trouble using either the octal or hexadecimal format.

Binary representation

Numbers are stored on the computer in binary form. In other words, information is encoded as a sequence of 1's and 0's. *Precision* is the number of distinct or different values. We express precision in alternatives, decimal digits, bytes, or binary bits. We use the expression $4^{1/2}$ decimal digits to mean about 20,000 alternatives, and the expression $4^{3/4}$ decimal digits to mean more than 20,000 alternatives but less than 100,000 alternatives. The following table illustrates the various representations of precision.

binary bits	bytes	alternatives	decimal digits
8	1	256	$2^{1/2}$

10		1024	3
12		4096	3 3/4
16	2	65,536	4 3/4
20		1,048,576	5
24	3	16,777,216	7 1/2
30		1,073,741,824	9
32	4	4,294,967,296	9 3/4

Table 3-1. Relationships between various representations of precision.

Observation: A good rule of thumb to remember is $2^{10 \cdot n}$ is about $10^{3 \cdot n}$.

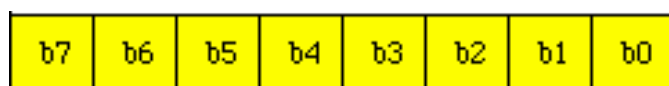
For large numbers we use abbreviations, as shown in the following table. For example, 16K means $16 \cdot 1024$ which equals 16384. Computer engineers use the same symbols as other scientists, but with slightly different values.

abbreviation	pronunciation	Computer Engineering Value	Scientific Value
K	"kay"	2^{10} 1024	10^3
M	"meg"	2^{20} 1,048,576	10^6
G	"gig"	2^{30} 1,073,741,824	10^9
T	"tera"	2^{40} 1,099,511,627,776	10^{12}
P	"peta"	2^{50} 1,125,899,906,843,624	10^{15}
E	"exa"	2^{60} 1,152,921,504,606,846,976	10^{18}

Table 3-2. Common abbreviations for large numbers.

8-bit unsigned numbers

A byte contains 8 bits



where each bit b_7, \dots, b_0 is binary and has the value 1 or 0. We specify b_7 as the *most significant bit* or MSB, and b_0 as the least significant bit or LSB. If a byte is used to represent an unsigned number, then the value of the number is

$$N = 128 \cdot b_7 + 64 \cdot b_6 + 32 \cdot b_5 + 16 \cdot b_4 + 8 \cdot b_3 + 4 \cdot b_2 + 2 \cdot b_1 + b_0$$

There are 256 different unsigned 8-bit numbers. The smallest unsigned 8-bit number is 0 and the largest is 255. For example, 00001010_2 is $8+2$ or 10. Other examples are shown in the following table.

binary	hex	Calculation	decimal
00000000	0x00		0
01000001	0x41	$64+1$	65
00010110	0x16	$16+4+2$	22
10000111	0x87	$128+4+2+1$	135
11111111	0xFF	$128+64+32+16+8+4+2+1$	255

Table 3-3. Example conversions from unsigned 8-bit binary to hexadecimal and to decimal.

The *basis* of a number system is a subset from which linear combinations of the basis elements can be used to construct the entire set. For the unsigned 8-bit number system, the basis is

$$\{ 1, 2, 4, 8, 16, 32, 64, 128 \}$$

One way for us to convert a decimal number into binary is to use the basis elements. The overall approach is to start with the largest basis element and work towards the smallest. One by one we ask ourselves whether or not we need that basis element to create our number. If we do, then we set the corresponding bit in our binary result and subtract the basis element from our number. If we do not need it, then we clear the corresponding bit in our binary result. We will work through the algorithm with the example of converting 100 to 8 bit binary. We with the largest basis element (in this case 128) and ask whether or not we need to include it to make 100. Since our number is less than 128, we do not need it so bit 7 is zero. We go the next largest basis element, 64 and ask do we need it. We do need 64 to generate our 100, so bit 6 is one and subtract 100 minus 64 to get 36. Next we go the next basis element, 32 and ask do we need it. Again we do need 32 to generate our 36, so bit 5 is one and we subtract 36 minus 32 to get 4. Continuing along, we need basis element 4 but not 16 8 2 or 1, so bits 43210 are 00100 respectively. Putting it together we get 01100100_2 (which means $64+32+4$).

Observation: If the least significant binary bit is zero, then the number is even.

Observation: If the right most n bits (least significant) are zero, then the number is divisible by 2^n .

Number	Basis	Need it	bit	Operation
100	128	no	bit7=0	none
100	64	yes	bit6=1	subtract 100-64
36	32	yes	bit5=1	subtract 36-32
4	16	no	bit4=0	none
4	8	no	bit3=0	none
4	4	yes	bit2=1	subtract 4-4
0	2	no	bit1=0	none
0	1	no	bit0=0	none

Table 3-4. Example conversion from decimal to unsigned 8-bit binary to hexadecimal.

We define an unsigned 8-bit number using the `unsigned char` format. When a number is stored into an `unsigned char` it is converted to 8-bit unsigned value. For example

```
unsigned char data; // 0 to 255
unsigned char function(unsigned char input){
    data=input+1;
    return data;}

```

8-bit signed numbers

If a byte is used to represent a *signed 2's complement* number, then the value of the number is

$$N = -128 \cdot b_7 + 64 \cdot b_6 + 32 \cdot b_5 + 16 \cdot b_4 + 8 \cdot b_3 + 4 \cdot b_2 + 2 \cdot b_1 + b_0$$

There are also 256 different signed 8 bit numbers. The smallest signed 8-bit number is -128 and the largest is 127. For example, 10000010_2 is $-128+2$ or -126. Other examples are shown in the following table.

binary	hex	Calculation	decimal
00000000	0x00		0
01000001	0x41	$64+1$	65
00010110	0x16	$16+4+2$	22
10000111	0x87	$-128+4+2+1$	-121

11111111 0xFF -128+64+32+16+8+4+2+1 -1

Table 3-5. Example conversions from signed 8-bit binary to hexadecimal and to decimal.

For the signed 8-bit number system the basis is

{ 1, 2, 4, 8, 16, 32, 64, -128 }

Observation: The most significant bit in a 2's complement signed number will specify the sign.

Notice that the same binary pattern of 11111111_2 could represent either 255 or -1. It is very important for the software developer to keep track of the number format. The computer can not determine whether the 8-bit number is signed or unsigned. You, as the programmer, will determine whether the number is signed or unsigned by the specific assembly instructions you select to operate on the number. Some operations like addition, subtraction, and shift left (multiply by 2) use the same hardware (instructions) for both unsigned and signed operations. On the other hand, multiply, divide, and shift right (divide by 2) require separate hardware (instruction) for unsigned and signed operations. For example, the 6805/6808/6811 multiply instruction, **mul**, operates only on unsigned values. So if you use the **mul** instruction, you are implementing unsigned arithmetic. The Motorola 6812 has both unsigned, **mul**, and signed, **smul**, multiply instructions. So if you use the **smul** instruction, you are implementing signed arithmetic. The compiler will automatically choose the proper implementation.

It is always good programming practice to have clear understanding of the data type for each number, variable, parameter, etc. For some operations there is a difference between the signed and unsigned numbers while for others it does not matter.

	<u>signed different from unsigned</u>		<u>signed same as unsigned</u>
/ %	division	+	addition
*	multiplication	-	subtraction
>	greater than	==	is equal to
<	less than		logical or
>=	greater than or equal to	&	logical and
<=	less than or equal to	^	logical exclusive or
>>	right shift	<<	left shift

Table 3-6. Operations either depend or don't depend on whether the number is signed/unsigned.

The point is that care must be taken when dealing with a mixture of numbers of different sizes and

types.

Similar to the unsigned algorithm, we can use the basis to convert a decimal number into signed binary. We will work through the algorithm with the example of converting -100 to 8-bit binary. We start with the largest basis element (in this case -128) and decide do we need to include it to make -100. Yes (without -128, we would be unable to add the other basis elements together to get any negative result), so we set bit 7 and subtract the basis element from our value. Our new value is -100 minus -128, which is 28. We go to the next largest basis element, 64 and ask do we need it. We do not need 64 to generate our 28, so bit6 is zero. Next we go to the next basis element, 32 and ask do we need it. We do not need 32 to generate our 28, so bit5 is zero. Now we need the basis element 16, so we set bit4, and subtract 16 from our number 28 (28-16=12). Continuing along, we need basis elements 8 and 4 but not 2 or 1, so bits 3210 are 1100. Putting it together we get 100111002 (which means -128+16+8+4).

Number	Basis	Need it	bit	Operation
-100	-128	yes	bit7=1	subtract -100 - -128
28	64	no	bit6=0	none
28	32	no	bit5=0	none
28	16	yes	bit4=1	subtract 28-16
12	8	yes	bit3=1	subtract 12-8
4	4	yes	bit2=1	subtract 4-4
0	2	no	bit1=0	none
0	1	no	bit0=0	none

Table 3-7. Example conversion from decimal to signed 8-bit binary to hexadecimal.

Observation: To take the negative of a 2's complement signed number we first complement (flip) all the bits, then add 1.

A second way to convert negative numbers into binary is to first convert them into unsigned binary, then do a 2's complement negate. For example, we earlier found that +100 is 011001002. The 2's complement negate is a two step process. First we do a logic complement (flip all bits) to get 100110112. Then add one to the result to get 100111002.

A third way to convert negative numbers into binary is to first subtract the number from 256, then convert the unsigned result to binary using the unsigned method. For example, to find -100, we subtract 256 minus 100 to get 156. Then we convert 156 to binary resulting in 100111002. This method works because in 8 bit binary math adding 256 to number does not change the value. E.g., 256-100 is the same value as -100.

Common Error: An error will occur if you use signed operations on unsigned numbers, or use unsigned operations on signed numbers.

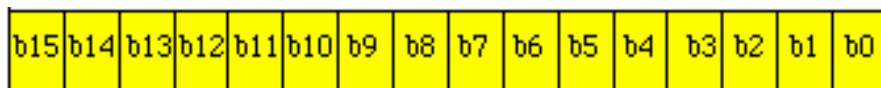
Maintenance Tip: To improve the clarity of our software, always specify the format of your data (signed versus unsigned) when defining or accessing the data.

We define a signed 8-bit number using the `char` format. When a number is stored into a `char` it is converted to 8-bit signed value. For example

```
char data; // -128 to 127
char function(char input){
    data=input+1;
    return data;}
```

16 bit unsigned numbers

A *word* or *double byte* contains 16 bits



where each bit b_{15}, \dots, b_0 is binary and has the value 1 or 0. If a word is used to represent an unsigned number, then the value of the number is

$$\begin{aligned} N = & 32768 \cdot b_{15} + 16384 \cdot b_{14} + 8192 \cdot b_{13} + 4096 \cdot b_{12} \\ & + 2048 \cdot b_{11} + 1024 \cdot b_{10} + 512 \cdot b_9 + 256 \cdot b_8 \\ & + 128 \cdot b_7 + 64 \cdot b_6 + 32 \cdot b_5 + 16 \cdot b_4 + 8 \cdot b_3 + 4 \cdot b_2 + 2 \cdot b_1 + b_0 \end{aligned}$$

There are 65,536 different unsigned 16-bit numbers. The smallest unsigned 16-bit number is 0 and the largest is 65535. For example, $0010,0001,1000,0100_2$ or $0x2184$ is $8192+256+128+4$ or 8580. Other examples are shown in the following table.

binary	hex	Calculation	decimal
--------	-----	-------------	---------

0000,0000,0000,0000	0x0000		0
0000,0100,0000,0001	0x0401	1024+1	1025
0000,1100,1010,0000	0x0CA0	2048+1024+128+32	3232
1000,1110,0000,0010	0x8E02	32768+2048+1024+512+2	36354
1111,1111,1111,1111	0xFFFF	32768+16384+8192+4096+2048+1024 +512+256+128+64+32+16+8+4+2+1	65535

Table 3-8. Example conversions from unsigned 16-bit binary to hexadecimal and to decimal.

For the unsigned 16-bit number system the basis is

$$\{ 1, 2, 4, 8, 16, 32, 64, 128, 256, 512, 1024, 2048, 4096, 8192, 16384, 32768 \}$$

If a word is used to represent a signed 2's complement number, then the value of the number is

$$\begin{aligned}
 N = & -32768 \cdot b_{15} + 16384 \cdot b_{14} + 8192 \cdot b_{13} + 4096 \cdot b_{12} \\
 & + 2048 \cdot b_{11} + 1024 \cdot b_{10} + 512 \cdot b_9 + 256 \cdot b_8 \\
 & + 128 \cdot b_7 + 64 \cdot b_6 + 32 \cdot b_5 + 16 \cdot b_4 + 8 \cdot b_3 + 4 \cdot b_2 + 2 \cdot b_1 + b_0
 \end{aligned}$$

We define an unsigned 16-bit number using the unsigned short format. When a number is stored into an unsigned short it is converted to 16-bit unsigned value. For example

```

unsigned short data; // 0 to 65535
unsigned short function(unsigned short input){
    data=input+1;
    return data;}

```

16-bit signed numbers

There are also 65,536 different signed 16-bit numbers. The smallest signed 16-bit number is -32768 and the largest is 32767. For example, 1101,0000,0000,0100₂ or 0xD004 is -32768+16384+4096+4 or -12284. Other examples are shown in the following table.

binary	hex	Calculation	decimal
0000,0000,0000,0000	0x0000		0
0000,0100,0000,0001	0x0401	1024+1	1025
0000,1100,1010,0000	0x0CA0	2048+1024+128+32	3232
1000,0100,0000,0010	0x8402	-32768+1024+2	-31742
1111,1111,1111,1111	0xFFFF	-32768+16384+8192+4096+2048+1024 +512+256+128+64+32+16+8+4+2+1	-1

Table 3-9. Example conversions from signed 16-bit binary to hexadecimal and to decimal.

For the signed 16-bit number system the basis is

{ 1, 2, 4, 8, 16, 32, 64, 128, 256, 512, 1024, 2048, 4096, 8192, 16384, -32768 }

Maintenance Tip: To improve the quality of our software, we should always specify the precision of our data when defining or accessing the data.

We define a signed 16-bit number using the `short` format. When a number is stored into a `short` it is converted to 16-bit signed value. For example

```
short data; // -32768 to 32767
short function(short input){
    data=input+1;
    return data;}
```

Big and Little Endian

When we store 16-bit data into memory it requires two bytes. Since the memory systems on most computers are byte addressable (a unique address for each byte), there are two possible ways to store in memory the two bytes that constitute the 16-bit data. Motorola microcomputers implement the *big endian* approach that stores the most significant part first. Intel microcomputers implement the *little endian* approach that stores the least significant part first. The PowerPC is *biendian*, because it can be configured to efficiently handle both big and little endian. For example, assume we wish to store the 16 bit number 1000 (0x03E8) at locations 0x50,0x51, then

address	contents	address	contents
0x0050	0x03	0x0050	0xE8
0x0051	0xE8	0x0051	0x03

Big Endian **Little Endian**

We also can use either the big or little endian approach when storing 32-bit numbers into memory that is byte (8-bit) addressable. If we wish to store the 32-bit number 0x12345678 at locations 0x50-0x53 then

address	contents	address	contents
0x0050	0x12	0x0050	0x78
0x0051	0x34	0x0051	0x56
0x0052	0x56	0x0052	0x34
0x0053	0x78	0x0053	0x12

Big Endian Little Endian

In the above two examples we normally would not pick out individual bytes (e.g., the 0x12), but rather capture the entire multiple byte data as one nondivisible piece of information. On the other hand, if each byte in a multiple byte data structure is individually addressable, then both the big and little endian schemes store the data in first to last sequence. For example, if we wish to store the 4 ASCII characters '6811' which is 0x36383131 at locations 0x50-0x53, then the ASCII '6'=0x36 comes first in both big and little endian schemes.

address	contents
0x0050	0x36
0x0051	0x38
0x0052	0x31
0x0053	0x31

Big and Little Endian

The term "Big Endian" comes from Jonathan Swift's satire Gulliver's Travels. In Swift's book, a Big Endian refers to a person who cracks their egg on the big end. The Lilliputians considered the big endians as inferiors. The big endians fought a long and senseless war with the Lilliputians who insisted it was only proper to break an egg on the little end.

Common Error: An error will occur when data is stored in Big Endian by one computer and read in Little Endian format on another.

Boolean information

A boolean number has two states. The two values could represent the logical true or false. The positive logic representation defines true as a 1 or high, and false as a 0 or low. If you were controlling a motor, light, heater or air conditioner the boolean could mean on or off. In communication systems, we represent the information as a sequence of booleans: mark or space. For black or white graphic displays we use booleans to specify the state of each pixel. The most efficient storage of booleans on a computer is to map each boolean into one memory bit. In this way, we could pack 8 booleans into each

byte. If we have just one boolean to store in memory, out of convenience we allocate an entire byte or word for it. Most C compilers including ICC11/ICC12/Hiware define:

```
False be all zeros, and
True be any nonzero value.
```

Many programmers add the following macros

```
#define TRUE 1
#define FALSE 0
```

Decimal Numbers

Decimal numbers are written as a sequence of decimal digits (0 through 9). The number may be preceded by a plus or minus sign or followed by a **L** or **U**. Lower case **l** or **u** could also be used. The minus sign gives the number a negative value, otherwise it is positive. The plus sign is optional for positive values. Unsigned 16-bit numbers between 32768 and 65535 should be followed by **U**. You can place a **L** at the end of the number to signify it to be a 32-bit signed number. The range of a decimal number depends on the data type as shown in the following table.

<u>type</u>	<u>range</u>	<u>precision</u>	<u>examples</u>
unsigned char	0 to 255	8 bits	0 10 123
char	-127 to 127	8 bits	-123 0 10 +10
unsigned int	0 to 65535U	16 bits	0 2000 2000U 50000U
int	-32767 to 32767	16 bits	-1000 0 1000 +20000
unsigned short	0 to 65535U	16 bits	0 2000 2000U 50000U
short	-32767 to 32767	16 bits	-1000 0 1000 +20000
long	-2147483647L to 2147483647L	32 bits	-1234567L 0L 1234567L

Table 3-10. The range of decimal numbers.

Because the 6811 and 6812 microcomputers are most efficient for 16 bit data (and not 32 bit data), the **unsigned int** and **int** data types are 16 bits. On the other hand, on a x86-based machine, the **unsigned int** and **int** data types are 32 bits. In order to make your software more compatible with other machines, it is preferable to use the **short** type when needing 16 bit data and the **long** type for 32 bit data.

<u>type</u>	<u>6811/6812</u>	<u>x86</u>
unsigned char	8 bits	8 bits
char	8 bits	8 bits
unsigned int	16 bits	32 bits
int	16 bits	32 bits
unsigned short	16 bits	16 bits
short	16 bits	16 bits

long

32 bits

32 bits

Table 3-11. Differences between a 6811/6812 and an x86

Since the 6811 and 6812 microcomputers do not have direct support of 32-bit numbers, the use of long data types should be minimized. On the other hand, a careful observation of the code generated yields the fact that these compilers are more efficient with 16 bit numbers than with 8 bit numbers.

Decimal numbers are reduced to their two's complement or unsigned binary equivalent and stored as 8/16/32-bit binary values.

The manner in which decimal literals are treated depends on the context. For example

```
short I;
unsigned short J;
char K;
unsigned char L;
long M;
void main(void){
    I=97;    /* 16 bits 0x0061 */
    J=97;    /* 16 bits 0x0061 */
    K=97;    /* 8 bits 0x61 */
    L=97;    /* 8 bits 0x61 */
    M=97;    /* 32 bits 0x00000061 */}
```

The 6812 code generated by the ICC12 compiler is as follows

```
.area text
_main::
    pshx
    tfr s,x
    movw #97,_I    ;16 bits
    movw #97,_J    ;16 bits
    movb #97,_K    ;8 bits
    movb #97,_L    ;8 bits
    ldy #L2
    jsr __ly2reg ;32 bits
    ldy #_M
    jsr __lreg2y
    tfr x,s
    pulx
    rts
.area bss
_M:: .blkb 4
_L:: .blkb 1
_K:: .blkb 1
```

```

_J:: .blkb 2
_I:: .blkb 2
     .area text
L2: .word 0,97

```

The 6812 code generated by the Hiware compiler is much more efficient when dealing with 32 bit long integers

```

LDAB #97
CLRA
STD I
STD J
STAB K
STAB L
STD M:2
CLRB
STD M
RTS

```

Octal Numbers

If a sequence of digits begins with a leading **0**(zero) it is interpreted as an octal value. There are only eight octal digits, 0 through 7. As with decimal numbers, octal numbers are converted to their binary equivalent in 8-bit or 16-bit words. The range of an octal number depends on the data type as shown in the following table.

<u>type</u>	<u>range</u>	<u>precision</u>	<u>examples</u>
unsigned char	0 to 0377	8 bits	0 010 0123
char	-0200 to 0177	8 bits	-0123 0 010 +010
unsigned int	0 to 0177777	16 bits	0 02000 0150000U
int	-077777 to 077777	16 bits	-01000 0 01000 +020000
unsigned short	0 to 0177777	16 bits	0 02000 0150000U
short	-077777 to 077777	16 bits	-01000 0 01000 +020000
long	-017777777777L to 017777777777L	32 bits	-01234567L 0L 01234567L

Table 3-12. The range of octal numbers.

Notice that the octal values 0 through 07 are equivalent to the decimal values 0 through 7. One of the advantages of this format is that it is very easy to convert back and forth between octal and binary. Each octal digit maps directly to/from 3 binary digits.

Hexadecimal Numbers

The hexadecimal number system uses base 16 as opposed to our regular decimal number system that

uses base 10. Like the octal format, the hexadecimal format is also a convenient mechanism for us humans to represent binary information, because it is extremely simple for us to convert back and forth between binary and hexadecimal. A *nibble* is defined as 4 binary bits. Each value of the 4-bit nibble is mapped into a unique hex digit.

Hex Digit	Decimal Value	Binary Value
0	0	0000
1	1	0001
2	2	0010
3	3	0011
4	4	0100
5	5	0101
6	6	0110
7	7	0111
8	8	1000
9	9	1001
A or a	10	1010
B or b	11	1011
C or c	12	1100
D or d	13	1101
E or e	14	1110
F or f	15	1111

Table 3-13. Definition of hexadecimal representation.

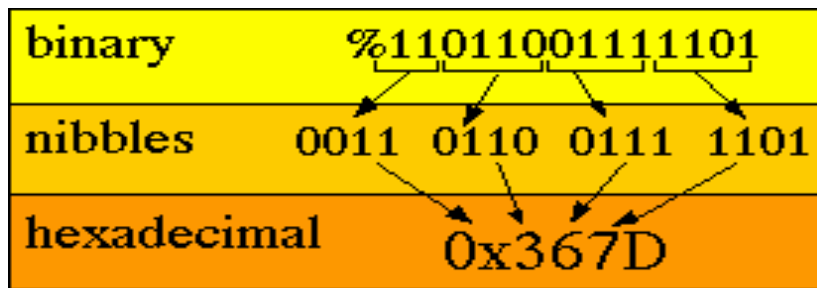
Computer programming environments use a wide variety of symbolic notations to specify the numbers in various bases. The following table illustrates various formats for numbers

environment	binary format	hexadecimal format	decimal format
Motorola assembly language	%01111010	\$7A	122
Intel and TI assembly language	01111010B	7AH	122
C language	-	0x7A	122

Table 3-14. Various hexadecimal formats.

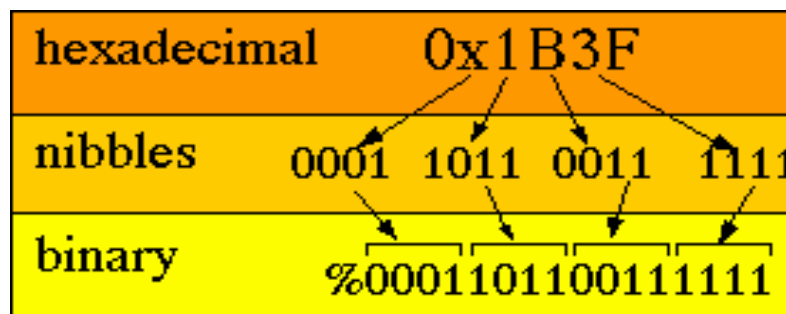
To convert from binary to hexadecimal we can:

- 1) divide the binary number into right justified nibbles;
- 2) convert each nibble into its corresponding hexadecimal digit.



To convert from hexadecimal to binary we can:

- 1) convert each hexadecimal digit into its corresponding 4 bit binary nibble;
- 2) combine the nibbles into a single binary number.



If a sequence of digits begins with `0x` or `0X` then it is taken as a hexadecimal value. In this case the word digits refers to hexadecimal digits (0 through F). As with decimal numbers, hexadecimal numbers are converted to their binary equivalent in 8-bit bytes or 16-bit words. The range of a hexadecimal number depends on the data type as shown in the following table.

<u>type</u>	<u>range</u>	<u>precision</u>	<u>examples</u>
unsigned char	0x00 to 0xFF	8 bits	0x01 0x3a 0xB3
char	-0x7F to 0x7F	8 bits	-0x01 0x3a -0x7B
unsigned int	0x0000 to 0xFFFF	16 bits	0x22 0Xabcd 0xF0A6
int	-0x7FFF to 0x7FFF	16 bits	-0x22 0X0 +0x70A6
unsigned short	0x0000 to 0xFFFF	16 bits	0x22 0Xabcd 0xF0A6
short	-0x7FFF to 0x7FFF	16 bits	-0x1234 0x0 +0x7abc
long	-0x7FFFFFFFFF to 0x7FFFFFFFFF	32 bits	-0x1234567 0xABCDEF

Table 3-15. The range of hexadecimal numbers.

Character Literals

Character literals consist of one or two characters surrounded by apostrophes. The manner in which character literals are treated depends on the context. For example

```

short I;
unsigned short J;
char K;
unsigned char L;
long M;
void main(void){
    I='a';    /* 16 bits 0x0061 */
    J='a';    /* 16 bits 0x0061 */
    K='a';    /* 8 bits 0x61 */
    L='a';    /* 8 bits 0x61 */
    M='a';    /* 32 bits 0x00000061 */}

```

The 6812 code generated by the ICC12 compiler is as follows

```

    .area text
_main::
    pshx
    tfr s,x
    movw #97,_I    ;16 bits
    movw #97,_J    ;16 bits
    movb #97,_K    ;8 bits
    movb #97,_L    ;8 bits
    ldy #L2
    jsr __ly2reg ;32 bits
    ldy #_M
    jsr __lreg2y
    tfr x,s
    pulx
    rts
    .area bss
_M:: .blkb 4
_L:: .blkb 1
_K:: .blkb 1
_J:: .blkb 2
_I:: .blkb 2
    .area text
L2: .word 0,97

```

The 6812 code generated by the Hiware compiler is as follows

```

LDAB #97
CLRA
STD I
STD J
STAB K

```

```
STAB L
STD M:2
CLRB
STD M
RTS
```

All [standard ASCII characters](#) are positive because the high-order bit is zero. In most cases it doesn't matter if we declare character variables as signed or unsigned. On the other hand, we have seen earlier that the compiler treats signed and unsigned numbers differently. Unless a character variable is specifically declared to be unsigned, its high-order bit will be taken as a sign bit. Therefore, we should not expect a character variable, which is not declared unsigned, to compare equal to the same character literal if the high-order bit is set. For more on this see [Chapter 4 on Variables](#).

String Literals

Strictly speaking, C does not recognize character strings, but it does recognize arrays of characters and provides a way to write character arrays, which we call *strings*. Surrounding a character sequence with quotation marks, e.g., "**Jon**", sets up an array of characters and generates the address of the array. In other words, at the point in a program where it appears, a string literal produces the address of the specified array of character literals. The array itself is located elsewhere. ICC11 and ICC12 will place the strings into the text area. I.e., the string literals are considered constant and will be defined in the ROM of an embedded system. This is very important to remember. Notice that this differs from a character literal which generates the value of the literal directly. Just to be sure that this distinct feature of the C language is not overlooked, consider the following example:

```
char *pt;
void main(void){
    pt="Jon"; /* pointer to the string */
    printf(pt); /* passes the pointer not the data itself */
}
```

The 6812 code generated by the ICC12 compiler is as follows

```
.area text
_main::
    movw #L2,_pt
    ldd _pt
    jsr _printf
    rts
.area bss
_pt:: .blkb 2
.area text
L2: .byte 'J','o','n',0
```

The 6812 code generated by the Hiware compiler is virtually the same as ICC12. Both compilers place

the string in memory and use a pointer to it when calling `printf`. ICC12 will pass the parameter in RegD, while Hiware pushes the parameter on the stack.

```
MOVW # "Jon", pt
LDD pt
PSHD
JSR printf
PULD
RTS
```

Notice that the pointer, `pt`, is allocated in RAM (`.area bss`) and the string is stored in ROM (`.area text`). The assignment statement `pt = "Jon";` copies the address not the data. Similarly, the function `printf()` must receive the address of a string as its first (in this case, only) argument. First, the address of the string is assigned to the character pointer `pt` (ICC11/ICC12 use the 16 bit Register D for the first parameter). Unlike other languages, the string itself is not assigned to `pt`, only its address is. After all, `pt` is a 16-bit object and, therefore, cannot hold the string itself. The same program could be written better as

```
void main(void){
    printf("Jon"); /* passes the pointer not the data itself */
}
```

Notice again that the program passes a pointer to the string into `printf()`, and not the string itself. The 6812 code generated by the ICC12 compiler is as follows

```
.area text
_main::
    ldd #L2
    jsr _printf
    rts
.area text
L2: .byte 'J','o','n',0
```

Except for the parameter passing, the 6812 code generated by the Hiware compiler is virtually the same as ICC12.

```
LDD # "Jon"
PSHD
JSR printf
PULD
RTS
```

In this case, it is tempting to think that the string itself is being passed to `printf()`; but, as before, only its address is.

Since strings may contain as few as one or two characters, they provide an alternative way of writing character literals in situations where the address, rather than the character itself, is needed.

It is a convention in C to identify the end of a character string with a null (zero) character. Therefore, C compilers automatically suffix character strings with such a terminator. Thus, the string "**Jon**" sets up an array of four characters ('J', 'o', 'n', and zero) and generates the address of the first character, for use by the program.

Remember that 'A' is different from "A", consider the following example:

```
char letter,*pt;
void main(void){
    pt="A";          /* pointer to the string */
    letter='A';     /* the data itself ('A' ASCII 65=$41) */
}
```

The 6812 code generated by the ICC12 compiler is as follows

```
.area text
_main::
    movw #L2,_pt
    movb #65,_letter
    rts
.area bss
_letter:: .blkb 1
_pt:: .blkb 2
.area text
L2: .byte 'A,0
```

The 6812 code generated by the Hiware compiler is as follows

```
MOVW #"A",pt
LDAB #65
STAB letter
RTS
```

Escape Sequences

Sometimes it is desirable to code nongraphic characters in a character or string literal. This can be done by using an *escape sequence*--a sequence of two or more characters in which the first (escape) character changes the meaning of the following character(s). When this is done the entire sequence generates only one character. C uses the backslash (\) for the escape character. The following escape sequences are recognized by the ICC11/ICC12/Hiware compilers:

<u>sequence</u>	<u>name</u>	<u>value</u>
-----------------	-------------	--------------

<code>\n</code>	newline, linefeed	<code>\$0A = 10</code>
<code>\t</code>	tab	<code>\$09 = 9</code>
<code>\b</code>	backspace	<code>\$08 = 8</code>
<code>\f</code>	form feed	<code>\$0C = 12</code>
<code>\a</code>	bell	<code>\$07 = 7</code>
<code>\r</code>	return	<code>\$0D = 13</code>
<code>\v</code>	vertical tab	<code>\$0B = 11</code>
<code>\0</code>	null	<code>\$00 = 0</code>
<code>\"</code>	ASCII quote	<code>\$22 = 34</code>
<code>\\</code>	ASCII back slash	<code>\$5C = 92</code>
<code>\'</code>	ASCII single quote	<code>\$27 = 39</code>

Table 3-16. The escape sequences supported by ICC11 ICC12 and Hiware.

Other nonprinting characters can also be defined using the `\ooo` octal format. The digits `ooo` can define any 8-bit octal number. The following three lines are equivalent:

```
printf( "\tJon\n" );
printf( "\11Jon\12" );
printf( "\011Jon\012" );
```

The term *newline* refers to a single character which, when written to an output device, starts a new line. Some hardware devices use the ASCII carriage return (13) as the newline character while others use the ASCII line feed (10). It really doesn't matter which is the case as long as we write `\n` in our programs. Avoid using the ASCII value directly since that could produce compatibility problems between different compilers.

There is one other type of escape sequence: anything undefined. If the backslash is followed by any character other than the ones described above, then the backslash is ignored and the following character is taken literally. So the way to code the backslash is by writing a pair of backslashes and the way to code an apostrophe or a quote is by writing `\'` or `\"` respectively.

Go to [Chapter 4 on Variables](#) Return to [Table of Contents](#)

Chapter 4: Variables and Constants

What's in Chapter 4?

[A static variable exists permanently](#)

[A static global can be accessed only from within the same file](#)

[A static local can be accessed only in the function](#)

[We specify volatile variables when using interrupts and I/O ports](#)

[Automatic variables are allocated on the stack](#)

[We can understand automatics by looking at the assembly code](#)

[A constant local can not be changed](#)

[External variables are defined elsewhere](#)

[The scope of a variable defines where it can be accessed](#)

[Variables declarations](#)

[8-bit variables are defined with char](#)

[Discussion of when to use static versus automatic variables](#)

[Initialization of variables and constants](#)

[We can understand initialization by looking at the assembly code](#)

The purpose of this chapter is to explain how to create and access variables and constants. The storage and retrieval of information are critical operations of any computer system. This chapter will also present the C syntax and resulting assembly code generated by the ImageCraft and Hiware compilers.

A *variable* is a named object that resides in RAM memory and is capable of being examined and modified. A variable is used to hold information critical to the operation of the embedded system. A *constant* is a named object that resides in memory (usually in ROM) and is only capable of being examined. As we saw in the last chapter a *literal* is the direct specification of a number character or string. The difference between a literal and a constant is that constants are given names so that they can be accessed more than once. For example

```
short MyVariable;           /* variable allows read/write access
*/
const short MyConstant=50; /* constant allows only read access
*/
#define fifty 50
void main(void){
    MyVariable=50;          /* write access to the variable */
    OutSDec(MyVariable);   /* read access to the variable */
    OutSDec(MyConstant);   /* read access to the constant */
    OutSDec(50);           /* "50" is a literal */
    OutSDec(fifty);        /* fifty is also a literal */
}
```

Listing 4-1: Example showing a variable, a constant and some literals

With ICC11 and ICC12 both **int** and **short** specify to 16-bit parameters, and can be used interchangeably. The compiler options in Hiware can be used to select the precision of each of the data formats. I recommend using **short** because on many computers, **int** specifies a 32-bit parameter. As we saw in the last chapter, the ICC11 and ICC12 compilers actually implement 32-bit long [integer literals](#) and [string literals](#) in a way very similar to constants.

The concepts of [precision](#) and type ([unsigned](#) vs. [signed](#)) developed for numbers in the last chapter apply to variables and constants as well. In this chapter we will begin the discussion of variables that contain integers and characters. Even though pointers are similar in many ways to 16 bit unsigned integers, pointers will be treated in detail in [Chapter 7](#). Although arrays and structures fit also the definition of a variable, they are regarded as collections of variables and will be discussed in [Chapter 8](#) and [Chapter 9](#).

The term *storage class* refers to the method by which an object is assigned space in memory. The Imagecraft and Hiware compilers recognize three storage classes--[static](#), [automatic](#), and [external](#). In this document we will use the term *global variable* to mean a regular static variable that can be accessed by all other functions. Similarly we will use the term *local variable* to mean an automatic variable that can be accessed only by the function that created it. As we will see in the following sections there are other possibilities like a [static global](#) and [static local](#).

Statics

Static variables are given space in memory at some fixed location within the program. They exist when the program starts to execute and continue to exist throughout the program's entire lifetime. The value of a static variable is faithfully maintained until we change it deliberately (or remove power from the memory). A constant, which we define by adding the modifier **const**, can be read but not changed.

In an embedded system we normally wish to place all variables in RAM and constants in ROM. In the ICC11/ICC12 compilers we specify the starting memory address for the static variables in the *options_compiler_linker* dialog with the data section. The constants and program instructions will be placed in the *text* section. For more information on how to set the absolute addresses for statics (*data* section), automatics (stack), and program object codes (text section) using ICC12, see [ICC12 options menu for developing software for the Adapt812](#) The ICC11/ICC12 compilers place the static variables in the bss area, which we can view in the assembly listing following the **.area bss** pseudoop. The ICC11/ICC12 compilers place the constants and program in the text area, which we can view in the assembly listing following the **.area text** pseudoop.

At the assembly language ICC11/ICC12 uses the **.blkb** directive to define a block of uninitialized bytes. Each static variable has a label associated with its **.blkb** directive. The label consists of the variable's name prefixed by a compiler generated underscore character. The following example sets a global, called TheGlobal, to the value 1000. This global can be referenced by any function from any

file in the software system. It is truly global.

```
short TheGlobal;    /* a regular global variable*/
void main(void){
    TheGlobal=1000;
}
```

Listing 4-2: Example showing a regular global variable

In assembly language the ICC11 assembler defines a label to be global (can be accessed from modules in other files) using the *.global* pseudoop. The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
    .area text
    .global _main
_main:
    ldd #1000
    std _TheGlobal
    rts
    .area bss
    .global _TheGlobal
_TheGlobal: .blkb 2
```

In assembly language the ICC12 assembler defines a label to be global (can be accessed from modules in other files) using the *::* symbol after the label. The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
    .area text
_main::
    movw #1000,_TheGlobal
    rts
    .area bss
_TheGlobal:: .blkb 2
```

The 6812 code generated by the Hiware compiler is as follows

```
main:
    LDD #1000
    STD TheGlobal
    RTS
```

The fact that these types of variables exist in permanently reserved memory means that static variables exist for the entire life of the program. When the power is first applied to an embedded computer, the values in its RAM are usually undefined. Therefore, initializing global variables requires special run-time software consideration. The ICC11/ICC12 compilers will attach the

assembly code in the CRT11.s/CRT12.s file to the beginning of every program. This software is executed first, before our **main()** program is started. We can see by observing the CRT11.s/CRT12.s file that the ICC11/ICC12 compilers will clear all statics to zero immediately after a hardware reset. See [the section on initialization](#) for more about initializing variables and constants.

A **static global** is very similar to a regular global. In both cases, the variable is defined in RAM permanently. The assembly language access is identical. The only difference is the scope. The static global can only be accessed within the file where it is defined. The following example also sets a global, called **TheGlobal**, to the value 1000. This global can not be referenced by modules in other files. In particular, notice the line `.global _TheGlobal` is missing in the 6811 code. Similarly, notice the double colon, `::`, is replaced by a single colon, `:`, in the 6812 code. In both cases, this static global can not be referenced outside the scope of this file.

```
static short TheGlobal; /* a static global variable*/
void main(void){
    TheGlobal=1000;
}
```

Listing 4-3: Example showing a static global variable

The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
.area text
.global _main
_main:
    ldd #1000
    std _TheGlobal
    rts
.area bss
_TheGlobal: .blkb 2
```

The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
.area text
_main::
    movw #1000,_TheGlobal
    rts
.area bss
_TheGlobal: .blkb 2
```

The 6812 code generated by the Hiware compiler is the same as a regular global. Hiware does properly limit the access only to the static global to functions defined in this file.

```
main:
    LDD #1000
```

```
STD TheGlobal
RTS
```

A **static local** is similar to the static global. Just as with the other statics, the variable is defined in RAM permanently. The assembly language code generated by the compiler that accesses the variable is identical. The only difference is the scope. The static local can only be accessed within the function where it is defined. The following example sets a static local, called **TheLocal**, to the value 1000. The compiler limits the access to the static local, so that this variable can not be accessed by other functions in this file or in other files. Notice that the assembly language name of the static local is a unique compiler-generated name (L2 in this example.) This naming method allows other functions to also define a static local or automatic local with the same name.

```
void main(void){
    static stort TheLocal;    /* a static local variable*/
    TheLocal=1000;
}
```

Listing 4-4: Example showing a static local variable

The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
.area text
.global _main
_main:
    ldd #1000
    std L2
    rts
.area bss
L2: .blkb 2
```

The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
.area text
_main::
    movw #1000,L2
    rts
.area bss
L2: .blkb 2
```

Again the 6812 code generated by the Hiware compiler is the same as a regular global. Hiware does properly limit the access only to the static local to the function in which it is defined.

```
main:
    LDD #1000
    STD TheLocal
```

A **static local** can be used to save information from one instance of the function call to the next. Assume each function wished to know how many times it has been called. Remember upon reset, the ICC11/ICC12/Hiware compilers will initialize all statics to zero (including static locals). The following functions maintain such a count, and these counts can not be accessed by other functions. Even though the names are the same, the two static locals are in fact distinct.

```
void function1(void){
    static short TheCount;
    TheCount=TheCount+1;
}
void function2(void){
    static short TheCount;
    TheCount=TheCount+1;
}
```

Listing 4-5: Example showing two static local variables with the same name

The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
.area text
.global _function1
_function1:
    ldd L2
    addd #1
    std L2
    rts
.global _function2
_function2:
    ldd L3
    addd #1
    std L3
    rts
.area bss
L2: .blkb 2
L3: .blkb 2
```

The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
.area text
_function1::
    ldd L2
    addd #1
    std L2
```

```

    rts
_function2::
    ldd L3
    addd #1
    std L3
    rts
    .area bss
L2: .blkb 2
L3: .blkb 2

```

Volatile

We add the **volatile** modifier to a variable that can change value outside the scope of the function. Usually the value of a global variable changes only as a result of explicit statements in the C function that is currently executing. The paradigm results when a single program executes from start to finish, and everything that happens is an explicit result of actions taken by the program. There are two situations that break this simple paradigm in which the value of a memory location might change outside the scope of a particular function currently executing:

- 1) interrupts and
- 2) input/output ports.

An interrupt is a hardware-requested software action. Consider the following multithreaded interrupt example. There is a foreground thread called **main()**, which we setup as the usual main program that all C programs have. Then, there is a background thread called **TOFhandler()**, which we setup to be executed on a periodic basis (e.g., every 16 ms). Both threads access the global variable, **Time**. The interrupt thread increments the global variable, and the foreground thread waits for time to reach 100. Notice that **Time** changes value outside the influence of the **main()** program.

```

volatile char Time;
#pragma interrupt_handler TOFhandler
void TOFhandler(void){    /* every 16ms */
    TFLG2=0x80;    /* TOF interrupt acknowledge */
    Time=Time+1;
}
void main(void){
    TSCR |=0x80; /* TEN(enable) */
    TMSK2=0xA2; /* TOI arm, TPU(pullup) timer/4 (500ns) */
    CLKCTL=0x00;
    Time=0;
    while(Time<100){}; /* wait for 100 counts of the 16 ms
timer*/
}

```

Listing 4-6: ICC12 example showing shared access to a common global variable

Without the **volatile** modifier the compiler might look at the two statements:

```
Time=0;
while(Time<100){};
```

and conclude that since the while loop does not modify **Time**, it could never reach 100. Some compilers (not yet in the current versions of ICC11 and ICC12) might attempt to move the read **Time** operation, performing it once before the while loop is executed. The **volatile** modifier disables the optimization, forcing the program to fetch a new value from the variable each time the variable is accessed.

In the next 6812 example, assume PORTA is an input port containing the current status of some important external signals. The program wishes to collect status versus time data of these external signals.

```
unsigned char data[100];
#define PORTA *(unsigned char volatile*)(0x0000)
#define DDRA *(unsigned char volatile*)(0x0004)
void main(void){ short i;
    DDRA=0x00; /* make Port A an input */
    for(i=0;i<100;i++){ /* collect 100 measurements */
        data[i]=PORTA; /* collect ith measurement */
    }
}
```

Listing 4-7: Example showed shared access to a common global variable

Without the **volatile** modifier in the PORTA definition, the compiler might optimize the for loop, reading PORTA once, then storing 100 identical copies into the data array. [I/O ports](#) will be handled in more detail in [Chapter 7](#) on pointers.

Automatics

Automatic variables, on the other hand, do not have fixed memory locations. They are dynamically allocated when the block in which they are defined is entered, and they are discarded upon leaving that block. Specifically, they are allocated on the 6811/6812 stack by subtracting a value (one for characters, two for integers and four for long integers) from the stack pointer register (SP). Since automatic objects exist only within blocks, they can only be declared locally. Automatic variables can only be referenced (read or write) by the function that created it. In this way, the information is protected or local to the function.

When a local variable is created it has no dependable initial value. It must be set to an initial value by means of an assignment operation. C provides for automatic variables to be initialized in their declarations, like globals. It does this by generating "hidden" code that assigns values automatically

after variables are allocated space.

It is tempting to forget that automatic variables go away when the block in which they are defined exits. This sometimes leads new C programmers to fall into the "dangling reference" trap in which a function returns a pointer to a local variable, as illustrated by

```
int *BadFunction(void) {
    int z;
    z=1000;
    return (&z);
}
```

Listing 4-8: Example showing an illegal reference to a local variable

When callers use the returned address of **z** they will find themselves messing around with the stack space that **z** used to occupy. This type of error is NOT flagged as a syntax error, but rather will cause unexpected behavior during execution.

Implementation of automatic variables

If locals are dynamically allocated at unspecified memory (stack) locations, then how does the program find them? This is done by using the pointer register (X) to designate a stack frame for the currently active function. There is a difference between the ICC11 and ICC12 compilers. The ICC11 compiler generates code that will define a new value of X (executing the **tsx** instruction) whenever it wishes to access a local variable. Consequently we see many **tsx** instructions throughout the function. On the other hand, the ICC12 compiler generates code that attempts to define the stack frame pointer x only once at the beginning of the function. Consequently we usually see only one **tfr s,x** instruction in each the function. The 6812 **tfr s,x** instruction is just an alternative specification of the instruction **tsx** (i.e., they produce the same machine code and perform the same function when executed). When the ICC12 function is entered, the prior value of Register X is pushed onto the stack and then the new value of SP is moved to X. The ICC11 function does not save the prior value of Register X. This address--the new value of SP--then becomes the base for references to local variables that are declared within the function. The 6812 has a much richer set of machine instructions and addressing modes to simplify this process. The 6811 SP register points to a free memory space to be used to place the next byte to be pushed. On the other hand the 6812 SP register points to the top data byte that has already been pushed.

In order to understand both the machine architecture and the C compiler, we can look at the assembly code generated. For both the ICC11 and ICC12 compilers, the linker/loader allocates 3 segmented memory areas: code pointed to by the PC (*text area*); global accessed with absolute addressing (*data area*); and locals pointed to by the stack pointer SP. This example shows a simple C program with three local variables. Although the function doesn't do much it will serve to illustrate how local variables are created (allocation), accessed (read and write) and destroyed (deallocated.)

```

void sub(void){ short y1,y2,y3;    /* 3 local variables*/
    y1=1000;
    y2=2000;
    y3=y1+y2;
}

```

Listing 4-9: Example showing three local variables

The first compiler we will study is the ImageCraft ICC11 version 4.0 for the Motorola 6811. The disassembled output has been edited to clarify its operation (although the compiler does create the "; y3 -> 0,x" comment). The **pshx** instruction allocates the local variable, and the **tsx** instruction establishes a stack frame pointer, X.

```

        .area text    ; _sub in ROM
        .globl _main
; y3 -> 0,x
; y2 -> 2,x
; y1 -> 4,x
_sub: pshx    ; allocate y1
      pshx    ; allocate y2
      pshx    ; allocate y3
      tsx
      ldd    #1000
      std    4,x    ; y1=1000
      ldd    #2000
      std    2,x    ; y2=2000
      ldd    4,x
      addd   2,x
      std    0,x    ; y3=y1+y2
      pulx   ; deallocate y3
      pulx   ; deallocate y2
      pulx   ; deallocate y1
      rts

```

The stack frame at the time of the **addd** instruction is shown. Within the subroutine the local variables of other functions are not accessible.

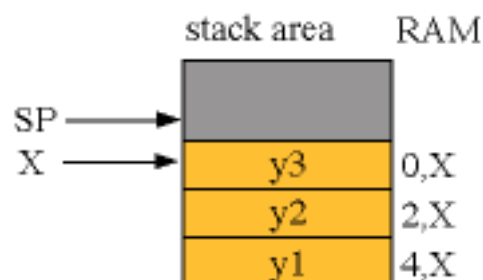


Figure 4-1. 6811 stack frame showing three local variables.

The next compiler we will study is the ImageCraft ICC12 version 5.0 for the Motorola 6812. Again, the disassembled output has been edited to clarify its operation (although the compiler does create the "; y3 -> -6,x" comment). Like the 6811, the linker/loader also allocates 3 segmented memory areas: code pointed to by the PC; global accessed with absolute addressing; and locals pointed to by the stack pointer SP. The `leas -6,sp` instruction allocates the local variables, and the `tfr s,x` instruction establishes a stack frame pointer, X. Within the subroutine the local variables of other functions are not accessible.

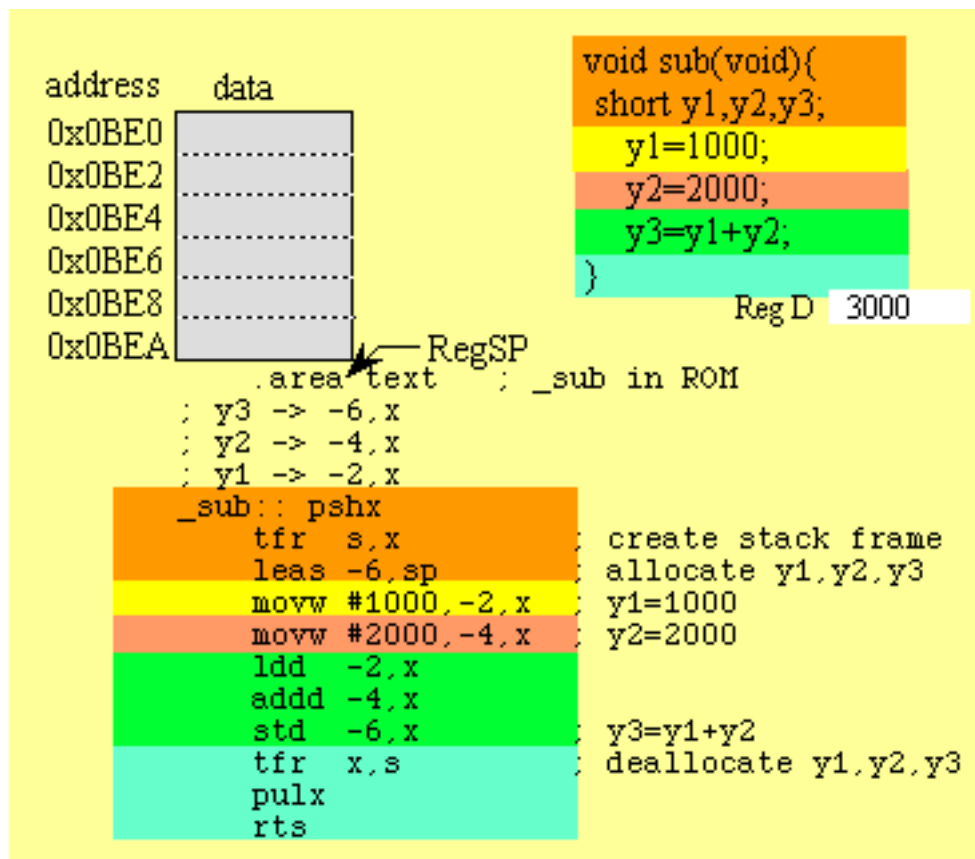


Figure 4-2. 6812 implementation of three local variables.

A **constant local** is similar to the regular local. Just as with the other locals, the constant is defined temporarily on the stack. The difference is that the constant local can not be changed. The assembly language code generated by the compiler that accesses the constant local is identical to the regular local.

```

short TheGlobal; /* a regular global variable*/
void main(void){
    const short TheConstant=1000; /* a constant local*/
    TheGlobal=TheConstant;
}
  
```

Listing 4-10: Example showing a constant local

The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
.area text    ; _main in ROM
.global _main
; TheConstant -> 0,x
_main: pshx   ; allocate TheConstant
      tsx
      ldd    #1000
      std    0,x          ; TheConstant=1000
      std    _TheGlobal  ; TheGlobal=TheConstant
      pulx           ; deallocate TheConstant
      rts
      .area bss
      .global _TheGlobal
_TheGlobal: .blkb 2
```

The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
.area text
_main::
      pshx
      tfr s,x
      leas -2,sp
      movw #1000,-2,x
      movw -2,x,_TheGlobal
      tfr x,s
      pulx
      rts
      .area bss
_TheGlobal:: .blkb 2
```

Externals

Objects that are defined outside of the present source module have the external storage class. This means that, although the compiler knows what they are (signed/unsigned, 8-bit 16-bit 32-bit etc.), it has no idea where they are. It simply refers to them by name without reserving space for them. Then when the linker brings together the object modules, it resolves these "pending" references by finding the external objects and inserting their addresses into the instructions that refer to them. The compiler knows an external variable by the keyword **extern** that must precede its declaration.

Only global declarations can be designated extern and only globals in other modules can be

referenced as external.

The following example sets an external global, called **ExtGlobal**, to the value 1000. This global can be referenced by any function from any file in the software system. It is truly global.

```
extern short ExtGlobal;    /* an external global variable*/
void main(void){
    ExtGlobal=1000;
}
```

Listing 4-11: Example showing an external global

Notice the assembly language the ICC11 generates does not include the definition of **ExtGlobal**. The 6811 code generated by the ICC11 (Version 4) compiler is as follows

```
.area text
.global _main
_main:
    ldd #1000
    std _ExtGlobal
    rts
```

Similarly the assembly language the ICC12 generates also does not include the definition of **ExtGlobal**. The 6812 code generated by the ICC12 (Version 5.1) compiler is as follows

```
.area text
_main::
    movw #1000,_ExtGlobal
    rts
```

Scope

The *scope* of a variable is the portion of the program from which it can be referenced. We might say that a variable's scope is the part of the program that "knows" or "sees" the variable. As we shall see, different rules determine the scopes of global and local objects.

When a variable is declared globally (outside of a function) its scope is the part of the source file that follows the declaration--any function following the declaration can refer to it. Functions that precede the declaration cannot refer to it. Most C compilers would issue an error message in that case.

The scope of local variables is the block in which they are declared. Local declarations must be grouped together before the first executable statement in the block--at the head of the block. This is different from C++ that allows local variables to be declared anywhere in the function. It follows that

the scope of a local variable effectively includes all of the block in which it is declared. Since blocks can be nested, it also follows that local variables are seen in all blocks that are contained in the one that declares the variables.

If we declare a local variable with the same name as a global object or another local in a superior block, the new variable temporarily supersedes the higher level declarations. Consider the following program.

```
unsigned char x;    /* a regular global variable*/
void sub(void){
    x=1;
    { unsigned char x;    /* a local variable*/
      x=2;
      { unsigned char x; /* a local variable*/
        x=3;
        PORTA=x; }
      PORTA=x; }
    PORTA=x; }
}
```

Listing 4-12: An example showing the scope of local variables

This program declares variables with the name **x**, assigns values to them, and outputs them to **PORTA** with in such a way that, when we consider its output, the scope of its declarations becomes clear. When this program runs, it outputs 321. This only makes sense if the **x** declared in the inner most block masks the higher level declarations so that it receives the value '3' without destroying the higher level variables. Likewise the second **x** is assigned '2' which it retains throughout the execution of the inner most block. Finally, the global **x**, which is assigned '1', is not affected by the execution of the two inner blocks. Notice, too, that the placement of the last two **PORTA=x;** statements demonstrates that leaving a block effectively un.masks objects that were hidden by declarations in the block. The second **PORTA=x;** sees the middle **x** and the last **PORTA=x;** sees the global **x**.

This masking of higher level declarations is an advantage, since it allows the programmer to declare local variables for temporary use without regard for other uses of the same names.

One of the mistakes a C++ programmer makes when writing C code is trying to define local variables in the middle of a block. In C local variables must be defined at the beginning of a block. The following example is proper C++ code, but results in a syntax error in C.

```
void sub(void){ int x; /* a valid local variable declaration
*/
    x=1;
    int y; /* This declaration is improper */
    y=2;
}
```

Listing 4-13: Example showing an illegal local variable declaration

Declarations

Unlike BASIC and FORTRAN, which will automatically declare variables when they are first used, every variable in C must be declared first. This may seem unnecessary, but when we consider how much time is spent debugging BASIC and FORTRAN programs simply because misspelled variable names are not caught for us, it becomes obvious that the time spent declaring variables beforehand is time well spent. Declarations also force us to consider the precision (8-bit, 16-bit etc.) and format (unsigned vs. signed) of each variable.

As we saw in [Chapter 1](#), describing a variable involves two actions. The first action is declaring its type and the second action is defining it in memory (reserving a place for it). Although both of these may be involved, we refer to the C construct that accomplishes them as a *declaration*. As we saw above, if the declaration is preceded by **extern** it only declares the type of the variables, without reserving space for them. In such cases, the definition must exist in another source file. Failure to do so, will result in an unresolved reference error at link time.

Table 4-1 contains examples of legitimate variable declarations. Notice that the declarations are introduced by one or type keywords that states the data type of the variables listed. The keyword **char** declares 8-bit values, **int** declares 16-bit values, **short** declares 16-bit values and **long** declares 32-bit values. Unless the modifier **unsigned** is present, the variables declared by these statements are assumed by the compiler to contain signed values. You could add the keyword **signed** before the data type to clarify its type.

When more than one variable is being declared, they are written as a list with the individual names separated by commas. Each declaration is terminated with a semicolon as are all simple C statements.

Declaration	Comment	Range
unsigned char uc;	8-bit unsigned number	0 to +255
char c1,c2,c3;	three 8-bit signed numbers	-128 to +127
unsigned int ui;	16-bit unsigned number	0 to +65535
int i1,i2;	two 16-bit signed numbers	-32768 to +32767
unsigned short us;	16-bit unsigned number	0 to +65535
short s1,s2;	two 16-bit signed numbers	-32768 to +32767
long l1,l2,l3,l4;	four signed 32 bit integers	-2147483648L to 2147483647L
float f1,f2;	two 32-bit floating numbers	not recommended
double d1,d2;	two 64-bit floating numbers	not recommended

Table 4-1: Variable Declarations

ICC11 version 4 does not support long integers, and ICC12 does not support unsigned long integers. ICC11 and ICC12 compilers allow the **register** modifier for automatic variables, but the compilers still define the register locals on the stack. The keywords **char int short long** specifies the precision of the variable. The following tables shows the available modifiers for variables.

Modifier	Comment
<code>auto</code>	automatic, allocated on the stack
<code>extern</code>	defined in some other program file
<code>static</code>	permanently allocated
<code>register</code>	attempt to implement an automatic using a register instead of on the stack

Table 4-2: Variable storage classes

Modifier	Comment
<code>volatile</code>	can change value by means other than the current program
<code>const</code>	fixed value, defined in the source code and can not be changed during execution
<code>unsigned</code>	range starts with 0 includes only positive values
<code>signed</code>	range includes both negative and positive values

Table 4-3 Variable modifiers

As we shall see, a similar syntax is used to declare pointers, arrays, and functions ([Chapters 7, 8, and 10](#)).

Character Variables

Character variables are stored as 8-bit quantities. When they are fetched from memory, they are always promoted automatically to 16-bit integers. Unsigned 8-bit values are promoted by adding 8 zeros into the most significant bits. Signed values are promoted by coping the sign bit (bit7) into the 8 most significant bits.

There is a confusion when signed and unsigned variables are mixed into the same expression. It is good programming practice to avoid such confusions. As with integers, when a signed character enters into an operation with an unsigned quantity, the character is interpreted as though it was unsigned. The result of such operations is also unsigned. When a signed character joins with another signed quantity, the result is also signed.

```
char x;    /* signed 8 bit global */
unsigned short y; /* unsigned signed 16 bit global */
```

```

void sub(void){
    y=y+x;
    /* x treated as unsigned even though defined as signed */
}

```

Listing 4-13: An example showing the mixture of signed and unsigned variables

There is also a need to change the size of characters when they are stored, since they are represented in the CPU as 16-bit values. In this case, however, it matters not whether they are signed or unsigned. Obviously there is only one reasonable way to put a 16-bit quantity into an 8-bit location. When the high-order byte is chopped off, an error might occur. It is the programmer's responsibility to ensure that significant bits are not lost when characters are stored.

When Do We Use Automatics Versus Statics?

Because their contents are allowed to change, all variables must be allocated in RAM and not ROM. An **automatic variable** contains temporary information used only by one software module. As we saw, automatic variables are typically allocated, used, then deallocated from the stack. Since an interrupt will save registers and create its own stack frame, the use of automatic variables is important for creating reentrant software. Automatic variables provide protection limiting the scope of access in such a way that only the program that created the local variable can access it. The information stored in an automatic variable is not permanent. This means if we store a value into an automatic variable during one execution of the module, the next time that module is executed the previous value is not available. Typically we use automatics for loop counters, temporary sums. We use an automatic variable to store data that is temporary in nature. In summary, reasons why we place automatic variables on the stack include

- dynamic allocation release allows for reuse of memory
- limited scope of access provides for data protection
- can be made reentrant.
- limited scope of access provides for data protection
- since absolute addressing is not used, the code is relocatable
- the number of variables is only limited by the size of the stack allocation.

A **static variable** is information shared by more than one program module. E.g., we use globals to pass data between the main (or foreground) process and an interrupt (or background) process. Static variables are not deallocated. The information they store is permanent. We can use static variables for the time of day, date, user name, temperature, pointers to shared data. The ICC11/ICC12 compilers use absolute addressing (direct or extended) to access the static variables.

Initialization of variables and constants

Most programming languages provide ways of specifying *initial values*; that is, the values that variables have when program execution begins. We saw earlier that the ICC11/ICC12/Hiware compilers will initially set all static variables to zero. Constants must be initialized at the time they are

declared, and we have the option of initializing the variables.

Specifying initial values is simple. In its declaration, we follow a variable's name with an equal sign and a constant expression for the desired value. Thus

```
short Temperature = -55;
```

declares `Temperature` to be a 16-bit signed integer, and gives it an initial value of -55. Character constants with backslash-escape sequences are permitted. Thus

```
char Letter = '\t';
```

declares `Letter` to be a character, and gives it the value of the tab character. If array elements are being initialized, a list of constant expressions, separated by commas and enclosed in braces, is written. For example,

```
const unsigned short Steps[4] = {10, 9, 6, 5};
```

declares `Steps` to be an unsigned 16-bit constant integer array, and gives its elements the values 10, 9, 6, and 5 respectively. If the size of the array is not specified, it is determined by the number of initializers. Thus

```
char Waveform[] = {28, 27, 60, 30, 40, 50, 60};
```

declares `Waveform` to be a signed 8-bit array of 7 elements which are initialized to the 28, 27, 60, 30, 40, 50, 60. On the other hand, if the size of the array is given and if it exceeds the number of initializers, the leading elements are initialized and the trailing elements default to zero. Therefore,

```
char Waveform[100] = {28, 27, 60, 30, 40, 50, 60};
```

declares `Waveform` to be an integer array of 100 elements, the first 7 elements of which are initialized to the 28, 27, 60, 30, 40, 50, 60 and the others to zero. Finally, if the size of an array is given and there are too many initializers, the compiler generates an error message. In that case, the programmer must be confused.

Character arrays and character pointers may be initialized with a character string. In these cases, a terminating zero is automatically generated. For example,

```
char Name[4] = "Jon";
```

declares `Name` to be a character array of four elements with the first three initialized to 'J', 'o', and 'n' respectively. The fourth element contains zero. If the size of the array is not given, it will be set to the size of the string plus one. Thus ca in

```
char Name[] = "Jon";
```

also contains the same four elements. If the size is given and the string is shorter, trailing elements default to zero. For example, the array declared by

```
char Name[6] = "Jon";
```

contains zeroes in its last three elements. If the string is longer than the specified size of the array, the array size is increased to match. If we write

```
char *NamePt = "Jon";
```

the effect is quite different from initializing an array. First a word (16 bits) is set aside for the pointer itself. This pointer is then given the address of the string. Then, beginning with that byte, the string and its zero terminator are assembled. The result is that `NamePt` contains the address of the string "Jon". The Imagecraft and Hiware compilers accept initializers for character variables, pointers, and arrays, and for integer variables and arrays. The initializers themselves may be either constant expressions, lists of constant expressions, or strings.

Implementation of the initialization

The compiler initializes static constants simply by defining its value in ROM. In the following example, `J` is a static constant (actually `K` is a literal)

```
short I;           /* 16 bit global */
const short J=96;  /* 16 bit constant */
#define K 97;
void main(void){
    I=J;
    I=K; }
```

Listing 4-14: An example showing the initialization of a static constant

The 6811 code generated by the ICC11 Version 4 compiler is as follows

```
.area text
.global _main
_main:
    ldd _J
    std _I    ;16 bits
    ldd #97
    std _I    ;16 bits
```

```

    rts
    .area bss
    .global _I
_I: .blkb 2
    .area text
    .global _J
_J: .word 96

```

The 6812 code generated by the ICC12 Version 5.1 compiler is as follows

```

    .area text
_main::
    pshx
    tfr s,x
    movw _J,_I    ;16 bits
    movw #97,_I  ;16 bits
    tfr x,s
    pulx
    rts
    .area bss
_I:: .blkb 2
    .area text
_J:: .word 96

```

Notice the use of the **#define** macro implements an operation similar to the literal I=97;

The compiler initializes static variables simply by defining its initial value in ROM. It creates another segment called *idata* (in addition to the *data* and *text* sections). It places the initial values in the *idata* segment, then copies the data dynamically from *idata* ROM information into *data* RAM variables at the start of the program (before main is started). (How ICC11 and ICC12 have handled initialized variables has changed over the various release versions. The particular compiler version you are using may handle this process differently.) For example

```

short I=95;          /* 16 bit global */
void main(void){

}

```

The ICC11 Version 4 code would not function properly on an embedded system because the global is defined as

```

    .area bss
_I:: .word 95

```

The ICC11 Version 4 code would work on a RAM based system like the Motorola 6811 EVB where

the power is not removed between the time the S19 record is loaded into RAM and the time the software is executed. Proper 6812 code is generated by the ICC12 compiler. In the 6812 solution, code in the **CRT12.S** file will copy the 95 from **.idata** (ROM) into **_I** in **bss** (RAM) upon a hardware reset. This copy is performed transparently before the main program is started.

```
        .area text
_main::
    pshx
    tfr s,x

    pulx
    rts
    .area bss
_I:: .blkb 2
    .area idata
    .word 95
```

Even though the following two applications of global variable are technically proper, the explicit initialization of global variables in my opinion is a better style.

```
/* poor style */      /* good style */
int I=95;              int I;
void main(void){      void main(void){
                      I=95;
}                      }
```

Opinion: I firmly believe a good understanding of the assembly code generated by our compiler makes us better programmers.

Go to [Chapter 5 on Expressions](#) Return to [Table of Contents](#)

Chapter 7: Pointers

What's in Chapter 7?

[Definitions of address and pointer](#)

[Declarations of pointers define the type and allocate space in memory](#)

[How do we use pointers](#)

[Memory architecture of the 6811 and 6812](#)

[Pointer math](#)

[Pointer comparisons](#)

[FIFO queue implemented with pointers](#)

[I/O port access](#)

The ability to work with memory addresses is an important feature of the C language. This feature allows programmers the freedom to perform operations similar to assembly language. Unfortunately, along with the power comes the potential danger of hard-to-find and serious run-time errors. In many situations, array elements can be reached more efficiently through pointers than by subscripting. It also allows pointers and pointer chains to be used in data structures. Without pointers the run-time dynamic memory allocation and deallocation using the heap would not be possible. We will also use a format similar to pointers to develop mechanisms for [accessing I/O ports](#). These added degrees of flexibility are absolutely essential for embedded systems.

Addresses and Pointers

Addresses that can be stored and changed are called *pointers*. A pointer is really just a variable that contains an address. Although, they can be used to reach objects in memory, their greatest advantage lies in their ability to enter into arithmetic (and other) operations, and to be changed. Just like other variables, pointers have a type. In other words, the compiler knows the format (8-bit 16-bit 32-bit, unsigned signed) of the data pointed to by the address.

Not every address is a pointer. For instance, we can write **&var** when we want the address of the variable **var**. The result will be an address that is not a pointer since it does not have a name or a place in memory. It cannot, therefore, have its value altered.

Other examples include an array or a structure name. As we shall see in [Chapter 8](#), an unsubscripted array name yields the address of the array. In [Chapter 9](#), a structure name yields the address of the structure. But, since arrays and structures cannot be moved around in memory, their addresses are not variable. So, although, such addresses have a name, they do not exist as objects in memory (the array does, but its address does not) and cannot, therefore, be changed.

A third example is a character string. [Chapter 3](#) indicated that a character string yields the address of the character array specified by the string. In this case the address has neither a name or a place in memory, so it too is not a pointer.

Pointer Declarations

The syntax for declaring pointers is like that for variables ([Chapter 4](#)) except that pointers are distinguished by an asterisk that prefixes their names. Listing 7-1 illustrates several legitimate pointer declarations. Notice, in the third example, that we may mix pointers and variables in a single declaration. I.e., the variable `data` and the pointer `pt3` are declared in the same statement. Also notice that the data type of a pointer declaration specifies the type of object to which the pointer refers, not the type of the pointer itself. As we shall see, all ICC11 and ICC12 pointers contain 16-bit unsigned absolute addresses. This means that the ICC12 compiler does not provide for direct support of the extended memory available on the MC68HC812A4 microcomputer. There is an example from Valvano's [Embedded Microcomputer Systems: Real Time Interfacing](#) shown in [Chapter 9](#), which, while illustrating structures, implements an extended address mechanism for the MC68HC812A4.

```
short *pt1;      /* define pt1, declare as a pointer to a 16-bit
integer */
char *pt2;      /* define pt2, declare as a pointer to an 8-bit
character */
unsigned short data,*pt3;      /* define data and pt3,
declare data as an unsigned 16-bit integer and
declare pt3 as a pointer to a 16-bit unsigned integer */
long *pt4;      /* define pt4, declare as a pointer to a 32-bit
integer */
extern short *pt5;      /* declare pt5 as a pointer to an integer
*/
```

Listing 7-1: Example showing a pointer declarations

The best way to think of the asterisk is to imagine that it stands for the phrase "object at" or "object pointed to by." The first declaration in Listing 7-1 then reads "the object at (pointed to by) **pt1** is a 16-bit signed integer."

Pointer Referencing

We can use the pointer to retrieve data from memory or to store data into memory. Both operations are classified as *pointer references*. The syntax for using pointers is like that for variables except that pointers are distinguished by an asterisk that prefixes their names. Figures 7-1 through 7-4 illustrate several legitimate pointer references. In the first figure, the global variables contain unknown data (actually we know ICC11/ICC12/Hiware will zero global variables). The arrow identifies the execution location. Assume addresses 0x0810 through 0x081A exist in RAM.

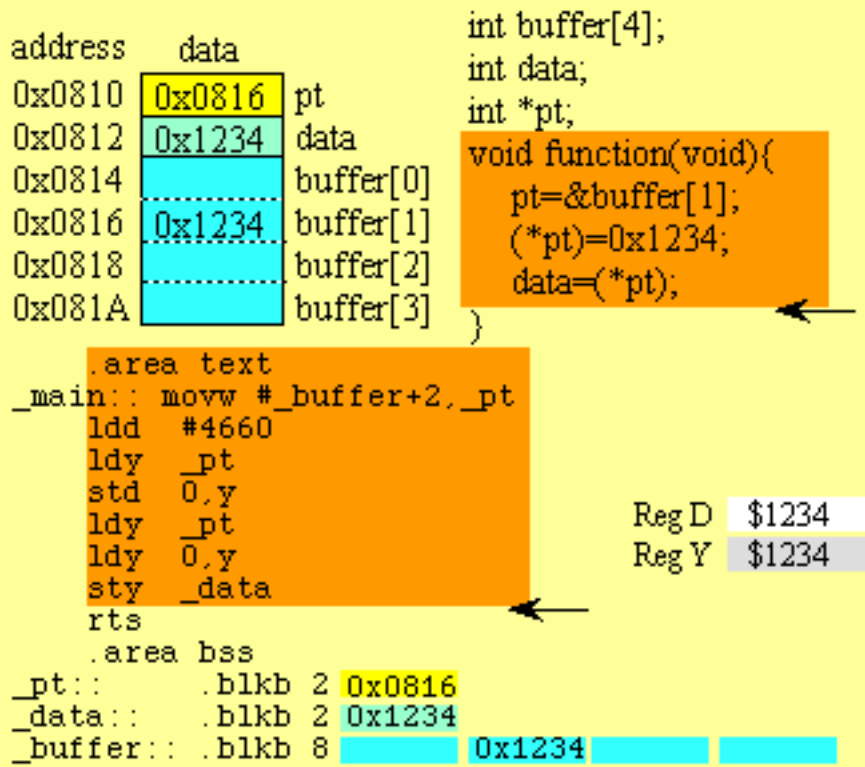


Figure 7-1: Pointer Referencing

The expression **&buffer[1]** returns the address of the second 16 bit element of the **buffer** (0x0816). Therefore the line **pt=&buffer[1];** makes **pt** point to **buffer[1]**.

When the ***pt** occurs on the left-hand-side of an assignment statement data is stored into memory at the address. Recall the ***pt** means "the 16-bit signed integer at 0x0816". I like to add the parentheses () to clarify that ***** and **pt** are one object. In this case the parentheses are not needed. Later when we perform address arithmetic, the parentheses will be important. Therefore the line **(*pt)=0x1234;** sets **buffer[1]** to 0x1234.

When the ***pt** occurs on the right-hand-side of an assignment statement data is retrieved from memory at the address. Again, I like to add the parentheses () to clarify that ***** and **pt** are one object. Therefore the line **data=(*pt);** sets **data** to 0x1234 (more precisely, it copies the 16-bit information from **buffer[1]** into **data**.)

We can get a better understanding of pointers by observing the assembly generated by our compiler. The following 6811 assembly was generated by ICC11 Version 4 when the above pointer example (figure 7-1) was compiled.

```

.area text
.globl _main
_main: ldd #_buffer+2 ; pt=&buffer[1];
      std _pt
      ldy _pt ; (*pt)=0x1234;

```

```

    ldd #4660
    std 0,y
    ldy _pt    ; data=(*pt);
    ldd 0,y
    std _data
    rts
    .area bss
    .globl _pt
_pt:    .blkb 2
    .globl _data
_data:  .blkb 2
    .globl _buffer
_buffer: .blkb 8

```

The following 6812 assembly was generated by ICC12 Version 5.1 when the above pointer example (figure 7-1) was compiled.

```

    .area text
_main:: movw #_buffer+2,_pt ; pt=&buffer[1];
    ldd #4660    ; (*pt)=0x1234;
    ldy _pt
    std 0,y
    ldy _pt    ; data=(*pt);
    ldy 0,y
    sty _data
    rts
    .area bss
_pt::    .blkb 2
_data::  .blkb 2
_buffer:: .blkb 8

```

Memory Addressing

The size of a pointer depends on the architecture of the CPU and the implementation of the C compiler. Both the 6811 and 6812 employ an absolute memory addressing scheme in which an effective address is composed simply of a single 16-bit unsigned value. In particular the 6811 and 6812 registers are shown in Figure 7-5. The MC68HC812A4 does provide for extended addressing. For more information on this feature see Chapter 9 of Valvano's Embedded Microcomputer Systems: Real Time Interfacing.

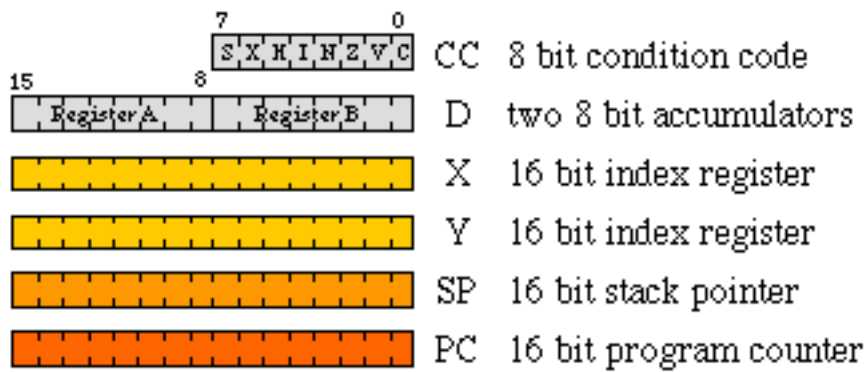


Figure 7-2: The 6811 and 6812 have 16 bit address registers, X Y, SP PC.

Most embedded systems employ a segmented memory architecture. From a physical standpoint we might have a mixture of regular RAM, battery-backed-up RAM, regular EEPROM, flash EPROM, regular PROM, one-time-programmable PROM and ROM. RAM is the only memory structure that allows the program both read and write access. The other types are usually loaded with object code from our S19 file and our program is allowed only to read the data. Table 7-1 shows the various types of memory available in the 6811 and 6812 microcomputer. The RAM contains temporary information that is lost when the power is shut off. This means that all variables allocated in RAM must be explicitly initialized at run time by the software. If the embedded system includes a separate battery for the RAM, then information is not lost when the main power is removed. Some Motorola microcomputers have EEPROM. The number of erase/program cycles depends on the memory technology. EEPROM is often used as the main program memory during product development. In the final product we can use EEPROM for configuration constants and even nonvolatile data logging. For more information on how to write C code that dynamically writes EEPROM see Chapter 1 of Valvano's Embedded Microcomputer Systems: Real Time Interfacing. The one-time-programmable PROM is a simple nonvolatile storage used in small volume products that can be programmed only once with inexpensive equipment. The ROM is a low-cost nonvolatile storage used in large volume products that can be programmed only once at the factory.

Memory	When power is removed	Ability to Read/Write	Program cycles
RAM	volatile	random and fast access	infinite
battery-backed RAM	nonvolatile	random and fast access	infinite
EEPROM	nonvolatile	easily reprogrammed	10,000 times
Flash	nonvolatile	easily reprogrammed	100 times
OTP PROM	nonvolatile	can be easily programmed	once
ROM	nonvolatile	programmed at the factory	once

Table 7-1: Various types of memory available for the 6811 and 6812.

From a logical standpoint we define implement segmentation when we group together in memory information that has similar properties or usage. Typical software segments include global variables (**data section**), the heap, local variables, fixed constants (**idata section**), and machine instructions (**text section**). Global variables are permanently allocated and usually accessible by more than one program. We must use global variables for information that must be permanently available, or for information that is to be shared by more than one module. We will see the first-in-first-out (FIFO) queue is a global data structure that is shared by more than one module. Imagecraft and Hiware both allow the use of a heap to dynamically allocate and release memory. This information can be shared or not shared depending on which modules have pointers to the data. The heap is efficient in situations where storage is needed for only a limited amount of time. Local variables are usually allocated on the stack at the beginning of the function, used within the function, and deallocated at the end of the function. Local variables are not shared with other modules. Fixed constants do not change and include information such as numbers, strings, sounds and pictures. Just like the heap the fixed constants can be shared or not shared depending on which modules have pointers to the data.

In an embedded application, we usually put global variables, the heap, and local variables in RAM because these types of information can change during execution. When software is to be executed on a regular computer, the machine instructions are usually read from a mass storage device (like a disk) and loaded into memory. Because the embedded system usually has no mass storage device, the machine instructions and fixed constants must be stored in nonvolatile memory. If there is both EEPROM and ROM on our microcomputer, we put some fixed constants in EEPROM and some in ROM. If it is information that we may wish to change in the future, we could put it in EEPROM. Examples include language-specific strings, calibration constants, finite state machines, and system ID numbers. This allows us to make minor modifications to the system by reprogramming the EEPROM without throwing the chip away. If our project involves producing a small number of devices then the program can be placed in EPROM or EEPROM. For a project with a large volume it will be cost effective to place the machine instructions in ROM.

Pointer Arithmetic

A major difference between addresses and ordinary variables or constants has to do with the interpretation of addresses. Since an address points to an object of some particular type, adding one (for instance) to an address should direct it to the next object, not necessarily the next byte. If the address points to integers, then it should end up pointing to the next integer. But, since integers occupy two bytes, adding one to an integer address must actually increase the address by two. Likewise, if the address points to long integers, then adding one to an address should end up pointing to the next long integer by increasing the address by four. A similar consideration applies to subtraction. In other words, values added to or subtracted from an address must be scaled according to the size of the objects being addressed. This automatic correction saves the programmer a lot of thought and makes programs less complex since the scaling need not be coded explicitly. The scaling factor for long integers is four; the scaling factor for integers is two; the scaling factor for characters is

one. Therefore, character addresses do not receive special handling. It should be obvious that when define structures (see [Chapter 9](#)) of other sizes, the appropriate factors would have to be used.

A related consideration arises when we imagine the meaning of the difference of two addresses. Such a result is interpreted as the number of objects between the two addresses. If the objects are integers, the result must be divided by two in order to yield a value which is consistent with this meaning. See [Chapter 8](#) for more on address arithmetic.

When an address is operated on, the result is always another address of the same type. Thus, if **ptr** is a signed 16-bit integer pointer, then **ptr+1** is also points to a signed 16-bit integer.

Precedence determines the order of evaluation. [See a table of precedence](#). One of the most common mistakes results when the programmer neglects the fact the ***** used as a unary pointer reference has precedence over all binary operators. This means the expression ***ptr+1** is the same as **(*ptr)+1** and not ***(ptr+1)**. This is an important point so I'll mention it again, *"When confused about precedence (and aren't we all) add parentheses to clarify the expression."*

Pointer Comparisons

One major difference between pointers and other variables is that pointers are always considered to be unsigned. This should be obvious since memory addresses are not signed. This property of pointers (actually all addresses) ensures that only unsigned operations will be performed on them. It further means that the other operand in a binary operation will also be regarded as unsigned (whether or not it actually is). In the following example, **pt1** and **pt2[5]** return the current values of the addresses. For instance, if the array **pt2[]** contains addresses, then it would make sense to write

```
short *pt1;          /* define 16-bit integer pointer */
short *pt2[10];     /* define ten 16-bit integer pointers */
short done(void){   /* returns true if pt1 is higher than pt2[5]
*/
    if(pt1>pt2[5]) return(1);
    return(0);
}
```

Listing 7-2: Example showing a pointer comparisons

which performs an unsigned comparison since **pt1** and **pt2** are pointers. Thus, if **pt2[5]** contains **0xF000** and **pt1** contains **0x1000**, the expression will yield true, since **0xF000** is a higher unsigned

value than 0x1000.

It makes no sense to compare a pointer to anything but another address or zero. C guarantees that valid addresses can never be zero, so that particular value is useful in representing the absence of an address in a pointer.

Furthermore, to avoid portability problems, only addresses within a single array should be compared for relative value (e.g., which pointer is larger). To do otherwise would necessarily involve assumptions about how the compiler organizes memory. Comparisons for equality, however, need not observe this restriction, since they make no assumption about the relative positions of objects. For example if **pt1** points into one data array and **pt2** points into a different array, then comparing **pt1** to **pt2** would be meaningless. Which pointer is larger would depend on where in memory the two arrays were assigned.

A FIFO Queue Example

To illustrate the use of pointers we will design a two-pointer FIFO. The first in first out circular queue (FIFO) is also useful for data flow problems. It is a very common data structure used for I/O interfacing. The order preserving data structure temporarily saves data created by the source (producer) before it is processed by the sink (consumer). The class of FIFO's studied in this section will be statically allocated global structures. Because they are global variables, it means they will exist permanently and can be shared by more than one program. The advantage of using a FIFO structure for a data flow problem is that we can decouple the source and sink processes. Without the FIFO we would have to produce 1 piece of data, then process it, produce another piece of data, then process it. With the FIFO, the source process can continue to produce data without having to wait for the sink to finish processing the previous data. This decoupling can significantly improve system performance.

GETPT points to the data that will be removed by the next call to GET, and PUTPT points to the empty space where the data will be stored by the next call to PUT. If the FIFO is full when PUT is called then the subroutine should return a full error (e.g., V=1.) Similarly, if the FIFO is empty when GET is called, then the subroutine should return an empty error (e.g., V=1.) The PUTPT and GETPT must be wrapped back up to the top when they reach the bottom.

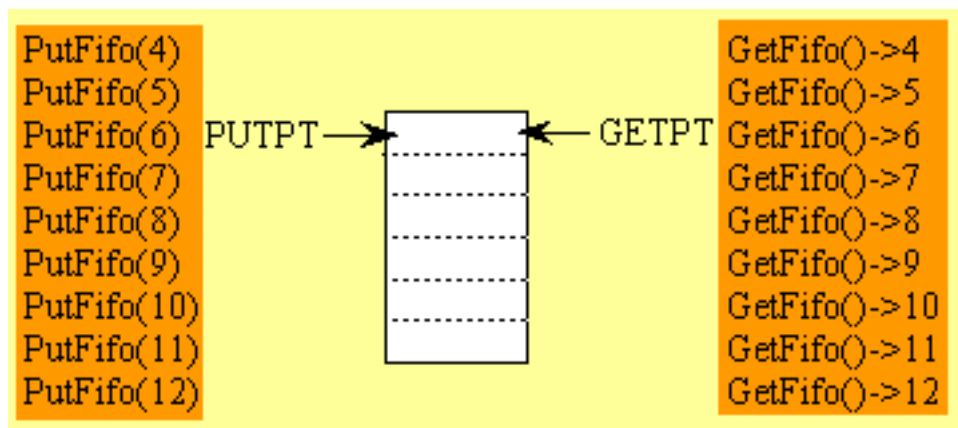


Figure 7-3: Fifo example showing the PUTPT and GETPT wrap.

There are two mechanisms to determine whether the FIFO is empty or full. A simple method is to implement a counter containing the number of bytes currently stored in the FIFO. GET would decrement the counter and PUT would increment the counter. The second method is to prevent the FIFO from being completely full. For example, if the FIFO had 100 bytes allocated, then the PUT subroutine would allow a maximum of 99 bytes to be stored. If there were already 99 bytes in the FIFO and another PUT were called, then the FIFO would not be modified and a full error would be returned. In this way if PUTPT equals GETPT at the beginning of GET, then the FIFO is empty. Similarly, if PUTPT+1 equals GETPT at the beginning of PUT, then the FIFO is full. Be careful to wrap the PUTPT+1 before comparing it to GETPT. This second method does not require the length to be stored or calculated.

```
/* Pointer implementation of the FIFO */
#define FifoSize 10 /* Number of 8 bit data in the Fifo */
#define START_CRITICAL() asm(" tpa\n staa %SaveSP\n sei")
#define END_CRITICAL() asm( ldaa %SaveSP\n tap")
char *PUTPT; /* Pointer of where to put next */
char *GETPT; /* Pointer of where to get next */
/* FIFO is empty if PUTPT=GETPT */
/* FIFO is full if PUTPT+1=GETPT */
char Fifo[FifoSize]; /* The statically allocated fifo data */
void InitFifo(void) {unsigned char SaveSP;
    START_CRITICAL(); /* make atomic, entering critical section
*/
    PUTPT=GETPT=&Fifo[0]; /* Empty when PUTPT=GETPT */
    END_CRITICAL(); /* end critical section */
}
int PutFifo (char data) { char *Ppt; /* Temporary put pointer
*/
unsigned char SaveSP;
    START_CRITICAL(); /* make atomic, entering critical section
*/
    Ppt=PUTPT; /* Copy of put pointer */
    *(Ppt++)=data; /* Try to put data into fifo */
    if (Ppt == &Fifo[FifoSize]) Ppt = &Fifo[0]; /* Wrap */
    if (Ppt == GETPT ){
        END_CRITICAL(); /* end critical section */
        return(0);} /* Failed, fifo was full */
    else{
        PUTPT=Ppt;
        END_CRITICAL(); /* end critical section */
        return(-1); /* Successful */
    }
}
```

```

    }
}
int GetFifo (char *datap) {unsigned char SaveSP;
    if (PUTPT== GETPT){
        return(0);} /* Empty if PUTPT=GETPT */
    else{
        START_CRITICAL(); /* make atomic, entering critical
section */
        *datap=*(GETPT++);
        if (GETPT == &Fifo[FifoSize])
            GETPT = &Fifo[0];
        END_CRITICAL(); /* end critical section */
        return(-1);
    }
}
}

```

Listing 7-3: Fifo queue implemented with pointers

The `START_CRITICAL` and `END_CRITICAL` macros are specific to ICC11/ICC12, otherwise this example will operate using Hiware.

Since these routines have read modify write accesses to global variables the three functions (InitFifo, PutFifo, GetFifo) are themselves not reentrant. Consequently interrupts are temporarily disabled, to prevent one thread from reentering these Fifo functions. One advantage of this pointer implementation is that if you have a single thread that calls the GetFifo (e.g., the main program) and a single thread that calls the PutFifo (e.g., the serial port receive interrupt handler), then this PutFifo function can interrupt this GetFifo function without loss of data. So in this particular situation, interrupts would not have to be disabled. It would also operate properly if there were a single interrupt thread calling GetFifo (e.g., the serial port transmit interrupt handler) and a single thread calling PutFifo (e.g., the main program.) On the other hand, if the situation is more general, and multiple threads could call PutFifo or multiple threads could call GetFifo, then the interrupts would have to be temporarily disabled as shown.

I/O Port Access

Even though the mechanism to access I/O ports technically does not fit the definition of pointer, it is included in this chapter because it involves addresses. The format used by both the Imagecraft and Hiware compilers fits the following model. The following listing shows one 8-bit and two 16-bit 6811 I/O ports. The line `TFLG1=0x08;` generates an 8-bit I/O write operation to the port at address 0x1023. The `TCNT` on the right hand side of the assignment statement generates a 16-bit I/O read operation from the port at address 0x100E. The `TOC5` on the left hand side of the assignment statement generates a 16-bit I/O write operation from the port at address 0x101E. The `TFLG1` inside the while loop generates repeated 8-bit I/O read operations until bit 3 is set.

```
#define TFLG1 *(unsigned char volatile*)(0x1023)
```

```

#define TCNT *(unsigned short volatile *)(0x100E)
#define TOC5 *(unsigned short volatile *)(0x101E)
void wait(unsigned int delay){
    TFLG1=0x08; /* clear OC5F */
    TOC5=TCNT+delay; /* TCNT at end of wait */
    while((TFLG1&0x08)==0){}; /* wait for OC5F*/
}

```

Listing 7-4: Sample ICC11/Hiware Program that accesses I/O ports

A similar 6812 program is shown below.

```

#define TFLG1 *(unsigned char volatile *)(0x008E)
#define TCNT *(unsigned short volatile *)(0x0084)
#define TC5 *(unsigned short volatile *)(0x009A)
void wait(unsigned int delay){
    TFLG1=0x20; /* clear C5F */
    TC5=TCNT+delay; /* TCNT at end of wait */
    while((TFLG1&0x20)==0){}; /* wait for C5F*/
}

```

Listing 7-5: Sample ICC12/Hiware Program that accesses I/O ports

It was mentioned earlier that the volatile modifier will prevent the compiler from optimizing I/O programs. I.e., these examples would not work if the compiler read TFLG1 once, then used the same data over and over inside the while loop.

To understand this syntax we break it into parts. Starting on the right is the absolute address of the I/O port. For example the 6811 TFLG1 register is at location 0x1023. The parentheses are necessary because the definition might be used in an arithmetic calculation. For example the following two lines are quite different:

```

TheTime=*(unsigned char volatile *)(0x1023)+100;
TheTime=*(unsigned char volatile *)0x1023+100;

```

In the second (incorrect) case the addition 0x01023+100 is performed on the address, not the data. The next part of the definition is a type casting. C allows you to change the type of an expression. For example (unsigned char volatile *) specifies that 0x1023 is an address that points at an 8-bit unsigned char. The * at the beginning of the definition causes the data to be fetched from the I/O port if the expression exists on the right-hand side of an assignment statement. The * also causes the data to be stored at the I/O port if the expression is on the left-hand side of the assignment statement. In this last way, I/O port accesses are indeed similar to pointers. For example the above example could have been implemented as:

```

unsigned char volatile *pTFLG1;

```

```
unsigned short volatile *pTCNT;
unsigned short volatile *pTC5;
void wait(unsigned int delay){
    pTFLG1=(unsigned char volatile *)(0x008E);
    pTCNT=(unsigned short volatile *)(0x0084);
    pTC5=(unsigned short volatile *)(0x009A);
    (*pTFLG1)=0x20;
    (*pTC5)=(*pTCNT)+delay;
    while((( *pTFLG1)&0x20)==0){};
}
```

Listing 7-6: ICC12/Hiware Program that accesses I/O ports using pointers

This function first sets the three I/O pointers then accesses the I/O ports indirectly through the pointers.

Go to [Chapter 8 on Arrays and Strings](#) Return to [Table of Contents](#)

Chapter 8: Arrays and Strings

What's in Chapter 8?

[Array Subscripts](#)

[Array Declarations](#)

[Array References](#)

[Pointers and Array Names](#)

[Negative Subscripts](#)

[Address Arithmetic](#)

[String Functions defined in string.h](#)

[Fifo Queue Example](#)

An array is a collection of like variables that share a single name. The individual elements of an array are referenced by appending a *subscript*, in square brackets, behind the name. The subscript itself can be any legitimate C expression that yields an integer value, even a general expression. Therefore, arrays in C may be regarded as collections of like variables. Although arrays represent one of the simplest data structures, it has widespread usage in embedded systems.

Strings are similar to arrays with just a few differences. Usually, the array size is fixed, while strings can have a variable number of elements. Arrays can contain any data type (**char short int** even other arrays) while strings are usually ASCII characters terminated with a NULL (0) character. In general we allow random access to individual array elements. On the other hand, we usually process strings sequentially character by character from start to end. Since these differences are a matter of semantics rather than specific limitations imposed by the syntax of the C programming language, the descriptions in this chapter apply equally to data arrays and character strings. [String literals](#) were discussed earlier in Chapter 3; in this chapter we will define data structures to hold our strings. In addition, C has a rich set of predefined functions to manipulate strings.

Array Subscripts

When an array element is referenced, the subscript expression designates the desired element by its position in the **data**. The first element occupies position zero, the second position one, and so on. It follows that the last element is subscripted by [N-1] where N is the number of elements in the array. The statement

```
data[9] = 0;
```

for instance, sets the tenth element of **data** to zero. The array subscript can be any expression that results in a 16-bit integer. The following for-loop clears 100 elements of the array **data** to zero.

```
for(j=0;j<100;j++) data[j] = 0;
```

If the array has two dimensions, then two subscripts are specified when referencing. As programmers we may assign logical meaning to the first and second subscripts. For example we could consider the first subscript as the row and the second as the column. Then, the statement

```
ThePosition = position[3][5];
```

copies the information from the 4th row 6th column into the variable **ThePosition**. If the array has three dimensions, then three subscripts are specified when referencing. Again we may any assign logical meaning to the various subscripts. For example we could consider the first subscript as the x coordinate, the second subscript as the y coordinate and the third subscript as the z coordinate. Then, the statement

```
humidity[2][3][4]=100;
```

sets the humidity at point (2,3,4) to 100. Array subscripts are treated as signed 16-bit integers. It is the programmer's responsibility to see that only positive values are produced, since a negative subscript would refer to some point in memory preceding the array. One must be particularly careful about assuming what existing either in front of or behind our arrays in memory.

Array Declarations

Just like any variable, arrays must be declared before they can be accessed. The number of elements in an array is determined by its declaration. Appending a constant expression in square brackets to a name in a declaration identifies the name as the name of an array with the number of elements indicated. Multi-dimensional arrays require multiple sets of brackets. The examples in Listing 8-1 are valid declarations.

```
short data[5];      /* define data, allocate space for 5 16-bit integers
*/
char string[20];   /* define string, allocate space for 20 8-bit
characters */
int time,width[6]; /* define time, width, allocate space for 16-bit
characters */
short xx[10][5];   /* define xx, allocate space for 50 16-bit integers
*/
short pts[5][5][5]; /* define pts, allocate space for 125 16-bit
integers */
extern char buffer[]; /* declare buffer as an external character array
*/
```

Listing 8-1: Example showing a array declarations

Notice in the third example that ordinary variables may be declared together with arrays in the same statement. In fact array declarations obey the syntax rules of ordinary declarations, as described in [Chapters 4](#) and [7](#), except that certain names are designated as arrays by the presence of a dimension expression.

Notice the size of the external array, **buffer[]**, is not given. This leads to an important point about how C deals with array subscripts. The array dimensions are only used to determine how much memory to reserve. **It is the programmer's responsibility to stay within the proper bounds.** In particular, you must not let the subscript become negative for above N-1, where N is the size of the array.

Another situation in which an array's size need not be specified is when the array elements are given initial values. As we will see in [Chapter 9](#), the compiler will determine the size of such an array from the number of initial values.

Array References

In C we may refer to an array in several ways. Most obviously, we can write subscripted references to array elements, as we have already seen. C interprets an unsubscripted array name as the address of the array. In the following example, the first two lines set **x** to equal the value of the first element of the array. The third and fourth lines both set **pt** equal to the address of the array. [Chapter 7](#) introduced the address operator **&** that yields the address of an object. This operator may also be used with array elements. Thus, the expression **&data[3]** yields the address of the fourth element. Notice too that **&data[0]** and **data+0** and **data** are all equivalent. It should be clear by analogy that **&data[3]** and **data+3** are also equivalent.

```
short x,*pt,data[5]; /* a variable, a pointer, and an array */
void Set(void){
    x=data[0];      /* set x equal to the first element of data */
    x=*data;        /* set x equal to the first element of data */
    pt=data;        /* set pt to the address of data */
    pt=&data[0];    /* set pt to the address of data */
    x=data[3];      /* set x equal to the fourth element of data */
    x=(data+3);     /* set x equal to the fourth element of data */
    pt=data+3;      /* set pt to the address of the fourth element */
    pt=&data[3];    /* set pt to the address of the fourth element */
}
```

Listing 8-2: Example showing array references

Pointers and Array Names

The examples in the section suggest that pointers and array names might be used interchangeably. And, in many cases, they may. C will let us subscript pointers and also use array names as addresses. In the following example, the pointer **pt** contains the address of an array of integers. Notice the expression **pt[3]** is equivalent to ***(pt+3)**.

```
short *pt,data[5]; /* a pointer, and an array */
void Set(void){
    pt=data;        /* set pt to the address of data */
    data[2]=5;      /* set the third element of data to 5 */
    pt[2]=5;        /* set the third element of data to 5 */
    *(pt+2)=5;     /* set the third element of data to 5 */
}
```

Listing 8-3: Example showing pointers to access array elements

It is important to realize that although C accepts unsubscripted array names at addresses, they are not the same as pointers. In the following example, we can not place the unsubscripted array name on the left-hand-side of an assignment statement.

```
short buffer[5],data[5]; /* two arrays */
void Set(void){
```

```

    data=buffer;      /* illegal assignment */
}

```

Listing 8-4: Example showing an illegal array assignment

Since the unsubscripted array name is its address, the statement **data=buffer;** is an attempt to change its address. What sense would that make? The array, like any object, has a fixed home in memory; therefore, its address cannot be changed. We say that array is not a *lvalue*; that is, it cannot be used on the left side of an assignment operator (nor may it be operated on by increment or decrement operators). It simply cannot be changed. Not only does this assignment make no sense, it is physically impossible because an array address is not a variable. There is no place reserved in memory for an array's address to reside, only the elements.

Negative Subscripts

Since a pointer may point to any element of an array, not just the first one, it follows that negative subscripts applied to pointers might well yield array references that are in bounds. This sort of thing might be useful in situations where there is a relationship between successive elements in an array and it becomes necessary to reference an element preceding the one being pointed to. In the following example, **data** is an array containing time-dependent (or space-dependent) information. If **pt** points to an element in the array, **pt[-1]** is the previous element and **pt[1]** is the following one. The function calculates the second derivative using a simple discrete derivative.

```

short *pt,data[100]; /* a pointer and an array */
void CalcSecond(void){ short d2Vdt2;
    for(pt=data+1;pt<data+99;pt++)
        d2Vdt2=(pt[-1]-2*pt[0]+pt[1]);
}

```

Listing 8-5: Example showing negative array subscripting

Address Arithmetic

As we have seen, addresses (pointers, array names, and values produced by the address operator) may be used freely in expressions. This one fact is responsible for much of the power of C.

As with pointers ([Chapter 7](#)), all addresses are treated as unsigned quantities. Therefore, only unsigned operations are performed on them. Of all the arithmetic operations that could be performed on addresses only two make sense, displacing an address by a positive or negative amount, and taking the difference between two addresses. All others, though permissible, yield meaningless results.

Displacing an address can be done either by means of subscripts or by use of the plus and minus operators, as we saw earlier. These operations should be used only when the original address and the displaced address refer to positions in the same array or data structure. Any other situation would assume a knowledge of how memory is organized and would, therefore, be ill-advised for portability reasons.

As we saw in [Chapter 7](#), taking the difference of two addresses is a special case in which the compiler interprets the result as the number of objects lying between the addresses.

String Functions in string.h

ICC11 and ICC12 implement many useful string manipulation functions. Recall that strings are 8-bit arrays with a null-termination. The prototypes for these functions can be found in the **string.h** file. You simply include this file whenever you wish to use any of these routines. The rest of this section explains the functions one by one. ICC11 and ICC12 treat each of the counts as an unsigned 16-bit integer.

```
typedef unsigned int size_t;
void *memchr(void *, int, size_t);
int memcmp(void *, void *, size_t);
void *memcpy(void *, void *, size_t);
void *memmove(void *, void *, size_t);
void *memset(void *, int, size_t);
char *strcat(char *, const char *);
char *strchr(const char *, int);
int strcmp(const char *, const char *);
int strcoll(const char *, const char *);
char *strcpy(char *, const char *);
size_t strcspn(const char *, const char *);
size_t strlen(const char *);
char *strncat(char *, const char *, size_t);
int strncmp(const char *, const char *, size_t);
char *strncpy(char *, const char *, size_t);
char *strpbrk(const char *, const char *);
char *strrchr(const char *, int);
size_t strspn(const char *, const char *);
char *strstr(const char *, const char *);
```

Listing 8-6: Prototypes for string functions

The first five functions are general-purpose memory handling routines.

```
void *memchr(void *p, int c, size_t n);
```

Starting in memory at address **p**, **memchr** will search for the first unsigned 8-bit byte that matches the value in **c**. At most **n** bytes are searched. If successful, a pointer to the 8-bit byte is returned, otherwise a NULL pointer is returned.

```
int memcmp(void *p, void *q, size_t n);
```

Assuming the two pointers are directed at 8-bit data blocks of size **n**, **memcmp** will return a negative value if the block pointed to by **p** is lexicographically less than the block pointed to by **q**. The return value will be zero if they match, and positive if the block pointed to by **p** is lexicographically greater than the block pointed to by **q**.

```
void *memcpy(void *dst, void *src, size_t n);
```

Assuming the two pointers are directed at 8-bit data blocks of size **n**, **memcpy** will copy the data pointed to by pointer **src**, placing it in the memory block pointed to by pointer **dst**. The pointer **dst** is returned.

```
void *memmove(void *dst, void *src, size_t);
```

Assuming the two pointers are directed at 8-bit data blocks of size **n**, **memmove** will copy the data pointed to by pointer **src**, placing it in the memory block pointed to by pointer **dst**. This routine works even if the blocks overlap. The pointer **dst** is returned.

```
void *memset(void *p, int c, size_t n);
```

Starting in memory at address **p**, **memset** will set **n** 8-bit bytes to the 8-bit value in **c**. The pointer **p** is returned.

The remaining functions are string-handling routines.

```
char *strcat(char *p, const char *q);
```

Assuming the two pointers are directed at two null-terminated strings, **strcat** will append a copy of the string pointed to by pointer **q**, placing it the end of the string pointed to by pointer **p**. The pointer **p** is returned. It is the programmer's responsibility to ensure the destination buffer is large enough.

```
char *strchr(const char *p, int c);
```

Assuming the pointer is directed at a null-terminated string. Starting in memory at address **p**, **strchr** will search for the first unsigned 8-bit byte that matches the value in **c**. It will search until a match is found or stop at the end of the string. If successful, a pointer to the 8-bit byte is returned, otherwise a NULL pointer is returned.

```
int strcmp(const char *p, const char *q);  
int strcoll(const char *p, const char *q);
```

Assuming the two pointers are directed at two null-terminated strings, **strcmp** will return a negative value if the string pointed to by **p** is lexicographically less than the string pointed to by **q**. The return value will be zero if they match, and positive if the string pointed to by **p** is lexicographically greater than the string pointed to by **q**. In general C allows the comparison rule used in **strcoll** to depend on the current locale, but in ICC11 and ICC12 **strcoll** is the same as **strcmp**.

```
char *strcpy(char *dst, const char *src);
```

We assume **src** points to a null-terminated string and **dst** points to a memory buffer large enough to hold the string. **strcpy** will copy the string (including the null) pointed to by **src**, into the buffer pointed to by pointer **dst**. The pointer **dst** is returned. It is the programmer's responsibility to ensure the destination buffer is large enough.

```
size_t strcspn(const char *p, const char *q);
```

The string function **strcspn** will compute the length of the maximal initial substring within the string pointed to by **p** that has no characters in common with the string pointed to by **q**. For example the following call returns the value 5.

```
n=strcspn("label: ldaa 10,x ;comment", " ;*\n\t\l");
```

A common application of this routine is parsing for tokens. The first parameter is a line of text and the second parameter is a list of delimiters (e.g., space, semicolon, colon, star, return, tab and linefeed). The function returns the length of the first token (i.e., the size of *label*).

```
size_t strlen(const char *p);
```

The string function **strlen** returns the length of the string pointed to by pointer **p**. The length is the number of characters in the string not counting the null-termination.

```
char *strncat(char *p, const char *q, size_t n);
```

This function is similar to **strcat**. Assuming the two pointers are directed at two null-terminated strings, **strncat** will append a copy of the string pointed to by pointer **q**, placing it the end of the string pointed to by pointer **p**. The parameter **n** limits the number of characters, not including the null that will be copied. The pointer **p** is returned. It is the programmer's responsibility to ensure the destination buffer is large enough.

```
int strncmp(const char *p, const char *q, size_t n);
```

This function is similar to **strcmp**. Assuming the two pointers are directed at two null-terminated strings, **strncmp** will return a negative value if the string pointed to by **p** is lexicographically less than the string pointed to by **q**. The return value will be zero if they match, and positive if the string pointed to by **p** is lexicographically greater than the string pointed to by **q**. The parameter **n** limits the number of characters, not including the null that will be compared. For example, the following function call will return a zero because the first 8 characters are the same:

```
n=strncmp("MC68HC11A8","MC68HC11E9",8);
```

The following function is similar to **strcpy**.

```
char *strncpy(char *dst, const char *src, size_t n);
```

We assume **src** points to a null-terminated string and **dst** points to a memory buffer large enough to hold the string. **strncpy** will copy the string (including the null) pointed to by **src**, into the buffer pointed to by pointer **dst**. The pointer **dst** is returned. The parameter **n** limits the number of characters, not including the null that will be copied. If the size of the string pointed to by **src** is equal to or larger than **n**, then the null will not be copied into the buffer pointer to by **dst**. It is the programmer's responsibility to ensure the destination buffer is large enough.

```
char *strpbrk(const char *p, const char *q);
```

This function, **strpbrk**, is called *pointer to break*. The function will search the string pointed to by **p** for the first instance of any of the characters in the string pointed to by **q**. A pointer to the found character is returned. If the search fails to find any characters of the string pointed to by **q** in the string pointed to by **p**, then a null pointer is returned. For example the following call returns a pointer to the colon.

```
pt=strpbrk("label: ldaa 10,x ;comment",";*\n\t\l");
```

This function, like **strcspn**, can be used for parsing tokens.

```
char *strrchr(const char *p, int c);
```

The function **strchr** will search the string pointed to by **p** from the right for the first instance of the character in **c**. A pointer to the found character is returned. If the search fails to find any characters with the 8-bit value **c** in the string pointed to by **p**, then a null pointer is returned. For example the following calls set the **pt1** to point to the 'a' in *label* and **pt2** to point to the second 'a' in *ldaa*.

```
pt1=strchr("label: ldaa 10,x ;comment",'a');
pt1=strrchr("label: ldaa 10,x ;comment",'a');
```

Notice that **strchr** searches from the left while **strrchr** searches from the right .

```
size_t strspn(const char *p, const char *q);
```

The function **strspn** will return the length of the maximal initial substring in the string pointed to by **p** that consists entirely of characters in the string pointed to by **q**. In the following example the second string contains the valid set of hexadecimal digits. The function call will return 6 because there is a valid 6-digit hexadecimal string at the start of the line.

```
n=strspn("A12F05+12BAD*45","01234567890ABCDEF");
```

```
char *strstr(const char *p, const char *q);
```

The function **strstr** will search the string pointed to by **p** from the left for the first instance of the string pointed to by **q**. A pointer to the found substring within the first string is returned. If the search fails to find a match, then a null pointer is returned. For example the following calls set the **pt** to point to the 'l' in *ldaa*.

```
pt=strstr("label: ldaa 10,x ;comment","ldaa");
```

A FIFO Queue Example using indices

Another method to implement a statically allocated first-in-first-out FIFO is to use indices instead of [pointers](#). This method is necessary for compilers that do not support pointers. The purpose of this example is to illustrate the use of arrays and indices. Just like the previous FIFO, this is used for order-preserving temporary storage. The function **PutFifo** will enter one 8-bit byte into the queue, and **GetFifo** will remove one byte. If you call **PutFifo** while the FIFO is full (**Size** is equal to **FifoSize**), the routine will return a zero. Otherwise, **PutFifo** will save the data in the queue and return a one. The index **PutI** specifies where to put the next 8-bit data. The routine **GetFifo** actually returns two parameters. The queue status is the regular function return parameter, while the data removed from the queue is return by reference. I.e., the calling routine passes in a pointer, and **GetFifo** stores the removed data at that address. If you call **GetFifo** while the FIFO is empty (**Size** is equal to zero), the routine will return a zero. Otherwise, **GetFifo** will return the oldest data from the queue and return a one. The index **GetI** specifies where to get the next 8-bit data. The following FIFO implementation uses two indices and a counter.

```
/* Index,counter implementation of the FIFO */
#define FifoSize 10 /* Number of 8 bit data in the Fifo */
#define START_CRITICAL() asm(" tpa\n staa %SaveSP\n sei")
#define END_CRITICAL() asm( ldaa %SaveSP\n tap")
unsigned char PutI; /* Index of where to put next */
```

```

unsigned char GetI;    /* Index of where to get next */
unsigned char Size;   /* Number currently in the FIFO */
                    /* FIFO is empty if Size=0 */
                    /* FIFO is full  if Size= FifoSize */
char Fifo[FifoSize]; /* The statically allocated data */
void InitFifo(void) { unsigned char SaveSP;
    START_CRITICAL(); /* make atomic, entering critical section */
    PutI=GetI=Size=0; /* Empty when Size==0 */
    asm(" cli");      /* end critical section */
}
int PutFifo (char data) { unsigned char SaveSP;
    if (Size == FifoSize )
        return(0);    /* Failed, fifo was full */
    else{
        START_CRITICAL(); /* make atomic, entering critical section */
        Size++;
        Fifo[PutI++]=data; /* put data into fifo */
        if (PutI == FifoSize) PutI = 0; /* Wrap */
        END_CRITICAL(); /* end critical section */
        return(-1);     /* Successful */
    }
}
int GetFifo (char *datapt) { unsigned char SaveSP;
    if (Size == 0 )
        return(0);     /* Empty if Size=0 */
    else{
        START_CRITICAL(); /* make atomic, entering critical section */
        *datapt=Fifo[GetI++];
        Size--;
        if (GetI == FifoSize) GetI = 0;
        END_CRITICAL(); /* end critical section */
        return(-1); }
}

```

Listing 8-7: FIFO implemented with two indices and a counter

The `START_CRITICAL` and `END_CRITICAL` macros are specific to ICC11/ICC12, otherwise this example will operate using Hiware.

Go to [Chapter 9 on Structures](#) Return to [Table of Contents](#)

Chapter 9: Structures

What's in Chapter 9?

[Structure Declarations](#)

[Accessing elements of a structure](#)

[Initialization of structure data](#)

[Using pointers to access structures](#)

[Passing structures as parameters to functions](#)

[Example of MC68HC812A4 extended addressing](#)

[Example of a Linear Linked List](#)

[Example of a Huffman Code](#)

A structure is a collection of variables that share a single name. In an array, each element has the same format. With structures we specify the types and names of each of the elements or members of the structure. The individual members of a structure are referenced by their subname. Therefore, to access data stored in a structure, we must give both the name of the collection and the name of the element. Structures are one of the most powerful features of the C language. In the same way that functions allow us to extend the C language to include new operations, structures provide a mechanism for extending the data types. With structures we can add new data types derived from an aggregate of existing types.

Structure Declarations

Like other elements of C programming, the structure must be declared before it can be used. The declaration specifies the tagname of the structure and the names and types of the individual members. The following example has three members: one 16-bit integer and two character pointers

```
struct theport{
    int mode;        // 0 for I/O, 1 for in only -1 for out only
    unsigned char volatile *addr; // pointer to its address
    unsigned char volatile *ddr;} // pointer to its direction reg
```

The above declaration does not create any variables or allocate any space. Therefore to use a structure we must define a global or local variable of this type. The tagname (**theport**) along with the keyword **struct** can be used to define variables of this new data type:

```
struct theport PortA,PortB,PortC;
```

The above line defines the three variables and allocates 6 bytes for each of variable. If you knew you needed just three copies of structures of this type, you could have defined them as

```
struct theport{
    int mode;
    unsigned char volatile *addr;
    unsigned char volatile *ddr;}PortA,PortB,PortC;
```

Definitions like the above are hard to extend, so to improve code reuse we can use **typedef** to actually create

a new data type (called **port** in the example below) that behaves syntactically like **char int short** etc.

```
struct theport{
    int mode;          // 0 for I/O, 1 for in only -1 for out only
    unsigned char volatile *addr; // address
    unsigned char volatile *ddr;}; // direction reg
typedef struct theport port;
port PortA,PortB,PortC;
```

Once we have used **typedef** to create **port**, we don't need access to the name **theport** anymore. Consequently, some programmers use the following short-cut:

```
typedef struct {
    int mode;          // 0 for I/O, 1 for in only -1 for out only
    unsigned char volatile *addr; // address
    unsigned char volatile *ddr;}port; // direction reg
port PortA,PortB,PortC;
```

Similarly, I have also seen the following approach to creating structures that uses the same structure name as the **typedef** name:

```
struct port{
    int mode;          // 0 for I/O, 1 for in only -1 for out only
    unsigned char volatile *addr; // address
    unsigned char volatile *ddr;}; // direction reg
typedef struct port port;
port PortA,PortB,PortC;
```

Imagecraft and Hiware support all of the above methods of declaring and defining structures.

Accessing Members of a Structure

We need to specify both the structure name (name of the variable) and the member name when accessing information stored in a structure. The following examples show accesses to individual members:

```
PortB.mode=-1;          // 6811 Port B is output only
PortB.addr=(unsigned char volatile *)(0x1004);
PortC.mode=0;          // 6811 Port C is input and output
PortC.addr=(unsigned char volatile *)(0x1003);
PortC.ddr=(unsigned char volatile *)(0x1007);
(*PortC.ddr)=0;          // specify PortC as inputs
(*PortB.addr)=(*PortC.addr); // copy from PortC to PortB
```

The syntax can get a little complicated when a member of a structure is another structure as illustrated in the next example:

```

struct theline{
    int x1,y1;    // starting point
    int x2,y2;    // starting point
    char color;}; // color
typedef struct theline line;
struct thepath{
    line L1,L2;  // two lines
    char direction;};
typedef struct thepath path;
path p;        // global
void Setp(void){ line myLine; path q;
    p.L1.x1=5;  // black line from 5,6 to 10,12
    p.L1.y1=6;
    p.L1.x2=10;
    p.L1.y2=12;
    p.L1.color=255;
    p.L2={5,6,10,12,255}; // black line from 5,6 to 10,12
    p.direction=-1;
    myLine=p.L1;
    q={{0,0,5,6,128},{5,6,-10,6,128},1};
    q=p;
};

```

Listing 9-1: Examples of accessing structures

The local variable declaration **line myLine**; will allocate 7 bytes on the stack while **path q**; will allocate 15 bytes on the stack. In actuality most C compilers in an attempt to maintain addresses as even numbers will actually allocate 8 and 16 bytes respectively. In particular, the 6812 executes faster out of external memory if 16 bit accesses occur on even addresses. For example, a 16-bit data access to an external odd address requires two bus cycles, while a 16-bit data access to an external even address requires only one bus cycle. There is no particular odd-address speed penalty for any 6811 address or for 6812 internal addresses (internal RAM or EEPROM). Notice that the expression **p.L1.x1** is of the type **int**, the term **p.L1** has the type **line**, while just **p** has the type **path**. The expression **q=p**; will copy the entire 15 bytes that constitute the structure from **p** to **q**.

Initialization of a Structure

Just like any variable, we can specify the initial value of a structure at the time of its definition.

```

path thePath={{0,0,5,6,128},{5,6,-10,6,128},1};
line thePath={0,0,5,6,128};
port PortE={1,
    (unsigned char volatile *) (0x100A),
    (unsigned char volatile *) (0)};

```

If we leave part of the initialization blank it is filled with zeros.

```
path thePath={{0,0,5,6,128},};
line thePath={5,6,10,12,};
port PortE={1, (unsigned char volatile *) (0x100A),};
```

To place a structure in ROM, we define it as a global constant. In the following example the structure fsm[3] will be allocated and initialized in ROM-space. The linked-structure finite syatem machine is a good example of a ROM-based structure. For more information about finite state machines see Chapter 2 of the book Embedded Microcomputer Systems: Real Time Interfacing by Jonathan Valvano published by Brooks-Cole.

```
const struct State{
    unsigned char Out;        /* Output to Port H */
    unsigned int Wait;       /* Time (E cycles) to wait */
    unsigned char AndMask[4];
    unsigned char EquMask[4];
    const struct State *Next[4]; /* Next states */
typedef const struct State StateType;
typedef StateType * StatePtr;
#define stop &fsm[0]
#define turn &fsm[1]
#define bend &fsm[2]
StateType fsm[3]={
{0x34, 2000,    // stop 1 ms
    {0xFF, 0xF0, 0x27, 0x00},
    {0x51, 0xA0, 0x07, 0x00},
    {turn, stop, turn, bend}},
{0xB3, 5000,    // turn 2.5 ms
    {0x80, 0xF0, 0x00, 0x00},
    {0x00, 0x90, 0x00, 0x00},
    {bend, stop, turn, turn}},
{0x75, 4000,    // bend 2 ms
    {0xFF, 0x0F, 0x01, 0x00},
    {0x12, 0x05, 0x00, 0x00},
    {stop, stop, turn, stop}}};
```

Listing 9-2: Example of initializing a structure in ROM

Using pointers to access structures

Just like other variables we can use pointers to access information stored in a structure. The syntax is illustrated in the following examples:

```

void Setp(void){ path *ppt;
    ppt=&p;          // pointer to an existing global variable
    ppt->L1.x1=5;    // black line from 5,6 to 10,12
    ppt->L1.y1=6;
    ppt->L1.x2=10;
    ppt->L1.y2=12;
    ppt->L1.color=255;
    ppt->L2={5,6,10,12,255}; // black line from 5,6 to 10,12
    ppt->direction=-1;
    (*ppt).direction=-1;
};

```

Listing 9-3: Examples of accessing a structure using a pointer

Notice that the syntax **ppt->direction** is equivalent to **(*ppt).direction**. The parentheses in this access are required, because along with () and [], the operators . and -> have the highest precedence and associate from left to right. Therefore ***ppt.direction** would be a syntax error because **ppt.direction** can not be evaluated.

As an another example of pointer access consider the finite state machine controller for the fsm[3] structure shown above. The state machine is illustrated in Figure 9-1, and the program shown in Listing 9-4. There is [an example in Chapter 10](#) that extends this machine to implement function pointers.

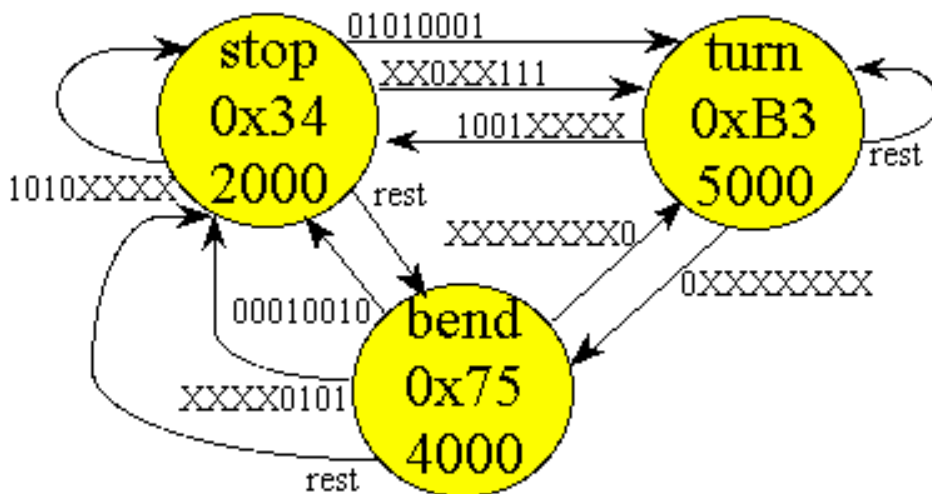


Figure 9-1: State machine

```

void control(void){ StatePtr Pt;
    unsigned char Input; int Endt; unsigned int i;
    TSCR |=0x80; // TEN(enable)
    TMSK2=0xA2; // TOI arm, TPU(pullup) timer/4 (500ns)
    DDRH=0xFF; // PortH bits 7-0 are outputs
    DDRJ=0x00; // PortJ bits 7-0 are inputs
    PUPSJ=0xFF; // Pullups J7-J0
    PULEJ=0xFF; // Enable pull up/down PortJ
    Pt=stop; // Initial State

```

```

while(1){
    PORTH=Pt->Out;          // 1) output
    Endt=TCNT+Pt->Wait;    // Time (500 ns each) to wait
    while(Endt-TCNT>0);   // 2) wait
    Input=PORTJ;          // 3) input
    for(i=0;i<4;i++)
        if((Input&Pt->AndMask[i])==Pt->EquMask[i]){
            Pt=Pt->Next[i]; // 4) next depends on input
            i=4; } } } ;

```

Listing 9-4: Finite state machine controller for MC68HC812A4

Passing Structures to Functions

Like any other data type, we can pass structures as parameters to functions. Because most structures occupy a large number of bytes, it makes more sense to pass the structure by reference rather than by value. In the following "call by value" example, the entire 6-byte structure is copied on the stack when the function is called.

```

unsigned char Input(port thePort){
    return (*thePort.addr); }

```

When we use "call by reference", a pointer to the structure is passed when the function is called.

```

typedef const struct {
    int mode; // 0 for I/O, 1 for in only -1 for out only
    unsigned char volatile *addr; // address
    unsigned char volatile *ddr;}port; // direction reg
port PortJ={
    0,
    (unsigned char volatile *) (0x0028),
    (unsigned char volatile *) (0x0029)};
int MakeOutput(port *ppt){
    if(ppt->mode==1) return 0; // input only
    if(ppt->mode== -1) return 1; // OK, output only
    (*ppt->ddr)=0xff; // make output
    return 1;}
int MakeInput(port *ppt){
    if(ppt->mode== -1) return 0; // output only
    if(ppt->mode==1) return 1; // OK, input only
    (*ppt->ddr)=0x00; // make input
    return 1;}
unsigned char Input(port *ppt){
    return (*ppt->addr);}
void Output(port *ppt, unsigned char data){
    (*ppt->addr)=data;
}

```

```

void main(void){ unsigned char MyData;
  MakeInput (&PortJ);
  MakeOutput (&PortJ);
  Output (&PortJ, 0);
  MyData=Input (&PortJ);
}

```

Listing 9-5: Port access organized with a data structure

Extended Address Data Page Interface to the MC68HC812A4

One of the unique features of the MC68HC812A4 is its ability to interface large RAM and ROM using the data page and program page memory respectively. Up to 1Meg bytes can be configured using the data page system. In this example, two 128K by 8 bit RAM chips are interfaced to the 6812 using 18 address pins (A17-A0). The 628128 is a 128K by 8 bit static RAM. When the paged memory is enabled Port G will contain address lines A21-A16 (although the data page system can only use up to A19). The built-in address decoder CSD must be used with the data page system. The details of this interface can be found in Chapter 9 of the book Embedded Microcomputer Systems: Real Time Interfacing by Jonathan Valvano published by Brooks Cole.

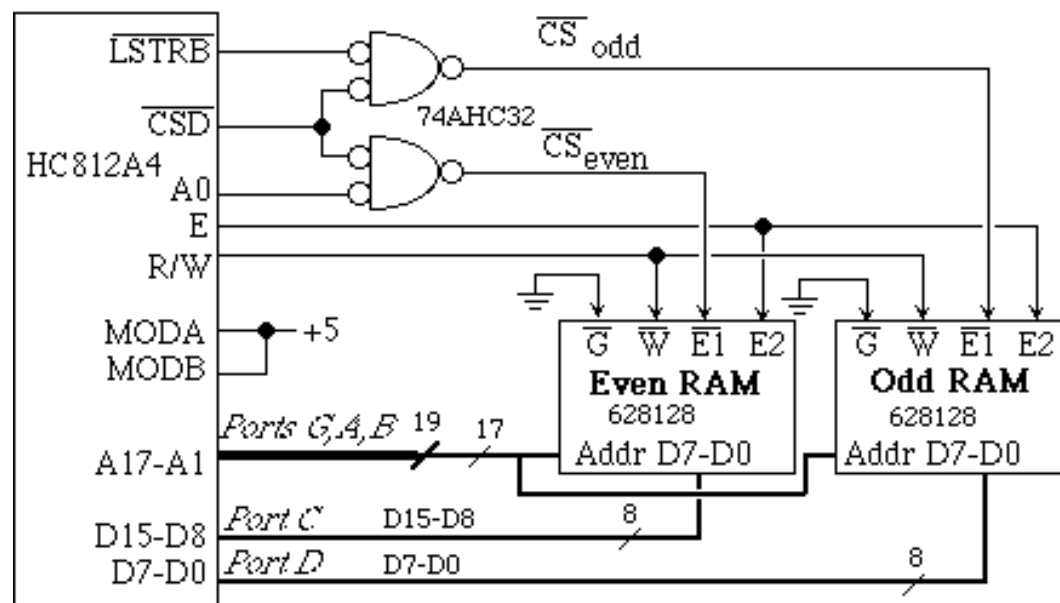


Figure 9-2: Extended data memory interface for the MC68HC812A4

We divide the software into initialization and access. The initialization software performs the usual steps:

- enable E, LSTRB, CSD, R/W outputs
- clear bit 4 in the CSCTL1 to set up CSD for the \$7000-\$7FFF range
- select 1 cycle stretch on CSD

To enable the data page system we must also

set bit 7 in WINDEF to enable the Data Page Window
 set bits 1,0 in MXAR to enable memory expansion pins A17-A16

```
void RAMinit(void){
    MODE=0x7B           // special expanded wide mode
    PEAR=0x2C;         // enable E, R/W, LSTRB
    WINDEF=WINDEF|0x80; // enable DPAGE
    MXAR=0x03;         // enable A17, A16 on Port G
    CSCTL0=CSCTL0|0x10; // enable CSD
    CSCTL1=CSCTL1&0xEF; // CSD $7000 to $7FFF
    CSSTR0=(CSSTR0&0xFC)|0x01; // 1 cycle stretch on CSD
```

Listing 9-6: Extended memory initialization on the MC68HC812A4

Let A17-A0 be the desired 256K RAM location. To access that location requires two steps

set the most significant addresses A17-A12 into DPAGE register
 access \$7000 to \$7FFF, with the least significant addresses A11-A0

```
struct addr20
{   unsigned char msb; // bits 19-12, only 17-12 used here
    unsigned int lsw;  // bits 11-0
};
typedef struct addr20 addr20Type;
char ReadMem(addr20Type addr){ char *pt;
    DPAGE=addr.msb; // set addr bits 19-12, only 17-12 used
    pt=(char*)(0x7000+addr.lsw); // set address bits 11-0
    return *pt;} // read access
void WriteMem(addr20Type addr, char data){ char *pt;
    DPAGE=addr.msb; // set addr bits 19-12, only 17-12 used
    pt=(char*)(0x7000+addr.lsw); // set address bits 11-0
    *pt=data;} // write access
```

Listing 9-7: Extended memory access on the MC68HC812A4

When MXAR is active, the MC68HC812A4 will convert all 16 addresses to the extended 22 bit addresses. When an access is outside the range of any active page window (EPAGE, DPAGE or PPAGE), the upper 6 bits are 1. In the following table it is assumed that only the DPAGE is active, the DPAGE register contains DP7-DP0, and MXAR is 0x3F (activating all 22 address bits)

internal	A21	A20	A19	A18	A17	A16	A15	A14	A13	A12	A11-A0
\$0xxx	1	1	1	1	1	1	0	0	0	0	xxx
\$1xxx	1	1	1	1	1	1	0	0	0	1	xxx
\$2xxx	1	1	1	1	1	1	0	0	1	0	xxx

\$3xxx	1	1	1	1	1	1	1	0	0	1	1	xxx
\$4xxx	1	1	1	1	1	1	1	0	1	0	0	xxx
\$5xxx	1	1	1	1	1	1	1	0	1	0	1	xxx
\$6xxx	1	1	1	1	1	1	1	0	1	1	0	xxx
\$7xxx	1	1	DP7	DP6	DP5	DP4	DP3	DP2	DP1	DP0		xxx

Table 9-1: Extended memory access on the MC68HC812A4

From this table, we can see another trick when using paged memory. If we were to switch the initialization so that CSD activated on \$0000-\$7FFF (CSCTL1=CSCTL1|0x10;), then 7 4K pages would overlap the regular address range \$0000 to \$6FFF. In this particular system, there are 64 data pages, numbered \$00 to \$3F. Notice that page numbers \$30 through \$36 overlap with regular data space \$0000 to \$6FFF. If we avoid using data pages \$30-\$36, we would have 28K of regular RAM from \$0000 to \$6FFF plus 57 4K windows of data paged memory space.

Linear Linked Lists

One of the applications of structures involves linking elements together with pointers. A linear linked list is a simple 1-D data structure where the nodes are chained together one after another. Each node contains data and a link to the next node. The first node is pointed to by the **HeadPt** and the last node has a null-pointer in the next field. A node could be defined as

```
struct node{
    unsigned short data; // 16 bit information
    struct node *next; // pointer to the next
};
typedef struct node nodeType;
nodeType *HeadPt;
```

Listing 9-8: Linear linked list node structure

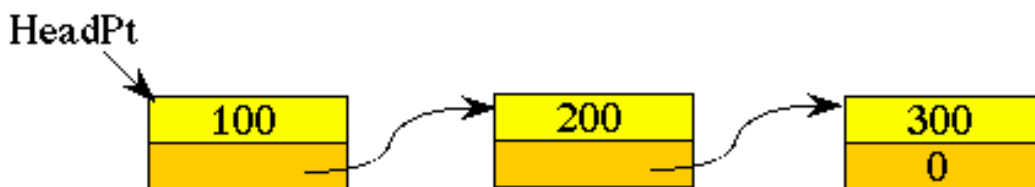


Figure 9-3: Linear linked list with 3 nodes

In order to store more data in the structure, we will first create a new node then link it into the list. The routine **StoreData** will return a true value if successful.

```
#include <STDLIB.H>;
int StoreData(unsigned short info){ nodeType *pt;
    pt=malloc(sizeof(nodeType)); // create a new entry
    if(pt){
```

```

    pt->data=info;                // store data
    pt->next=HeadPt;             // link into existing
    HeadPt=pt;
    return(1);
}
return(0);                      // out of memory
};

```

Listing 9-9: Code to add a node at the beginning of a linear linked list

In order to search the list we start at the **HeadPt**, and stop when the pointer becomes **null**. The routine **Search** will return a pointer to the node if found, and it will return a null-pointer if the data is not found.

```

nodeType *Search(unsigned short info){ nodeType *pt;
    pt=HeadPt;
    while(pt){
        if(pt->data==info)
            return (pt);
        pt=pt->next;    // link to next
    }
    return(pt);        // not found
};

```

Listing 9-10: Code to find a node in a linear linked list

To count the number of elements, we again start at the **HeadPt**, and stop when the pointer becomes **null**. The routine **Count** will return the number of elements in the list.

```

unsigned short Count(void){ nodeType *pt;
    unsigned short cnt;
    cnt=0;
    pt=HeadPt;
    while(pt){
        cnt++;
        pt=pt->next;    // link to next
    }
    return(cnt);
};

```

Listing 9-11: Code to count the number of nodes in a linear linked list

If we wanted to maintain a sorted list, then we can insert new data at the proper place, in between data elements smaller and larger than the one we are inserting. In the following figure we are inserting the element 250 in between elements 200 and 300.

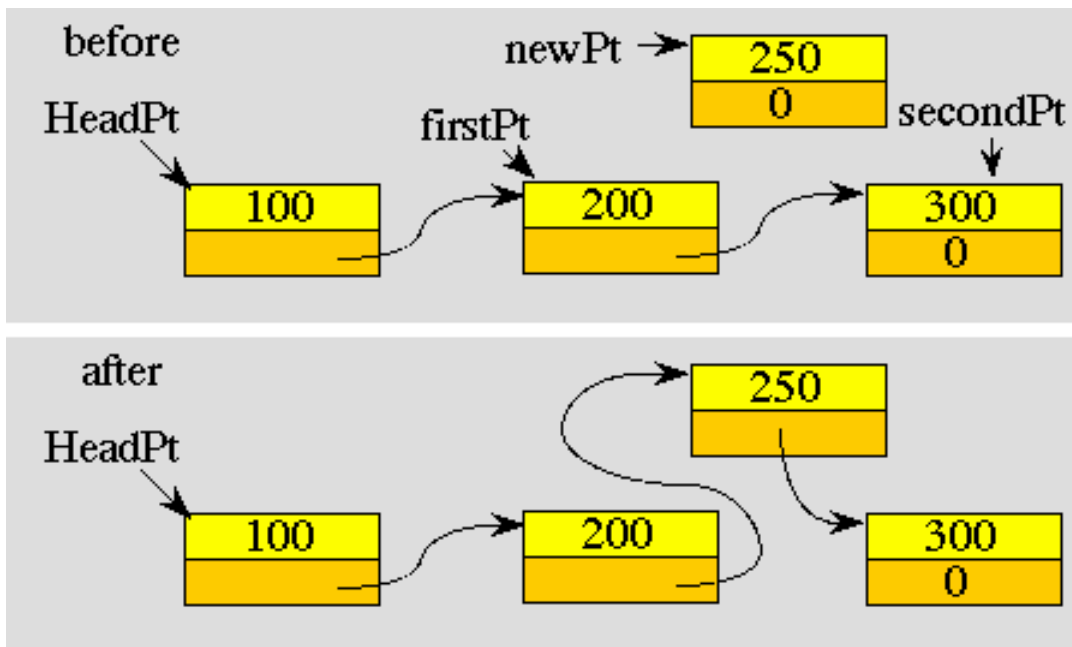


Figure 9-4: Inserting a node in sorted order

In case 1, the list is initially empty, and this new element is the first and only one. In case 2, the new element is inserted at the front of the list because it has the smallest data value. Case 3 is the general case depicted in the above figure. In this situation, the new element is placed in between **firstPt** and **secondPt**. In case 4, the new element is placed at the end of the list because it has the largest data value.

```
#include <STDLIB.H>
int InsertData(unsigned short info){
nodeType *firstPt,*secondPt,*newPt;
  newPt=malloc(sizeof(nodeType)); // create a new entry
  if(newPt){
    newPt->data=info; // store data
    if(HeadPt==0){ // case 1
      newPt->next=HeadPt; // only element
      HeadPt=newPt;
      return(1);
    }
    if(info<=HeadPt->data){ // case 2
      newPt->next=HeadPt; // first element in list
      HeadPt=newPt;
      return(1);
    }
    firstPt=HeadPt; // search from beginning
    secondPt=HeadPt->next;
    while(secondPt){
      if(info<=secondPt->data){ // case 3
        newPt->next=secondPt; // insert element here
        firstPt->next=newPt;
        return(1);
      }
      firstPt=secondPt; // search next
      secondPt=secondPt->next;
    }
  }
}
```

```

    }
    newPt->next=secondPt;    // case 4, insert at end
    firstPt->next=newPt;
    return(1);
}
return(0);    // out of memory
};

```

Listing 9-12: Code to insert a node in a sorted linear linked list

The following function will search and remove a node from the linked list. Case 1 is the situation in which an attempt is made to remove an element from an empty list. The return value of zero signifies the attempt failed. In case 2, the first element is removed. In this situation the **HeadPt** must be updated to now point to the second element. It is possible the second element does not exist, because the list originally had only one element. This is OK because in this situation **HeadPt** will be set to null signifying the list is now empty. Case 3 is the general situation in which the element at **secondPt** is removed. The element before, **firstPt**, is now linked to the element after. Case 4 is the situation where the element that was requested to be removed did not exist. In this case, the return value of zero signifies the request failed.

```

#include <STDLIB.H>;
int Remove(unsigned short info){
nodeType *firstPt,*secondPt;
    if(HeadPt==0)    // case 1
        return(0);    // empty list
    firstPt=HeadPt;
    secondPt=HeadPt->next;
    if(info==HeadPt->data){    // case 2
        HeadPt=secondPt;    // remove first element in list
        free(firstPt);    // return unneeded memory to heap
        return(1);
    }
    while(secondPt){
        if(secondPt->data==info){    // case 3
            firstPt->next=secondPt->next;    // remove this one
            free(secondPt);    // return unneeded memory to heap
            return(1);
        }
        firstPt=secondPt;    // search next
        secondPt=secondPt->next;
    }
    return(0);    // case 4, not found
};

```

Listing 9-13: Code to remove a node from a sorted linear linked list

Example of a Huffman Code

When information is stored or transmitted there is a fixed cost for each bit. Data compression and decompression provide a means to reduce this cost without loss of information. If the sending computer

compresses a message before transmission and the receiving computer decompresses it at the destination, the effective bandwidth is increased. In particular, this example introduces a way to process bit streams using Huffman encoding and decoding. A typical application is illustrated by the following flow diagram.

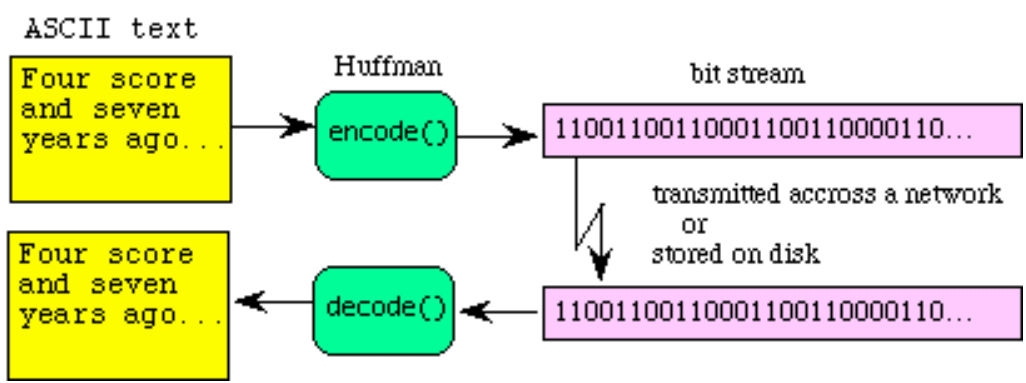


Figure 9-5: Data flow diagram showing a typical application of Huffman encoding and decoding

The Huffman code is similar to the Morse code in that they both use short patterns for letters that occur more frequently. In regular ASCII, all characters are encoded with the same number of bits (8). Conversely, with the Huffman code, we assign codes where the number of bits to encode each letter varies. In this way, we can use short codes for letter like "e s i a t o n" (that have a higher probability of occurrence) and long codes for seldom used consonants like "j q w z" (that have a lower probability of occurrence). To illustrate the encode-decode operations, consider the following Huffman code for the letters M,I,P,S. S is encoded as "0", I as "10", P as "110" and M as "111". We can store a Huffman code as a binary tree.

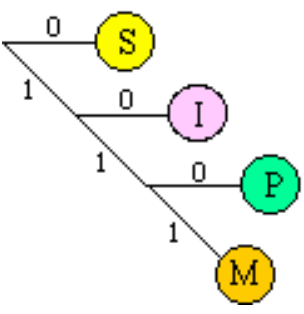


Figure 9-6: Huffman code for the letters S I P M

If "MISSISSIPPI" were to be stored in ASCII, it would require 10 bytes or 80 bits. With this simple Huffman code, the same string can be stored in 21 bits.



Figure 9-7: Huffman encoding for MISSISSIPPI

Of course, this Huffman code can only handle 4 letters, while the ASCII code has 128 possibilities, so it is not fair to claim we have an 80 to 21 bit savings. Nevertheless, for information that has a wide range of individual probabilities of occurrence, a Huffman code will be efficient. In the following implementation the functions **BitPut()** and **BitGet()** are called to save and recover binary data. The implementations of these two functions were presented back in [Chapter 2](#).

```

const struct Node{
    char Letter0;        // ASCII code if binary 0
    char Letter1;        // ASCII code if binary 1
    // Letter1 is NULL(0) if Link is pointer to another node
    const struct Node *Link;}; // binary tree pointer
typedef const struct Node NodeType;
typedef NodeType * NodePtr;
// Huffman tree
NodeType twentysixth= {'Q','Z',0};
NodeType twentyfifth= {'X',0,&twentysixth};
NodeType twentyfourth={'G',0,&twentyfifth};
NodeType twentythird= {'J',0,&twentyfourth};
NodeType twentysecond={'W',0,&twentythird};
NodeType twentyfirst= {'V',0,&twentysecond};
NodeType twentieth=    {'H',0,&twentyfirst};
NodeType nineteenth=   {'F',0,&twentieth};
NodeType eighteenth=  {'B',0,&nineteenth};
NodeType seventeenth= {'K',0,&eighteenth};
NodeType sixteenth=   {'D',0,&seventeenth};
NodeType fifteenth=   {'P',0,&sixteenth};
NodeType fourteenth=  {'M',0,&fifteenth};
NodeType thirteenth=  {'Y',0,&fourteenth};
NodeType twelfth=     {'L',0,&thirteenth};
NodeType eleventh=    {'U',0,&twelfth};
NodeType tenth=       {'R',0,&eleventh};
NodeType ninth=       {'C',0,&tenth};
NodeType eighth=      {'O',0,&ninth};
NodeType seventh=     {' ',0,&eighth};
NodeType sixth=       {'N',0,&seventh};
NodeType fifth=       {'I',0,&sixth};
NodeType fourth=      {'S',0,&fifth};
NodeType third=       {'T',0,&fourth};
NodeType second=      {'A',0,&third};
NodeType root=        {'E',0,&second};
//*****encode*****
// convert ASCII string to Huffman bit sequence
// returns bit count if OK
// returns 0          if BitFifo Full
// returns 0xFFFF    if illegal character

```

```

int encode(char *sPt){ // null-terminated ASCII string
    int NotFound; char data;
    int BitCount=0; // number of bits created
    NodePtr hpt; // pointer into Huffman tree
    while (data>(*sPt)){
        sPt++; // next character
        hpt=&root; // start search at root
        NotFound=1; // changes to 0 when found
        while(NotFound){
            if ((hpt->Letter0)==data){
                if(!BitPut(0))
                    return (0); // data structure full
                BitCount++;
                NotFound=0; }
            else {
                if(!BitPut(1))
                    return (0); // data structure full
                BitCount++;
                if ((hpt->Letter1)==data)
                    NotFound=0;
                else { // doesn't match either Letter0 or Letter1
                    hpt=hpt->Link;
                    if (hpt==0) return (0xFFFF); // illegal, end of tree?
                }
            }
        }
    }
    return BitCount;
}

//*****decode*****
// convert Huffman bit sequence to ASCII
// will remove from the BitFifo until it is empty
// returns character count
int decode(char *sPt){ // null-terminated ASCII string
    int CharCount=0; // number of ASCII characters created
    int NotFound; unsigned int data;
    NodePtr hpt; // pointer into Huffman tree
    hpt=&root; // start search at root
    while (BitGet(&data)){
        if (data==0){
            (*sPt)= (hpt->Letter0);
            sPt++;
            CharCount++;
            hpt=&root;} // start over and search at root
        else //data is 1
            if((hpt->Link)==0){
                (*sPt)= (hpt->Letter1);
                sPt++;
                CharCount++;
            }
    }
}

```

```
        hpt=&root;}    // start over and search at root
    else {            // doesn't match either Letter0 or Letter1
        hpt=hpt->Link;
    }
}
(*sPt)=0; // null terminated
return CharCount;
}
```

Listing 9-14: A Huffman code implementation

Go to [Chapter 10 on Functions](#) Return to [Table of Contents](#)

Chapter 10: Functions

What's in Chapter 10?

[Function Declarations](#)

[Function Definitions](#)

[Function Calls](#)

[Parameter Passing](#)

[Making our C programs "look like" C++](#)

[Stack frame created by ICC11 and ICC12](#)

[Animation of ICC12 function call](#)

[Finite State Machine using Function Pointers](#)

[Linked list interpreter](#)

We have been using functions throughout this document, but have put off formal presentation until now because of their immense importance. The key to effective software development is the appropriate division of a complex problem in modules. A module is a software task that takes inputs, operates in a well-defined way to create outputs. In C, functions are our way to create modules. A small module may be a single function. A medium-sized module may consist of a group of functions together with global data structures, collected in a single file. A large module may include multiple medium-sized modules. A hierarchical software system combines these software modules in either a top-down or bottom-up fashion. We can consider the following criteria when we decompose a software system into modules:

- 1) We wish to make the overall software system easy to understand;
- 2) We wish to minimize the coupling or interactions between modules;
- 3) We wish to group together I/O port accesses to similar devices;
- 4) We wish to minimize the size (maximize the number) of modules;
- 5) Modules should be able to be tested independently;
- 6) We should be able to replace/upgrade one module with effecting the others;
- 7) We would like to reuse modules in other situations.

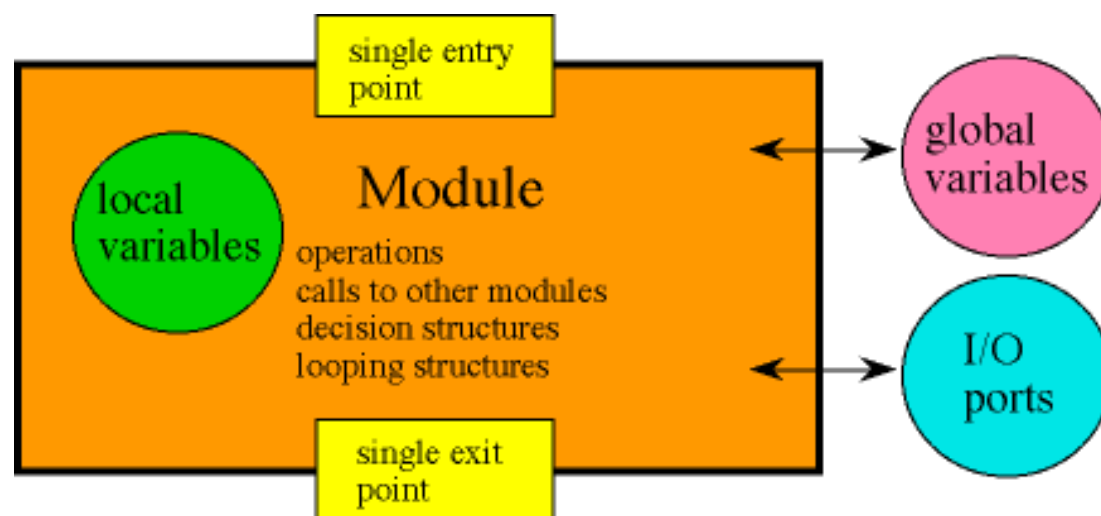


Figure 10-1: A module has inputs and outputs

As a programmer we must take special care when dealing with global variables and I/O ports. In order to reduce the complexity of the software we will limit access to global variables and I/O ports. It is essential to divide a large software task into smaller, well-defined and easy to debug modules. For more information about modular programming see Chapter 2 of the book Embedded Microcomputer Systems: Real Time Interfacing by Jonathan Valvano published by Brooks-Cole.

The term *function* in C is based on the concept of mathematical functions. In particular, a mathematical function is a well-defined operation that translates a set of input values into a set of output values. In C, a function translates a set of input values into a single output value. We will develop ways for our C functions to return multiple output values and for a parameter to be both an input and an output parameter. As a simple example consider the function that converts temperature in degrees F into temperature in degrees C.

```
int FtoC(int TempF){
    int TempC;
    TempC=(5*(TempF-32))/9;    // conversion
    return TempC;}

```

When the function's name is written in an expression, together with the values it needs, it represents the result that it produces. In other words, an operand in an expression may be written as a function name together with a set of values upon which the function operates. The resulting value, as determined by the function, replaces the function reference in the expression. For example, in the expression

```
FtoC(T+2)+4;    // T+2 degrees Fahrenheit plus 4 degrees Centigrade

```

the term **FtoC(T+2)** names the function **FtoC** and supplies the variable **T** and the constant **2** from which **FtoC** derives a value, which is then added to **4**. The expression effectively becomes

```
((5*((T+2)-32))/9)+4;
```

Although **FtoC(T+2)+4** returns the same result as **((5*((T+2)-32))/9)+4**, they are not identical. As will we see later in this chapter, the function call requires the parameter **(T+2)** to be passed on the stack and a subroutine call will be executed.

Function Declarations

Similar to the approach with variables, C differentiates between a function declaration and a function definition. A declaration specifies the syntax (name and input/output parameters), whereas a function definition specifies the actual program to be executed when the function is called. Many C programmers refer to function declaration as a prototype. Since the C compiler is essentially a one-pass process (not including the preprocessor), a function must be declared (or defined) before it can be called. A function declaration begins with the type (format) of the return parameter. If there is no return parameter, then the type can be either specified as **void** or left blank. Next comes the function name, followed by the parameter list. In a function declaration we do not have to specify names for the input parameters, just their types. If there are no input parameters, then the type can be either specified as **void** or left blank. The following examples illustrate that the function declaration specifies the name of the function and the types of the function parameters.

```

// declaration          input          output
void Ritual(void);      // none          none
char InChar(void);     // none          8-bit
void OutChar(char);    // 8-bit         none
short InSDec(void);    // none          16-bit
void OutSDec(short);   // 16-bit        none
char Max(char, char);  // two 8-bit     8-bit
int EMax(int, int);    // two 16-bit    16-bit
void OutString(char*); // pointer to 8-bit none
char *alloc(int);      // 16-bit        pointer to 8-bit
int Exec(void(*fnctPt)(void)); // function pointer 16-bit

```

Normally we place function declarations in the header file. We should add comments that explain what the function does.

```

void InitSCI(void);    // Initialize 38400 bits/sec
char InChar(void);    // Reads in a character, gadfly
void OutChar(char);   // Output a character, gadfly
char UpCase(char);    // Converts lower case character to upper
case
void InString(char *, unsigned int); // Reads in a String of max
length

```

To illustrate some options when declaring functions, we give alternative declarations of these same five functions:

```

InitSCI();
char InChar();
void OutChar(char letter);
char UpCase(char letter);
InString(char *pt, unsigned int MaxSize);

```

Sometimes we wish to call a function that will be defined in another module. If we define a function as external, software in this file can call the function (because the compiler knows everything about the function except where it is), and the linker will resolve the unknown address later when the object codes are linked.

```

extern void InitSCI(void);
extern char InChar(void);
extern void OutChar(char);
extern char UpCase(char);
extern void InString(char *, unsigned int);

```

One of the power features of C is to define pointers to functions. A simple example follows:

```

int (*fp)(int); // pointer to a function with input and output

```

```

int fun1(int input){
    return(input+1);    // this adds 1
};
int fun2(int input){
    return(input+2);    // this adds 2
};
void Setp(void){ int data;
    fp=&fun1;          // fp points to fun1
    data=( *fp)(5); // data=fun1(5);
    fp=&fun2;          // fp points to fun2
    data=( *fp)(5); // data=fun2(5);
};

```

Listing 10-1: Example of a function pointer

The declaration of **fp** looks a bit complicated because it has two sets of parentheses and an asterisk. In fact, it declares **fp** to be a pointer to any function that returns integers. In other words, the line **int (*fp)(int);** doesn't define the function. As in other declarations, the asterisk identifies the following name as a pointer. Therefore, this declaration reads "**fp** is a pointer to a function with a 16-bit signed input parameter that returns a 16-bit signed output parameter." Using the term *object* loosely, the asterisk may be read in its usual way as "object at." Thus we could also read this declaration as "the object at **fp** is a function with an **int** input that returns an **int**."

So why the first set of parentheses? By now you have noticed that in C declarations follow the same syntax as references to the declared objects. And, since the asterisk and parentheses (after the name) are expression operators, an evaluation precedence is associated with them. In C, parentheses following a name are associated with the name before the preceding asterisk is applied to the result. Therefore,

```
int *fp(int);
```

would be taken as

```
int *(fp(int));
```

saying that **fp** is a function returning a pointer to an integer, which is not at all like the declaration in Listing 10-1.

Function Definitions

The second way to declare a function is to fully describe it; that is, to *define* it. Obviously every function must be defined somewhere. So if we organize our source code in a bottom up fashion, we would place the lowest level functions first, followed by the function that calls these low level functions. It is possible to define large project in C without ever using a standard declaration (function prototype). On the other hand, most programmers like the top-down approach illustrated in the following example. This example includes three modules: the LCD interface, the COP functions, and some Timer routines. Notice the function names

are chosen to reflect the module in which they are defined. If you are a C++ programmer, consider the similarities between this C function call **LCDclear()** and a C++ LCD class and a call to a member function **LCD.clear()**. The *.H files contain function declarations and the *.C files contain the implementations.

```
#include "HC12.H"
#include "LCD12.H"
#include "COP12.H"
#include "Timer.H"
void main(void){ char letter; int n=0;
    COPinit(); // Enable TOF interrupt to make COP happy
    LCDinit();
    TimerInit()
    LCDString("Adapt812 LCD");
    TimerMsWait(1000);
    LCDclear();
    letter='a'-1;
    while(1){
        if (letter=='z')
            letter='a';
        else
            letter++;
        LCDputchar(letter);
        TimerMsWait(250);
        if(++n==16){
            n=0;
            LCDclear();
        }
    }
}
#include "LCD12.C"
#include "COP12.C"
#include "Timer.C"
#include "VECTORS.C"
```

Listing 10-2: Modular approach to software development

C function definitions have the following form

```
type Name(parameter list){
CompoundStatement
};
```

Just like the function declaration, we begin the definition with its **type**. The **type** specifies the function return parameter. If there is no return parameter we can use **void** or leave it blank. **Name** is the name of the function. The **parameter list** is a list of zero or more names for the arguments that will be received by the function when it is called. Both the type and name of each input parameter is required. As we will see later, ICC11 and ICC12 pass the first (left most) parameter in Reg D, and the remaining parameters are passed

on the stack. Then once inside the function, `ICC12` and `ICC12` functions will push register `D` on the stack, so after that all parameters are on the stack. The output parameter is returned in register `D`. 8-bit output parameters are promoted to 16-bits. Similarly, most input parameters are also passed as 16-bit values, 8-bit characters are promoted to 16-bit integers and arrays and strings are passed as pointers. The exception to this rule is 32-bit longs and 32-bit floats.

Although a character is passed as a word, we are free to declare its formal argument as either character or word. If it is declared as a character, only the low-order byte of the actual argument will be referenced. If it is declared as an integer, then all 16 bits will be referenced.

It is generally more efficient to reference integers than characters because there is no need for a machine instruction to set the high-order byte. So it is common to see situations in which a character is passed to a function which declares the argument to be an integer. But there is one caveat here: not all C compilers promote character arguments to integers when passing them to functions; the result is an unpredictable value in the high-order byte of the argument. This should be remembered as a portability issue.

Since there is no way in C to declare strings, we cannot declare formal arguments as strings, but we can declare them as character pointers or arrays. In fact, as we have seen, C does not recognize strings, but arrays of characters. The string notation is merely a shorthand way of writing a constant array of characters.

Furthermore, since an unsubscripted array name yields the array's address and since arguments are passed by value, an array argument is effectively a pointer to the array. It follows that, the formal argument declarations `arg[]` and `*arg` are really equivalent. The compiler takes both as pointer declarations. Array dimensions in argument declarations are ignored by the compiler since the function has no control over the size of arrays whose addresses are passed to it. It must either assume an array's size, receive its size as another argument, or obtain it elsewhere.

The last, and most important, part of the function definition above is **CompoundStatement**. This is where the action occurs. Since compound statements may contain local declarations, simple statements, and other compound statements, it follows that functions may implement algorithms of any complexity and may be written in a structured style. Nesting of compound statements is permitted without limit.

As an example of a function definition consider

```
int add3(int z1, int z2, int z3){ int y;
    y=z1+z2+z3;
    return(y); }
```

Listing 10-3: Example function with 3 inputs and one output.

Here is a function named **add3** which takes three input arguments.

Function Calls

A function is called by writing its name followed by a parenthesized list of argument expressions. The general form is

Name (parameter list)

where **Name** is the name of the function to be called. The **parameter list** specifies the particular input parameters used in this call. Notice that each input parameter is in fact an expression. It may be as simple as a variable name or a constant, or it may be arbitrarily complex, including perhaps other function calls. Whatever the case, the resulting value is pushed onto the stack where it is passed to the called function.

C programs evaluate arguments from left to right, pushing them onto the stack in that order. As we will see later, the ICC11 and ICC12 compilers allocate the stack space for the parameters at the start of the program that will make the function call. Then the values are stored into the pre-allocated stack position before it calls the function. On return, the return parameter is located in Reg D. The input parameters are removed from the stack at the end of the program.

When the called function receives control, it refers to the first actual argument using the name of the first formal argument. The second formal argument refers to the second actual argument, and so on. In other words, actual and formal arguments are matched by position in their respective lists. Extreme care must be taken to ensure that these lists have the same number and type of arguments.

It was mentioned earlier, that function calls appear in expressions. But, since expressions are legal statements, and since expressions may consist of only a function call, it follows that a function call may be written as a complete statement. Thus the statement

```
add3(--counter, time+5, 3);
```

is legal. It calls **add3()**, passing it three arguments **--counter**, **time+5**, and **3**. Since this call is not part of a larger expression, the value that **add3()** returns will be ignored. As a better example, consider

```
y=add3(--counter, time+5, 3);
```

which is also an expression. It calls **add3()** with the same arguments as before but this time it assigns the returned value to **y**. It is a mistake to use an assignment statement like the above with a function that does not return an output parameter.

The ability to pass one function a pointer to another function is a very powerful feature of the C language. It enables a function to call any of several other functions with the caller determining which subordinate function is to be called.

```
int fun1(int input){
    return(input+1);    // this adds 1
};
int fun2(int input){
    return(input+2);    // this adds 2
};
```

```

int execute(int (*fp)(int)){ int data;
    data=(*fp)(5); // data=fun1(5);
    return (data);
};
void main(void){ int result;
    result=execute(&fun1); // result=fun1(5);
    result=execute(&fun2); // result=fun2(5);
};

```

Listing 10-4: Example of passing a function pointer

Notice that **fp** is declared to be a function pointer. Also, notice that the designated function is called by writing an expression of the same form as the declaration.

Argument Passing

Now let us take a closer look at the matter of argument passing. With respect to the method by which arguments are passed, two types of subroutine calls are used in programming languages--*call by reference* and *call by value*.

The *call by reference* method passes arguments in such a way that references to the formal arguments become, in effect, references to the actual arguments. In other words, references (pointers) to the actual arguments are passed, instead of copies of the actual arguments themselves. In this scheme, assignment statements have implied side effects on the actual arguments; that is, variables passed to a function are affected by changes to the formal arguments. Sometimes side effects are beneficial, and some times they are not. Since C supports only one formal output parameter, we can implement additional output parameters using call by reference. In this way the function can return parameters back using the reference. As an example recall the fifo queue program shown earlier in [Listing 8-7](#). The function **GetFifo**, shown below, returns two parameters. The regular formal parameter is a boolean specifying whether or not the request was successful, and the actual data removed from the queue is returned via the call by reference. The calling program **InChar** passes the address of its local variable data. The assignment statement ***datapt=Fifo[GetI++]**; within **GetFifo** will store the return parameter into a local variable of **InChar**. Normally **GetFifo** does not have the scope to access local variables of **InChar**, but in this case **InChar** explicitly granted that right by passing a pointer to **GetFifo**.

```

int GetFifo (char *datapt) {
    if (Size == 0 )
        return(0);      /* Empty if Size=0 */
    else{
        asm(" sei");    /* make atomic, entering critical section */
        *datapt=Fifo[GetI++]; Size--;
        if (GetI == FifoSize) GetI = 0;
        asm(" cli");    /* end critical section */
        return(-1); }
}
char InChar(void){ char data;
    while(GetFifo(&data)){};
    return (data);}

```

Listing 10-5: Multiple output parameters can be implemented using call by reference

When we use the *call by value* scheme, the values, not references, are passed to functions. With call by value copies are made of the parameters. Within a called function, references to formal arguments see copied values on the stack, instead of the original objects from which they were taken. At the time when the computer is executing within PutFifo() of the example below, there will be three separate and distinct copies of the 0x41 data (main, OutChar and PutFifo).

```
int PutFifo (char data) {
    if (Size == FifoSize) {
        return(0); /* Failed, fifo was full */
    } else{
        Size++;
        *(PutPt++)=data; /* put data into fifo */
        if (PutPt == &Fifo[FifoSize]) PutPt = &Fifo[0]; /* Wrap */
        return(-1); /* Successful */
    }
}
void OutChar(char data){
    while(PutFifo(data)){};
    SC0CR2=0xAC;}
void main(void){ char data=0x41;
    OutChar(data);}
```

Listing 10-6: Call by value passes a copy of the data.

The most important point to remember about passing arguments by value in C is that there is no connection between an actual argument and its source. Changes to the arguments made within a function, have no affect what so ever on the objects that might have supplied their values. They can be changed with abandon and their sources will not be affected in any way. This removes a burden of concern from the programmer since he may use arguments as local variables without side effects. It also avoids the need to define temporary variables just to prevent side effects.

It is precisely because C uses call by value that we can pass expressions, not just variables, as arguments. The value of an expression can be copied, but it cannot be referenced since it has no existence in global memory. Therefore, call by value adds important generality to the language.

Although the C language uses the call by value technique, it is still possible to write functions that have side effects; but it must be done deliberately. This is possible because of C's ability to handle expressions that yield addresses. And, since any expression is a valid argument, addresses can be passed to functions.

Since expressions may include assignment, increment, and decrement operators ([Chapter 9](#)), it is possible for argument expressions to affect the values of arguments lying to their right. (Recall that C evaluates argument expressions from left to right.) Consider, for example,

```
func (y=x+1, 2*y);
```

where the first argument has the value $x+1$ and the second argument has the value $2*(x+1)$. What would be the value of the second argument if arguments were evaluated from right to left? This kind of situation should be avoided, since the C language does not guarantee the order of argument evaluation. The safe way to write this is

```
y=x+1;
func (y, 2*y);
```

It is the programmer's responsibility to ensure that the parameters passed match the formal arguments in the function's definition. Some mistakes will be caught as syntax errors by the compiler, but this mistake is a common and troublesome problem for all C programmers.

Occasionally, the need arises to write functions that work with a variable number of arguments. An example is **printf()** in the library. ICC11 and ICC12 implement this feature using macros defined in the library file **STDARG.C**. To use these features you include **STDARG.H** in your file. For examples see the **STDIO.C** source file in your **LIBSRC** directory.

Private versus Public Functions

For every function definition, ICC11 and ICC12 generates an assembler directive declaring the function's name to be *public*. This means that every C function is a potential entry point and so can be accessed externally. One way to create private/public functions is to control which functions have declarations. Consider again the main program in Listing 10-2 shown earlier. Now lets look inside the **Timer.H** and **Timer.C** files. To implement Private and Public functions we place the function declarations of the Public functions in the **Timer.H** file.

```
void TimerInit(void);
void TimerMsWait(unsigned int time);
```

Listing 10-7: **Timer.H** header file has public functions

The implementations of all functions are included in the **Timer.C** file. The function, **TimerWait**, is private and can only be called by software inside the **Timer.C** file. We can apply this same approach to private and public global variables. Notice that in this case the global variable, **TimerClock**, is private and can not be accessed by software outside the **Timer.C** file.

```
unsigned int TimerClock; // private global
void TimerInit(void){ // public function
    TSCR |=0x80; // TEN(enable)
    TMSK2=0xA2; // TOI arm, TPU(pullup) timer/4 (500ns)
    TimerClock=2000; // 2000 counts per ms
}
void TimerWait(unsigned int time){ // private function
    TC5=TCNT+TimerClock; // 1.00ms wait
```

```

TFLG1 = 0x20;           // clear C5F
while((TFLG1&0x20)==0){};
void TimerMsWait(unsigned int time){ // public function
    for(;time>0;time--)
        TimerWait(TimerClock); // 1.00ms wait
}

```

Listing 10-8: Timer.C implementation file defines all functions

For more information about software development see Chapter 2 of the book Embedded Microcomputer Systems: Real Time Interfacing by Jonathan Valvano published by Brooks Cole.

The Stack Frame

Figure 10-2 illustrates the structure of a C stack frame. The stack frame generated by the ICC11 compiler places the explicit local variables (and other temporary data) at the top of the stack. Input parameters to the function and the subroutine return address are also on the stack. Recall that the 6811 stack pointer points to an empty place just above the top element of the stack. Within a 6811 function, **RegX** is not saved, but rather whenever stack access is required, the SP is copied into **RegX** using the **tsx** instruction and the stack is accessed using X-index address. The 6811 stack picture in Figure 10-2 illustrates the condition after executing **tsx**. The stack frame generated by the ICC12 compiler is similar, but not identical. Just like the 6811, the 6812 stack includes local variables, temporaries, subroutine return address and the input parameters. Just like the 6811, the first input parameter is above the return address and the remaining input parameters are below the return addressing. In actuality, this first input parameter is passed into the function in **RegD**, and the function itself pushes it on the stack. Different from the 6811, the 6812 stack pointer points to the top element of the stack. Within the 6812 function, **RegX** is saved, and then **RegX** is using within the function as a stack frame pointer. Because the value of **RegX** is maintained throughout the function and the 6812 stack is accessed simply using X-index addressing without having to execute **tsx** or (**tfr s,x**) each time. In particular notice **tsx** is executed twice in the 6811 main program in Listing 10-10, but **tfr s,x** is executed only once in the 6812 main program in Listing 10-11.

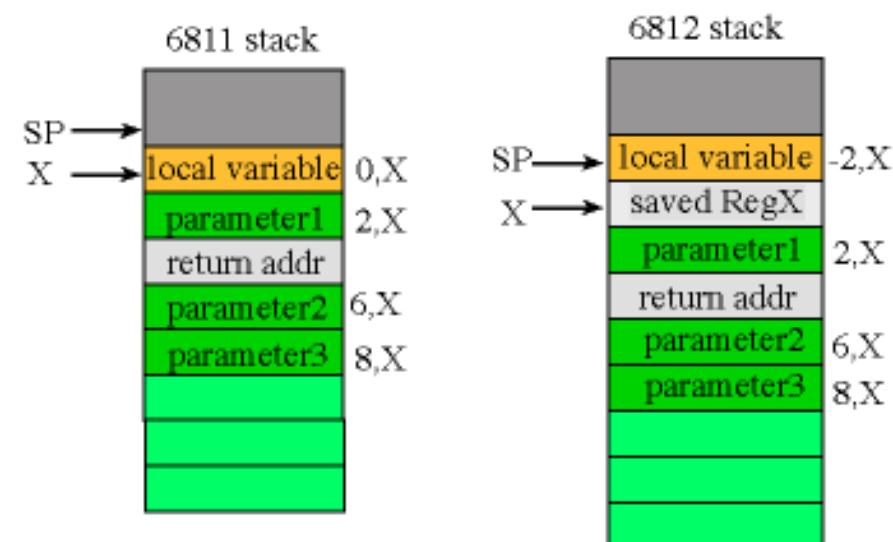


Figure 10-2: Stack frame for a function with one local variable and three input parameters.

In order to understand both the machine architecture and the C compiler, we can look at the assembly code generated. This example shows a simple C program with three global variables **x1**, **x2**, **x3**, two local variables both called **y** and three function parameters **z1**, **z2**, **z3**.

```
int x1;
static int x2;
const int x3=1000;
int add3(int z1, int z2, int z3){ int y;
    y=z1+z2+z3;
    return(y);}
void main(void){ int y;
    x1=1000;
    x2=1000;
    y=add3(x1,x2,x3);
```

Listing 10-9: Example function call with local variables

The first compiler we will study is the ImageCraft ICC11 version 4.0 for the Motorola 6811. The disassembled output has been edited to clarify its operation. The linker/loader allocates 3 segmented memory areas: code pointed to by the PC; global accessed with absolute addressing; and locals pointed to by the stack pointer SP. The global symbols, **_x1** **_x2** **_x3**, will be assigned or bound by the linker/loader. The **pshx** instruction allocates the local variable, and the **tsx** instruction establishes a stack frame pointer, X. This compiler passes the first input parameter (**z1**) into the subroutine by placing it in register D. The remaining parameters (**z2**, **z3** in this example) are pushed on the stack by the main program before the subroutine is called. The first operation the subroutine performs is to push the remaining parameter on the stack (**pshb psha**) so that all three parameters, **z1 z2 z3**, are on the stack.

```
.area text      ;text area is ROM
.globl _x3
_x3: .word 1000
.area text
.globl _add3
;   y -> 0,x
;   z3 -> 8,x
;   z2 -> 6,x
;   z1 -> 2,x
_add3: pshb      ;push z1 on stack
      psha
      pshx      ;allocate y
      tsx       ;create local stack frame
      ldd 2,x   ;RegD=z1
      addd 6,x  ;RegD=z1+z2
      addd 8,x  ;RegD=z1+z2+z3
      std 0,x   ;y=z1+z2+z3
      pulx     ;deallocate y
      pulx     ;deallocate z1
```

```

    rts
    .globl _main
;   y -> 4,x
_main:
    pshx    ;allocate y
    pshx    ;allocate temporary (z3)
    pshx    ;allocate temporary (z2)
    tsx
    ldd    #1000
    std    _x1    ;x1=1000
    std    _x2    ;x2=1000
    ldd    _x3
    std    2,x    ;z3=x3
    ldd    _x2
    std    0,x    ;z2=x2
    ldd    _x1    ;RegD=x1
    jsr    _add3
    tsx
    std    4,x    ;y=x1+x2+x3
    pulx    ;deallocate temporary (z2)
    pulx    ;deallocate temporary (z3)
    pulx    ;deallocate y
    rts
    .area bss    ;RAM
_x2: .blkb 2
    .globl _x1
_x1: .blkb 2

```

Listing 10-10: ICC11 assembly of function call with local variables

The stack frame at the time of the **add3** instructions is shown in Figure 10-3. Within the subroutine the local variables of main are not accessible.

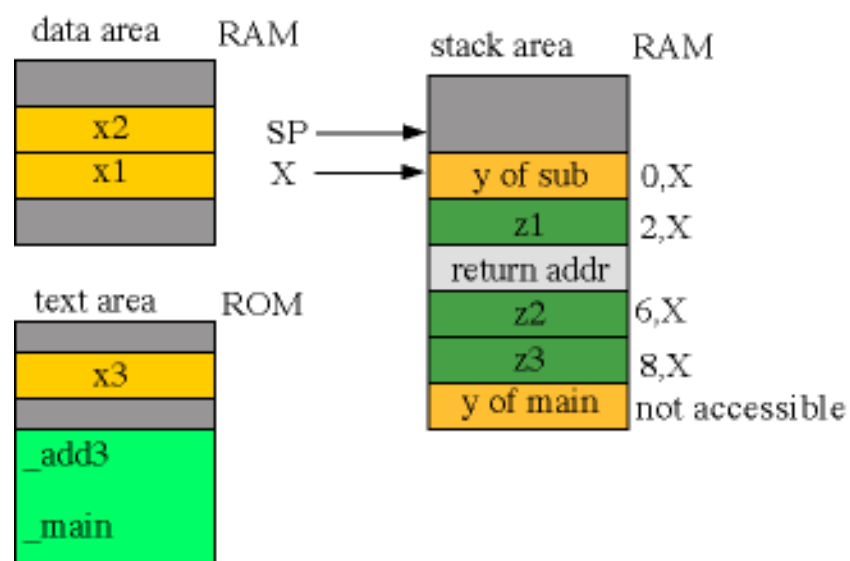


Figure 10-3: ICC11 stack within the add3 function

The second compiler we will study is the ImageCraft ICC12 version 5.1 for the Motorola 6812. This disassembled output also has been edited to clarify its operation. The linker/loader allocates 3 segmented memory areas: code pointed to by the PC; global accessed with absolute addressing; and locals pointed to by the stack pointer SP. The global symbols, `_x1 _x2 _x3`, will be assigned or bound by the linker/loader. The three instructions `pshx tfr s,x` and `leas -8,sp` allocates the local variable, and establishes a stack frame pointer, X. This compiler passes the first input parameter (`z1`) into the subroutine by placing it in register D. The remaining parameters (`z2, z3` in this example) are pushed on the stack by the main program before the subroutine is called. The first operation the subroutine performs is to push the remaining parameter on the stack (`pshd`) so that all three parameters, `z1 z2 z3`, are on the stack. Also notice that the `main` program allocates space for the parameters it needs to push when it calls `add3` at the beginning of `main`.

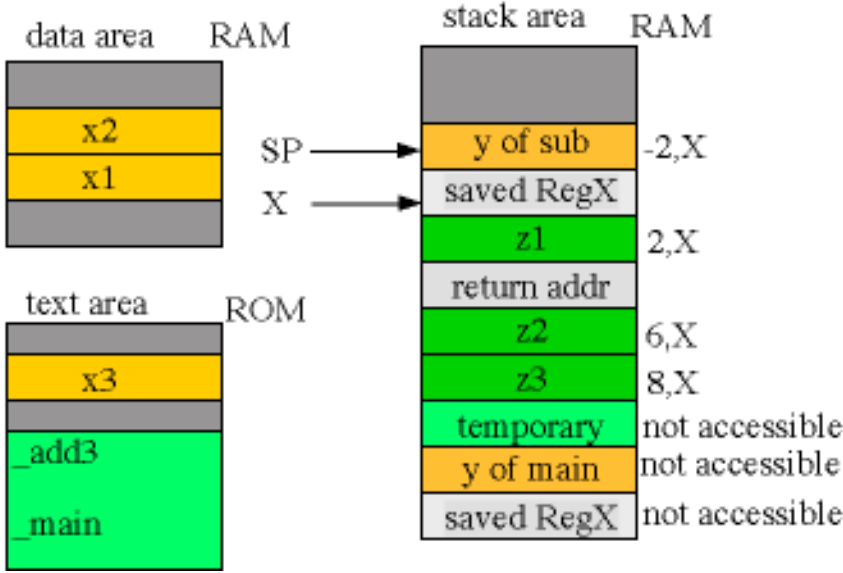


Figure 10-4: ICC12 stack within the add3 function

```

.area text
_x3:: .word 1000
.area text
; y -> -2,x
; z3 -> 8,x
; z2 -> 6,x
; z1 -> 2,x
_add3:: pshd
      pshx
      tfr s,x
      leas -2,sp
      ldd 2,x
      addd 6,x
      addd 8,x
      std -2,x
      ldd -2,x
      tfr x,s

```

```

    pulx
    leas 2,sp
    rts
; y -> -2,x
_main:: pshx
    tfr s,x
    leas -8,sp
    movw #1000,_x1
    movw #1000,_x2
    movw _x3,2,sp
    movw _x2,0,sp
    ldd _x1
    jsr _add3
    std -4,x
    tfr d,y
    sty -2,x
    tfr x,s
    pulx
    rts
.area bss
_x2:    .blkb 2
_x1::   .blkb 2

```

Listing 10-11: ICC12 assembly of function call with local variables

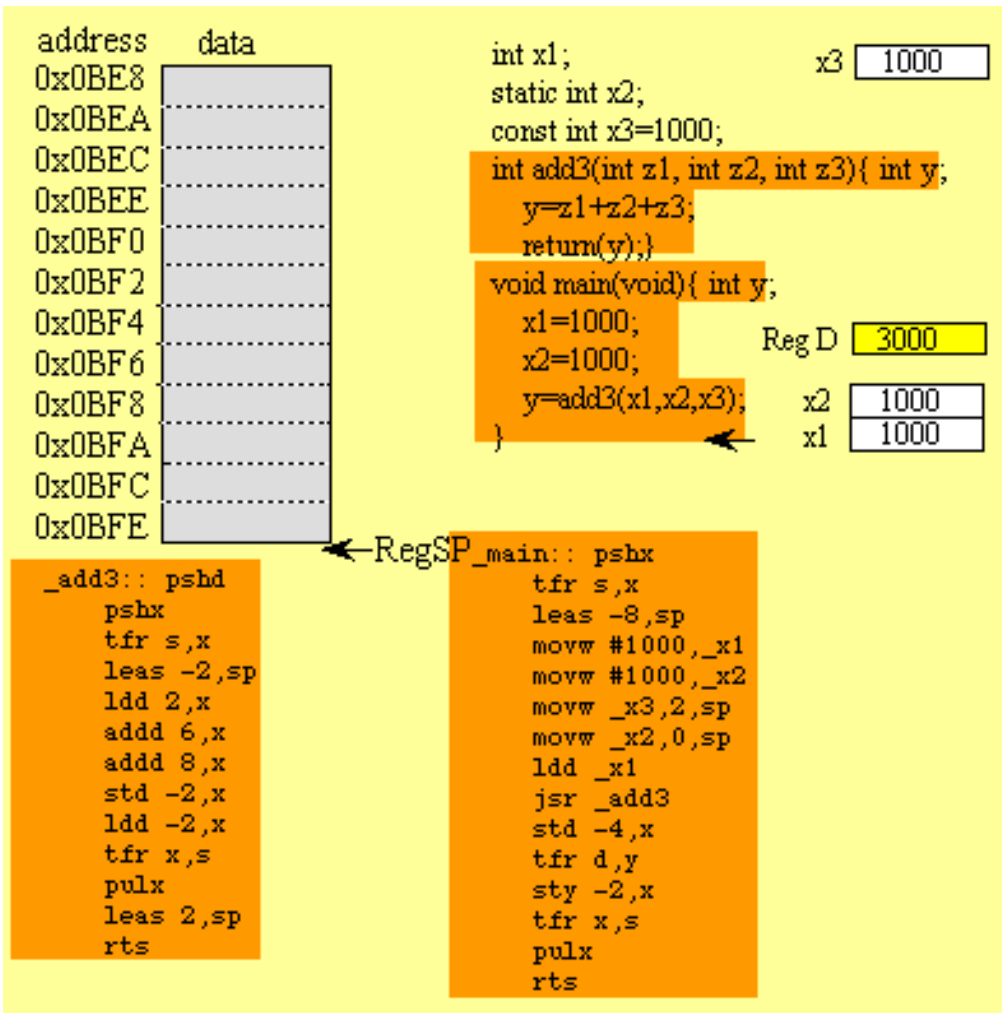


Figure 10-5: ICC12 animation of the `add3` function

Finite State Machine using Function Pointers

Now that we have learned how to declare, initialize and access function pointers, we can create very flexible finite state machines. In the finite state machine presented in [Listing 9-2](#) and [Listing 9-4](#), the output was a simple number that is written to the output port. In this next example, we will actually implement the exact same machine, but in a way that supports much more flexibility in the operations that each state performs. In fact we will define a general C function to be executed at each state. In this implementation the functions perform the same output as the previous machine.

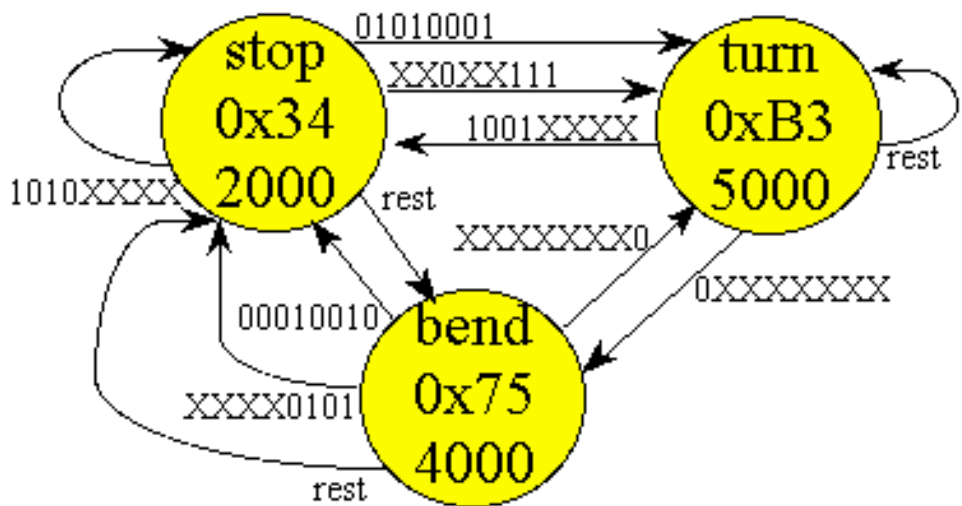


Figure 10-6: Finite State Machine (same as Figure 9-1)

Compare the following implementation to [Listing 9-2](#), and see that the **unsigned char Out;** constant is replaced with a **void (*CmdPt)(void);** function pointer. The three general function **DoStop()** **DoTurn()** and **DoBend()** are also added.

```
const struct State{
    void (*CmdPt)(void);    /* function to execute */
    unsigned int Wait;      /* Time (E cycles) to wait */
    unsigned char AndMask[4];
    unsigned char EquMask[4];
    const struct State *Next[4];}; /* Next states */
typedef const struct State StateType;
typedef StateType * StatePtr;
#define stop &fsm[0]
#define turn &fsm[1]
#define bend &fsm[2]
void DoStop(void){ PORTH=0x34;}
void DoTurn(void){ PORTH=0xB3;}
void DoBend(void){ PORTH=0x75;}
StateType fsm[3]={
    {&DoStop, 2000,    // stop 1 ms
     {0xFF,  0xF0,  0x27,  0x00},
     {0x51,  0xA0,  0x07,  0x00},
     {turn,  stop,  turn,  bend}},
    {&DoTurn, 5000,   // turn 2.5 ms
     {0x80,  0xF0,  0x00,  0x00},
     {0x00,  0x90,  0x00,  0x00},
     {bend,  stop,  turn,  turn}},
    {&DoBend, 4000,   // bend 2 ms
     {0xFF,  0x0F,  0x01,  0x00},
     {0x12,  0x05,  0x00,  0x00},
     {stop,  stop,  turn,  stop}}};
```

Listing 10-12: Linked finite state machine structure stored in ROM

Compare the following implementation to [Listing 9-4](#), and see that the **PORTH=pt-Out;** assignment is replaced with a **(*Pt->CmdPt)();** function call. In this way, the appropriate function **DoStop()** **DoTurn()** or **DoBend()** will be called.

```
void control(void){ StatePtr Pt;
    unsigned char Input; int Endt; unsigned int i;
    TSCR |=0x80;    // TEN(enable)
    TMSK2=0xA2;    // TOI arm, TPU(pullup) timer/4 (500ns)
    DDRH=0xFF;     // Porth bits 7-0 are outputs
```

```

DDRJ=0x00;      // PortJ bits 7-0 are inputs
PUPSJ=0xFF;    // Pullups J7-J0
PULEJ=0xFF;    // Enable pull up/down PortJ
Pt=stop;       // Initial State
while(1){
    (*Pt->CmdPt)();      // 1) execute function
    Endt=TCNT+Pt->Wait;  // Time (500 ns each) to wait
    while(Endt-TCNT>0); // 2) wait
    Input=PORTJ;        // 3) input
    for(i=0;i<4;i++){
        if((Input&Pt->AndMask[i])==Pt->EquMask[i]){
            Pt=Pt->Next[i]; // 4) next depends on input
            i=4; } } }

```

Listing 10-13: Finite state machine controller for MC68HC812A4

Linked List Interpreter using Function Pointers

In the next example, function pointers are stored in a linked-list. An interpreter accepts ASCII input from a keyboard and scans the list for a match. In this implementation, each node in the linked list has a function to be executed when the operator types the corresponding letter. The linked list **LL** has three nodes. Each node has a letter, a function and a link to the next node.

```

// Linked List Interpreter
const struct Node{
    unsigned char Letter;
    void (*fnctPt)(void);
    const struct Node *Next;};
typedef const struct Node NodeType;
typedef NodeType * NodePtr;
void CommandA(void){
    OutString("\nExecuting Command a");
}
void CommandB(void){
    OutString("\nExecuting Command b");
}
void CommandC(void){
    OutString("\nExecuting Command c");
}
NodeType LL[3]={
    { 'a', &CommandA, &LL[1] },
    { 'b', &CommandB, &LL[2] },
    { 'c', &CommandC, 0 } };
void main(void){ NodePtr Pt; char string[40];
    InitSCI(); // Enable SCI port
    TSCR |=0x80; // TEN(enable)
    TMSK2=0xA2; // TOI arm, TPU(pullup) timer/4 (500ns)
    OutString("\nEnter a single letter command followed by <enter>");

```

```

while(1){
    OutString("\n>");
    InString(string,39); // first character is interpreted
    Pt=&LL[0]; // first node to check
    while(Pt){
        if(string[0]==Pt->Letter){
            Pt->fnctPt(); // execute function
            break;} // leave while loop
        else{
            Pt=Pt->Next;
            if(Pt==0) OutString(" Error");}}}}

```

Listing 10-14: Linked list implementation of an interpreter.

Compare the syntax of the function call, **(*Pt->CmdPt)()**, in [Listing 10-13](#), with the syntax in this example, **Pt->fnctPt()**. In the Imagecraft compilers, these two expressions both generate code that executes the function.

Go to [Chapter 11 on Preprocessor Directives](#) Return to [Table of Contents](#)

Chapter 5: Expressions

What's in Chapter 5?

[Precedence and associativity](#)

[Unary operators](#)

[Binary operators](#)

[Assignment operators](#)

[Expression type and explicit casting](#)

[Selection operator](#)

[Arithmetic overflow and underflow](#)

Most programming languages support the traditional concept of an expression as a combination of constants, variables, array elements, and function calls joined by various operators (+, -, etc.) to produce a single numeric value. Each operator is applied to one or two operands (the values operated on) to produce a single value which may itself be an operand for another operator. This idea is generalized in C by including nontraditional data types and a rich set of operators. Pointers, unsubscripted array names, and function names are allowed as operands. And, as Tables 5-1 through 5-6 illustrate, many operators are available. All of these operators can be combined in any useful manner in an expression. As a result, C allows the writing very compact and efficient expressions which at first glance may seem a bit strange. Another unusual feature of C is that anywhere the syntax calls for an expression, a list of expressions, with comma separators, may appear.

Precedence and associativity

The basic problem in evaluating expressions is deciding which parts of an expression are to be associated with which operators. To eliminate ambiguity, operators are given three properties: *operand count*, *precedence*, and *associativity*.

Operand count refers to the classification of operators as unary, binary, or ternary according to whether they operate on one, two, or three operands. The unary minus sign, for instance, reverses the sign of the following operand, whereas the binary minus sign subtracts one operand from another.

The following example converts the distance x in inches to a distance y in cm. Without parentheses the following statement seems ambiguous

$$y=254*x/100;$$

If we divide first, then y can only take on values that are multiples of 254 (e.g., 0 254 508 etc.) So the following statement is incorrect.

$$y=254*(x/100);$$

The proper approach is to multiply first then divide. To multiply first we must guarantee that the

product $254*x$ will not overflow the precision of the computer. How do we know what precision the compiler used for the intermediate result $254*x$? To answer this question, we must observe the assembly code generated by the compiler. Since multiplication and division associate left to right, the first statement without parentheses although ambiguous will actually calculate the correct answer. It is good programming style to use parentheses to clarify the expression. So this last statement has both good style and proper calculation.

```
y = ( 254*x ) / 100 ;
```

The issues of [precedence and associativity](#) were explained in Chapter 1. Precedence defines the evaluation order. For example the expression $3+4*2$ will be 11 because multiplication has precedence over addition. Associativity determines the order of execution for operators that have the same precedence. For example, the expression $10-3-2$ will be 5, because subtraction associates left to right. On the other hand, if x and y are initially 10, then the expression $x+=y+=1$ will first make $y=y+1$ (11), then make $x=x+y$ (21) because the operator $+=$ associates right to left. The table from chapter 1 is repeated for your convenience.

Precedence	Operators	Associativity
highest	() [] . -> ++(postfix) --(postfix) ++(prefix) --(prefix) !~ sizeof(type) +(unary) - (unary) &(address) *(dereference)	left to right right to left
	* / %	left to right
	+ -	left to right
	<< >>	left to right
	< <= > >=	left to right
	== !=	left to right
	&	left to right
	^	left to right
		left to right
	&&	left to right
		left to right
	? :	right to left
	= += -= *= /= %= <<= >>= = &=	right to left
	^=	
lowest	,	left to right

Table 1-4: Precedence and associativity determine the order of operation

Unary operators

We begin with the unary operators, which take a single input and give a single output. In the following examples, assume all numbers are 16 bit signed (short). The following variables are listed

```
short data; /* -32767 to +32767 */
short *pt; /* pointer to memory */
```

```
short flag; /* 0 is false, not zero is true */
```

operator	meaning	example	result
~	binary complement	~0x1234	0xEDCB
!	logical complement	!flag	flip 0 to 1 and not zero to 0
&	address of	&data	address in memory where data is stored
-	negate	-100	negative 100
+	positive	+100	100
++	preincrement	++data	data=data+1, then result is data
--	predecrement	--data	data=data-1, then result is data
*	reference	*pt	16 bit information pointed to by pt

Table 5.1: Unary prefix operators.

operator	meaning	example	result
++	postincrement	data++	result is data, then data=data+1
--	postdecrement	data--	result is data, then data=data-1

Table 5.2: Unary postfix operators.

Binary operators

Next we list the binary arithmetic operators, which operate on two number inputs giving a single number result. The operations of addition, subtraction and shift left are the same independent of whether the numbers are signed or unsigned. As we will see later, [overflow and underflow](#) after an addition, subtraction and shift left are different for signed and unsigned numbers, but the operation itself is the same. On the other hand multiplication, division, and shift right have different functions depending on whether the numbers are signed or unsigned. It will be important, therefore, to avoid multiplying or dividing an unsigned number with a signed number.

operator	meaning	example	result
+	addition	100+300	400
-	subtraction	100-300	-200
*	multiplication	10*300	3000
/	division	123/10	12
%	remainder	123%10	3
<<	shift left	102<<2	408
>>	shift right	102>>2	25

Table 5.3: Binary arithmetic operators.

The binary bitwise logical operators take two inputs and give a single result.

operator	meaning	example	result
&	bitwise and	0x1234&0x00FF	0x0034
	bitwise or	0x1234 0x00FF	0x12FF
^	bitwise exclusive or	0x1234^0x00FF	0x12CB

Table 5.4: Binary bitwise logical operators.

The binary boolean operators take two boolean inputs and give a single boolean result.

operator	meaning	example	result
&&	and	0 && 1	0 (false)
	or	0 1	1 (true)

Table 5.5: Binary boolean operators.

Many programmers confuse the logical operators with the boolean operators. Logical operators take two numbers and perform a bitwise logical operation. Boolean operators take two boolean inputs (0 and notzero) and return a boolean (0 or 1). In the program below, the operation `c=a&b;` will perform a *bitwise logical and* of 0x0F0F **and** 0xF0F0 resulting in 0x0000. In the `d=a&&b;` expression, the value a is considered as a true (because it is not zero) and the value b also is considered a true (not zero). The boolean operation of true **and** true gives a true result (1).

```
int a,b,c,d;
void main(void){ a=0x0F0F; b=F0F0;
    c=a&b; /* logical result c will be 0x0000 */
    d=a&&b; /* boolean result d will be 1 (true) */
}
```

Listing 5-1: Illustration of the difference between logical and boolean operators

The binary relational operators take two number inputs and give a single boolean result.

operator	meaning	example	result
==	equal	100 == 200	0 (false)
!=	not equal	100 != 200	1 (true)
<	less than	100 < 200	1 (true)

<=	less than or equal	100 <= 200	1 (true)
>	greater than	100 > 200	0 (false)
>=	greater than or equal	100 >= 200	0 (false)

Table 5.6: Binary relational operators.

Some programmers confuse *assignment equals* with the *relational equals*. In the following example, the first **if** will execute the `subfunction()` if `a` is equal to zero (`a` is not modified). In the second case, the variable `b` is set to zero, and the `subfunction()` will never be executed because the result of the equals assignment is the value (in this case the 0 means false).

```
int a,b;
void program(void){
    if(a==0) subfunction(); /* execute subfunction if a is zero */
    if(b=0) subfunction(); /* set b to zero, never execute
subfunction */
}
```

Listing 5-2: Illustration of the difference between relational and assignment equals

Before looking at the kinds of expressions we can write in C, we will first consider the process of evaluating expressions and some general properties of operators.

Assignment Operators

The assignment operator is used to store data into variables. The syntax is `variable=expression;` where `variable` has been previously defined. At run time, the result of the expression is saved into the variable. If the type of the expression is different from the variable, then the result is automatically converted. For more information about types and conversion, see [expression type and explicit casting](#). The assignment operation itself has a result, so the assignment operation can be nested.

```
int a,b;
void initialize(void){
    a=b=0; /* set both variables to zero */
}
```

Listing 5-3: Example of a nested assignment operation

The read/modify write assignment operators are convenient. Examples are shown below.

```
int a,b;
void initialize(void){
    a+=b; /* same as a=a+b */
    a-=b; /* same as a=a-b */
    a*=b; /* same as a=a*b */
    a/=b; /* same as a=a/b */
    a%=b; /* same as a=a%b */
}
```

```

a<<=b; /* same as a=a<<b */
a<<=b; /* same as a=a<<b */
a>>=b; /* same as a=a>>b */
a|=b; /* same as a=a|b */
a&=b; /* same as a=a&b */
a^=b; /* same as a=a^b */
}

```

Listing 5-4 List of all read/modify/write assignment operations

Most compilers will produce the same code for the short and long version of the operation. Therefore you should use the read/modify/write operations only in situations that make the software easier to understand.

```

void function(void){
    PORTA|=0x01; /* set PA0 high */
    PORTB&=~0x80; /* clear PB7 low */
    PORTC^=0x40; /* toggle PC6 */
}

```

Listing 5-5 Good examples of read/modify/write assignment operations

Expression Types and Explicit Casting

We saw earlier that numbers are represented in the computer using a wide range of formats. A list of these formats is given in Table 5.7. Notice that for the 6811 and 6812, the **int** and **short** types are the same. On the other hand with the Intel Pentium, the **int** and **long** types are the same. This difference may cause confusion, when porting code from one system to another. I suggest you use the **int** type when you are interested in efficiency and don't care about precision, and use the **short** type when you want a variable with a 16-bit precision.

<u>type</u>	<u>range</u>	<u>precision</u>	<u>example variable</u>
unsigned char	0 to 255	8 bits	unsigned char uc;
char	-127 to 127	8 bits	char sc;
unsigned int	0 to 65535U	16 bits	unsigned int ui;
int	-32767 to 32767	16 bits	int si;
unsigned short	0 to 65535U	16 bits	unsigned short us;
short	-32767 to 32767	16 bits	short ss;
long	-2147483647L to 2147483647L	32 bits	long sl;

Table 5-7. Available number formats for the ICC11/ICC12/Hiware compilers

An obvious question arises, what happens when two numbers of different types are operated on? Before operation, the C compiler will first convert one or both numbers so they have the same type. The conversion of one type into another has many names:
 automatic conversion,

implicit conversion,
coercion,
promotion, or
widening.

There are three ways to consider this issue. The first way to think about this is if the range of one type completely fits within the range of the other, then the number with the smaller range is converted (promoted) to the type of the number with the larger range. In the following examples, a number of type1 is added to a number of type2. In each case, the number range of type1 fits into the range of type2, so the parameter of type1 is first promoted to type2 before the addition.

<u>type1</u>		<u>type2</u>	<u>example</u>
unsigned char	fits inside	unsigned short	uc+us is of type unsigned short
unsigned char	fits inside	short	uc+ss is of type short
unsigned char	fits inside	long	uc+sl is of type long
char	fits inside	short	sc+ss is of type short
char	fits inside	long	sc+sl is of type long
unsigned short	fits inside	long	us+sl is of type long
short	fits inside	long	ss+sl is of type long

Table 5-8. When the range of one type fits inside the range of another, then conversion is simple

The second way to consider mixed precision operations is that in most cases the compiler will promote the number with the smaller precision into the other type before operation. If the two numbers are of the same precision, then the signed number is converted to unsigned. These automatic conversions may not yield correct results. The third and best way to deal with mixed type operations is to perform the conversions explicitly using the **cast** operation. We can force the type of an expression by explicitly defining its type. This approach allows the programmer to explicitly choose the type of the operation. Consider the following digital filter with mixed type operations. In this example, we explicitly convert x and y to signed 16 bit numbers and perform 16 bit signed arithmetic. Note that the assignment of the result into y, will require a demotion of the 16 bit signed number into 8 bit signed. Unfortunately, C does not provide any simple mechanisms for error detection/correction (see [overflow and underflow.](#))

```
char y; // output of the filter
unsigned char x; // input of the filter
void filter(void){
    y = (12*(short)x + 56*(short)y)/100;
}
```

Listing 5-6: Examples of the selection operator

We apply an explicit cast simply by preceding the number or expression with parentheses surrounding the type. In this next digital filter all numbers are of the same type. Even so, we are

worried that the intermediate result of the multiplications and additions might overflow the 16-bit arithmetic. We know from digital signal processing that the final result will always fit into the 16-bit variable. For more information on the design and analysis of digital filters, see Chapter 15 of [Embedded Microcomputer Systems: Real Time Interfacing](#) by Jonathan W. Valvano. In this example, the cast `(long)` will specify the calculations be performed in 32-bit precision.

```
// y(n) = [113*x(n) + 113*x(n-2) - 98*y(n-2)]/128, channel specifies
the A/D channel
short x[3],y[3]; // MACQs containing current and previous
#define OC5 0x20
#pragma interrupt_handler TOC5handler()
void TOC5handler(void){
TFLG1=OC5; // ack OC5F
TC5=TC5+8333; // fs=240Hz
y[2]=y[1]; y[1]=y[0]; // shift MACQ
x[2]=x[1]; x[1]=x[0];
x[0] = A2D(channel); // new data
y[0]=(113*((long)x[0]+(long)x[2])-98*(long)y[2])>>7;}
```

Listing 5-7: We can use a cast to force higher precision arithmetic

We saw in [Chapter 1](#), casting was used to assign a symbolic name to an I/O port. In particular the following define casts the number `0x0000` as a pointer type, which points to an unsigned 8 bit data. More about pointers can be found in [Chapter 7](#).

```
#define PORTA *(unsigned char volatile*)(0x0000)
```

Selection operator

The selection operator takes three input parameters and yields one output result. The format is

$$\text{Expr1} \text{ ? Expr2 : Expr3}$$

The first input parameter is an expression, `Expr1`, which yields a boolean (0 for false, not zero for true). `Expr2` and `Expr3` return values that are regular numbers. The selection operator will return the result of `Expr2` if the value of `Expr1` is true, and will return the result of `Expr3` if the value of `Expr1` is false. The type of the expression is determined by the types of `Expr2` and `Expr3`. If `Expr2` and `Expr3` have different types, then the usual promotion is applied. The resulting type is determined at compile time, in a similar manner as the `Expr2+Expr3` operation, and not at run time depending on the value of `Expr1`. The following two subroutines have identical functions.

```
int a,b;
```

```

void sub1(void){
    a = (b==1) ? 10 : 1;
}
void sub2(void){
    if(b==1)
        a=10;
    else
        a=1;
}

```

Listing 5-8: Examples of the selection operator

Arithmetic Overflow and Underflow

An important issue when performing arithmetic calculations on integer values is the problem of underflow and overflow. Arithmetic operations include addition, subtraction, multiplication, division and shifting. Overflow and underflow errors can occur during all of these operations. In assembly language the programmer is warned that an error has occurred because the processor will set condition code bits after each of these operations. Unfortunately, the C compiler provides no direct access to these error codes, so we must develop careful strategies for dealing with overflow and underflow. It is important to remember that arithmetic operations (addition, subtraction, multiplication, division, and shifting) have constraints when performed with finite precision on a microcomputer. An overflow error occurs when the result of an arithmetic operation can not fit into the finite precision of the result. We will study addition and subtraction operations in detail, but the techniques for dealing with overflow and underflow will apply to the other arithmetic operations as well. We will consider two approaches

- avoiding the error
- detecting the error then correcting the result

For example when two 8 bit numbers are added, the sum may not fit back into the 8 bit result. We saw earlier that the same digital hardware (instructions) could be used to add and subtract unsigned and signed numbers. Unfortunately, we will have to design separate overflow detection for signed and unsigned addition and subtraction.

All microcomputers have a condition code register which contain bits which specify the status of the most recent operation. In this section, we will introduce 4 condition code bits common to most microcomputers. If the two inputs to an addition or subtraction operation are considered as unsigned, then the C bit (carry) will be set if the result does not fit. In other words, after an unsigned addition, the C bit is set if the answer is wrong. If the two inputs to an addition or subtraction operation are considered as signed, then the V bit (overflow) will be set if the result does not fit. In other words, after a signed addition, the V bit is set if the answer is wrong. The Motorola 6805 does not have a V bit, therefore it will be difficult to check for errors after an operation on signed numbers.

bit	name	meaning after addition or subtraction
N	negative	result is negative
Z	zero	result is zero
V	overflow	signed overflow
C	carry	unsigned overflow

Table 5.9. Condition code bits contain the status of the previous arithmetic or logical operation.

For an 8 bit unsigned number, there are only 256 possible values, 0 to 255. We can think of the numbers as positions along a circle. There is a discontinuity at the 0|255 interface, everywhere else adjacent numbers differ by ± 1 . If we add two unsigned numbers, we start at the position of the first number a move in a clockwise direction the number of steps equal to the second number. For example, if $96+64$ is performed in 8 bit unsigned precision, the correct result of 160 is obtained. In this case, the carry bit will be 0 signifying the answer is correct. On the other hand, if $224+64$ is performed in 8 bit unsigned precision, the incorrect result of 32 is obtained. In this case, the carry bit will be 1, signifying the answer is wrong.

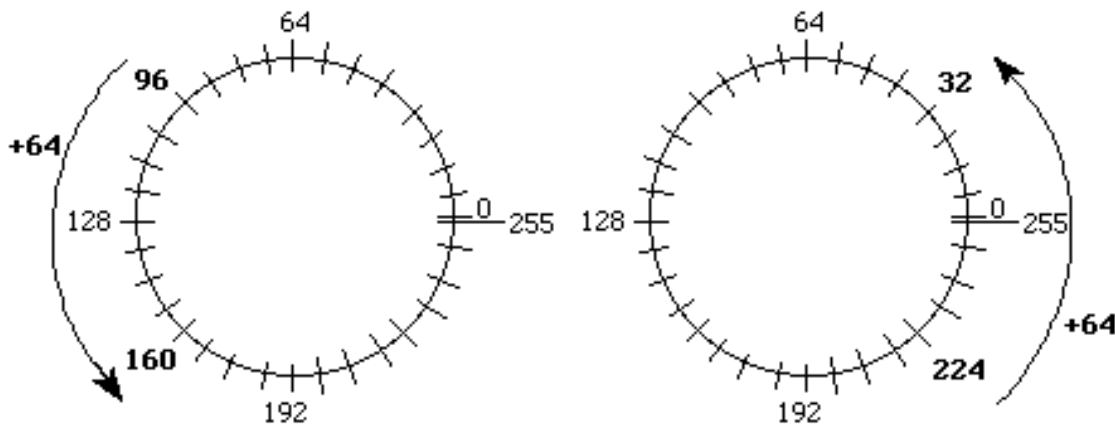


Figure 5-1: 8 bit unsigned addition.

For subtraction, we start at the position of the first number a move in a counterclockwise direction the number of steps equal to the second number. For example, if $160-64$ is performed in 8 bit unsigned precision, the correct result of 96 is obtained (carry bit will be 0.) On the other hand, if $32-64$ is performed in 8 bit unsigned precision, the incorrect result of 224 is obtained (carry bit will be 1.)

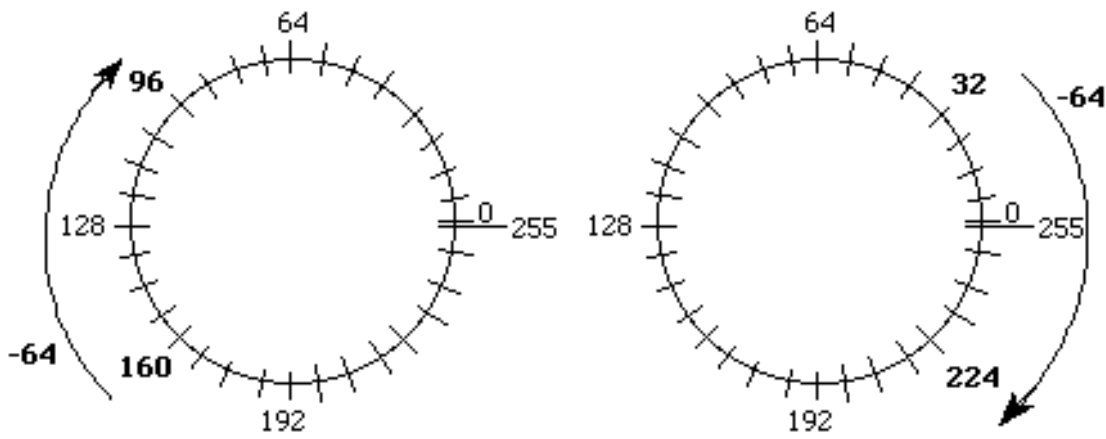


Figure 5-2: 8 bit unsigned subtraction.

In general, we see that the carry bit is set when we cross over from 255 to 0 while adding or cross over from 0 to 255 while subtracting.

Observation: The carry bit, C , is set after an unsigned add or subtract when the result is incorrect.

For an 8 bit signed number, the possible values range from -128 to 127. Again there is a discontinuity, but this time it exists at the -128|127 interface, everywhere else adjacent numbers differ by ± 1 . The meanings of the numbers with bit 7=1 are different from unsigned, but we add and subtract signed numbers on the number wheel in a similar way (e.g., addition of a positive number moves clockwise.) Adding a negative number is the same as subtracting a positive number hence this operation would cause a counterclockwise motion. For example, if $-32+64$ is performed, the correct result of 32 is obtained. In this case, the overflow bit will be 0 signifying the answer is correct. On the other hand, if $96+64$ is performed, the incorrect result of -96 is obtained. In this case, the overflow bit will be 1 signifying the answer is wrong.

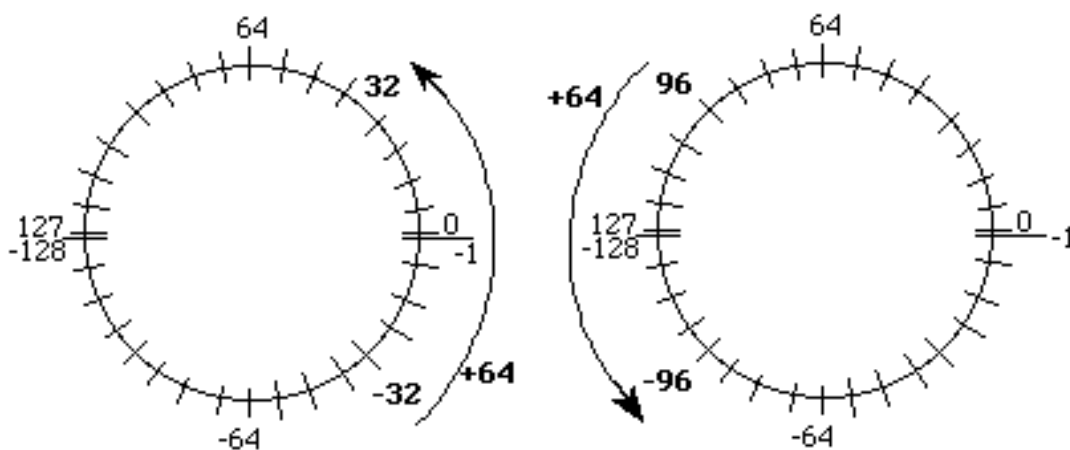


Figure 5-3: 8 bit signed addition.

For subtracting signed numbers, we again move in a counterclockwise direction. Subtracting a negative number is the same as adding a positive number hence this operation would cause a clockwise motion. For example, if $32-64$ is performed, the correct result of -32 is obtained (overflow

bit will be 0.) On the other hand, if $-96-64$ is performed, the incorrect result of 96 is obtained (overflow bit will be 1.)

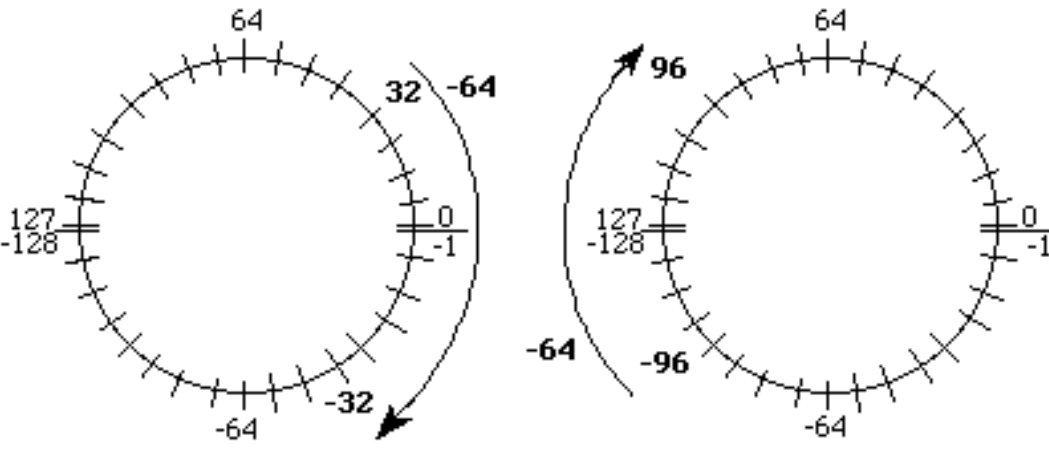


Figure 5-4: 8 bit signed subtraction.

In general, we see that the overflow bit is set when we cross over from 127 to -128 while adding or cross over from -128 to 127 while subtracting.

Observation: The overflow bit, *V*, is set after a signed add or subtract when the result is incorrect.

Another way to determine the overflow bit after an addition is to consider the carry out of bit 6. The *V* bit will be set of there is a carry out of bit 6 (into bit 7) but no carry out of bit 7 (into the *C* bit). It is also set if there is no carry out of bit 6 but there is a carry out of bit 7. Let $X_7, X_6, X_5, X_4, X_3, X_2, X_1, X_0$ and $M_7, M_6, M_5, M_4, M_3, M_2, M_1, M_0$ be the individual binary bits of the two 8 bit numbers which are to be added, and let $R_7, R_6, R_5, R_4, R_3, R_2, R_1, R_0$ be individual binary bits of the 8 bit sum. Then, the 4 condition code bits after an addition are shown in Table 5.10.

N	R_7	if unsigned result above 127, if signed result is negative
Z	$\overline{R_7 \cdot R_6 \cdot R_5 \cdot R_4 \cdot R_3 \cdot R_2 \cdot R_1 \cdot R_0}$	result is zero
V	$X_7 \cdot M_7 \cdot \overline{R_7}$ $+ \overline{X_7} \cdot \overline{M_7} \cdot R_7$	add two positives got a negative result or add two negatives got a positive result
C	$X_7 \cdot M_7$ $+ M_7 \cdot \overline{R_7}$ $+ \overline{X_7} \cdot R_7$	add two numbers both above 127 or add one number above 127 and got a number below 128 or add one number above 127 and got a number below 128

Table 5.10. Condition code bits after an 8 bit addition operation.

Let the result *R* be the result of the subtraction $X-M$. Then, the 4 condition code bits are shown in Table 5.11.

N	R7	if unsigned result above 127, if signed result is negative
Z	$\overline{R7 \cdot R6 \cdot R5 \cdot R4 \cdot R3 \cdot R2 \cdot R1 \cdot R0}$	result is zero
V	$X7 \cdot \overline{M7} \cdot \overline{R7}$ $+ \overline{X7} \cdot M7 \cdot R7$	a positive minus a negative and got a positive result or a negative minus a positive and got a negative result
C	$\overline{X7} \cdot M7$ $+ M7 \cdot R7$ $+ \overline{X7} \cdot R7$	a number below 128 minus a number above 127 or subtracted a number above 127 and got one above 127 or started with a number below 127 and got one above 127

Table 5-11. Condition code bits after an 8 bit subtraction operation.

Common Error: Ignoring overflow (signed or unsigned) can result in significant errors.

Observation: Microcomputers have two sets of conditional branch instructions (if statements) which make program decisions based on either the C or V bit.

Common Error: An error will occur if you unsigned conditional branch instructions (if statements) after operating on signed numbers, and vice-versa.

There are some applications where arithmetic errors are not possible. For example if we had two 8 bit unsigned numbers that we knew were in the range of 0 to 100, then no overflow is possible when they are added together.

Typically the numbers we are processing are either signed or unsigned (but not both), so we need only consider the corresponding C or V bit (but not both the C and V bits at the same time.) In other words, if the two numbers are unsigned, then we look at the C bit and ignore the V bit. Conversely, if the two numbers are signed, then we look at the V bit and ignore the C bit. There are two appropriate mechanisms to deal with the potential for arithmetic errors when adding and subtracting. The first mechanism, used by most compilers, is called promotion. Promotion involves increasing the precision of the input numbers, and performing the operation at that higher precision. An error can still occur if the result is stored back into the smaller precision. Fortunately, the program has the ability to test the intermediate result to see if it will fit into the smaller precision. To promote an unsigned number we add zero's to the left side. In a previous example, we added the unsigned 8 bit 224 to 64, and got the wrong result of 32. With promotion we first convert the two 8 bit numbers to 16 bits, then add.

We can check the 16 bit intermediate result (e.g., 228) to see if the answer will fit back into the 8 bit result. In the following flowchart, X and M are 8 bit unsigned inputs, X16, M16, and R16 are 16 bit intermediate values, and R is an 8 bit unsigned output. The oval symbol represents the entry and exit points, the rectangle is used for calculations, and the diamond shows a decision. Later in the book we will use parallelograms and trapezoids to perform input/output functions.

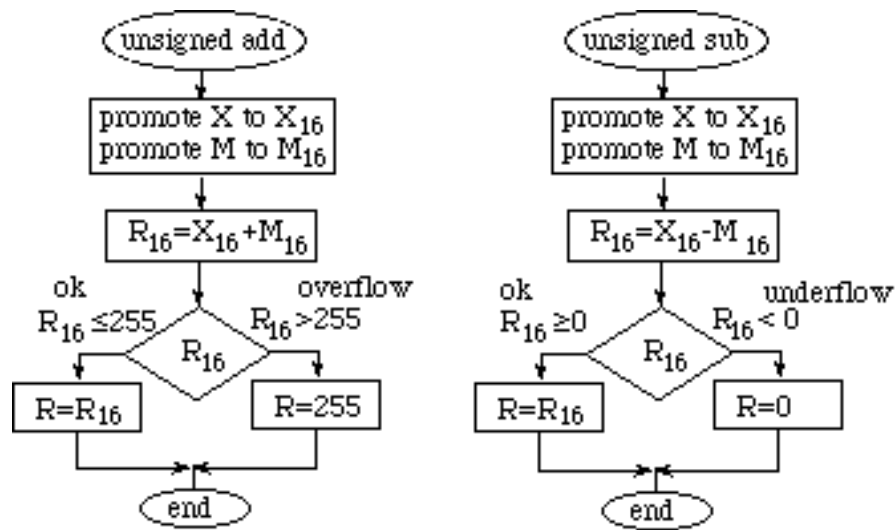


Figure 5-5: Promotion can be used to avoid overflow and underflow.

To promote a signed number, we duplicate the sign bit as we add binary digits to the left side. Earlier, we performed the 8 bit signed operation $-96-64$ and got a signed overflow. With promotion we first convert the two numbers to 16 bits, then subtract.

decimal	8 bit	16 bit
-96	1010,0000	1111,1111,1010,0000
-64	-0100,0000	-0000,0000,0100,0000
-160	0110,0000	1111,1111,0110,0000

We can check the 16 bit intermediate result (e.g., -160) to see if the answer will fit back into the 8 bit result. In the following flowchart, X and M are 8 bit signed inputs, X16, M16, and R16 are 16 bit signed intermediate values, and R is an 8 bit signed output.

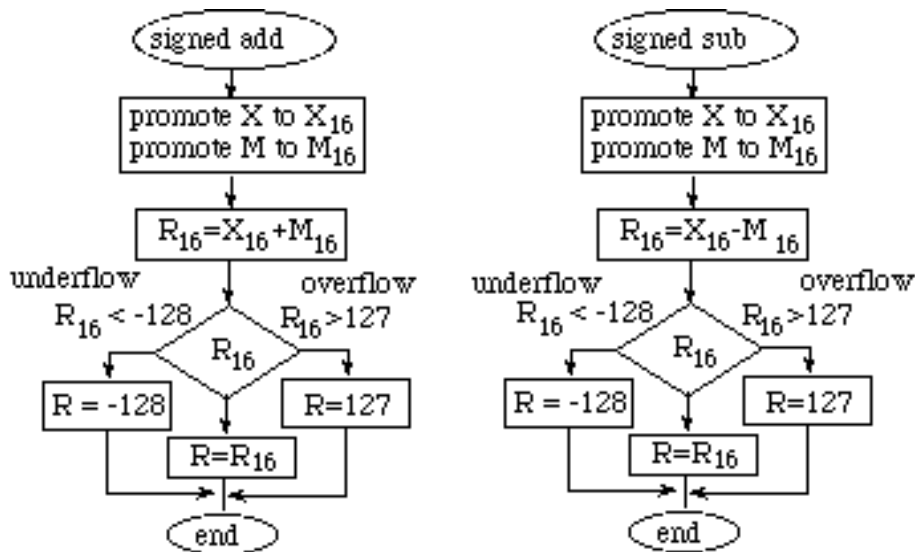


Figure 5-6: Promotion can be used to avoid overflow and underflow.

The other mechanism for handling addition and subtraction errors is called ceiling and floor. It is analogous to movements inside a room. If we try to move up (add a positive number or subtract a negative number) the ceiling will prevent us from exceeding the bounds of the room. Similarly, if we try to move down (subtract a positive number or add a negative number) the floor will prevent us from going too low. For our 8 bit addition and subtraction, we will prevent the 0 to 255 and 255 to 0 crossovers for unsigned operations and -128 to +127 and +127 to -128 crossovers for signed operations. These operations are described by the following flowcharts. If the carry bit is set after an unsigned addition the result is adjusted to the largest possible unsigned number (ceiling). If the carry bit is set after an unsigned subtraction, the result is adjusted to the smallest possible unsigned number (floor.)

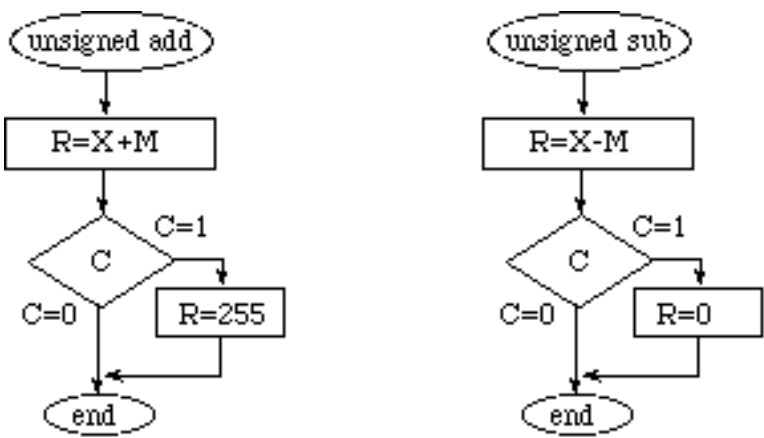


Figure 5-7: In assembly language we can detect overflow and underflow.

If the overflow bit is set after a signed operation the result is adjusted to the largest (ceiling) or smallest (floor) possible signed number depending on whether it was a -128 to 127 cross over (N=0) or 127 to -128 cross over (N=1). Notice that after a signed overflow, bit 7 of the result is always wrong because there was a cross over.

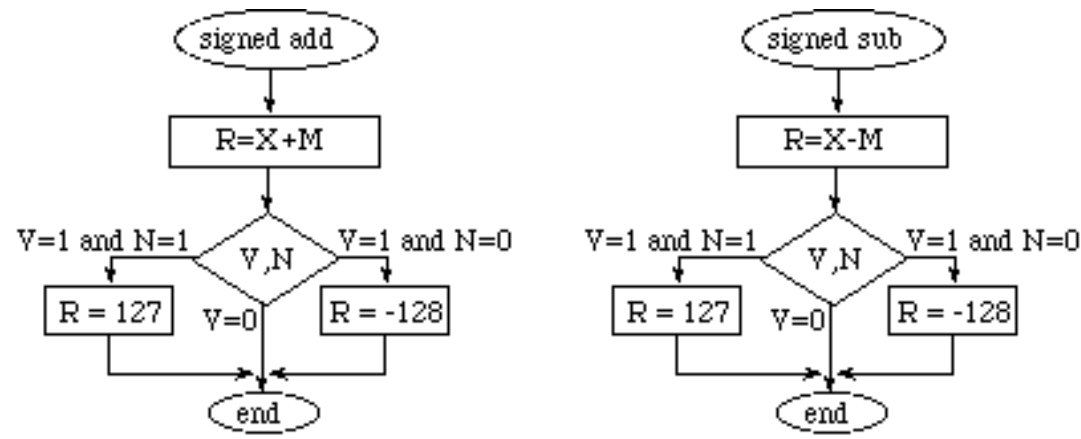


Figure 5-8: In assembly language we can detect overflow and underflow.

Chapter 6: Flow of Control

What's in Chapter 6?

[Simple statements](#)

[Compound statements](#)

[if and if-else statements](#)

[switch statements](#)

[while statements](#)

[for statements](#)

[do statements](#)

[return statements](#)

[goto statements](#)

[Null statements](#)

[Missing statements](#)

Every procedural language provides statements for determining the flow of control within programs. Although declarations are a type of statement, in C the unqualified word statement usually refers to procedural statements rather than declarations. In this chapter we are concerned only with procedural statements.

In the C language, statements can be written only within the body of a function; more specifically, only within compound statements. The normal flow of control among statements is sequential, proceeding from one statement to the next. However, as we shall see, most of the statements in C are designed to alter this sequential flow so that algorithms of arbitrary complexity can be implemented. This is done with statements that control whether or not other statements execute and, if so, how many times. Furthermore, the ability to write compound statements permits the writing a sequence of statements wherever a single, possibly controlled, statement is allowed. These two features provide the necessary generality to implement any algorithm, and to do it in a structured way.

Simple Statements

The C language uses semicolons as statement terminators. A semicolon follows every simple (non-compound) statement, even the last one in a sequence.

When one statement controls other statements, a terminator is applied only to the controlled statements. Thus we would write

```
if(x > 5) x = 0; else ++x;
```

with two semicolons, not three. Perhaps one good way to remember this is to think of statements that control other statements as "super" statements that "contain" ordinary (simple and compound) statements. Then remember that only simple statements are terminated. This implies, as stated above,

that compound statements are not terminated with semicolons. Thus

```
while(x < 5) {func(); ++x;}
```

is perfectly correct. Notice that each of the simple statements within the compound statement is terminated.

Compound Statements

The terms compound statement and block both refer to a collection of statements that are enclosed in braces to form a single unit. Compound statements have the form

```
{ObjectDeclaration?... Statement?... }
```

ObjectDeclaration?... is an optional set of local declarations. If present, C requires that they precede the statements; in other words, they must be written at the head of the block. Statement?... is a series of zero or more simple or compound statements. Notice that there is not a semicolon at the end of a block; the closing brace suffices to delimit the end. In this example the local variable `temp` is only defined within the inner compound statement.

```
void main(void){ int n1,n2;
    n1=1; n2=2;
    { int temp;
        temp=n1; n1=n2; n2=temp; /* switch n1,n2 */
    }
}
```

Listing 6.1: Examples of a compound statements

The power of compound statements derives from the fact that one may be placed anywhere the syntax calls for a statement. Thus any statement that controls other statements is able to control units of logic of any complexity.

When control passes into a compound statement, two things happen. First, space is reserved on the stack for the storage of local variables that are declared at the head of the block. Then the executable statements are processed.

One important limitation in C is that a block containing local declarations must be entered through its leading brace. This is because bypassing the head of a block effectively skips the logic that reserves space for local objects. Since the `goto` and `switch` statements (below) could violate this rule.

The If Statement

If statements provide a non-iterative choice between alternate paths based on specified conditions. They have either of two forms

```
if ( ExpressionList ) Statement1
```

or

```
if ( ExpressionList ) Statement1  
else Statement2
```

ExpressionList is a list of one or more expressions and Statement is any simple or compound statement. First, ExpressionList is evaluated and tested. If more than one expression is given, they are evaluated from left to right and the right-most expression is tested. If the result is true (non-zero), then the Statement1 is executed and the Statement2 (if present) is skipped. If it is false (zero), then Statement1 is skipped and Statement2 (if present) is executed. In this first example, the function `isGreater()` is executed if `G2` is larger than 100.

```
if(G2 > 100) isGreater();
```

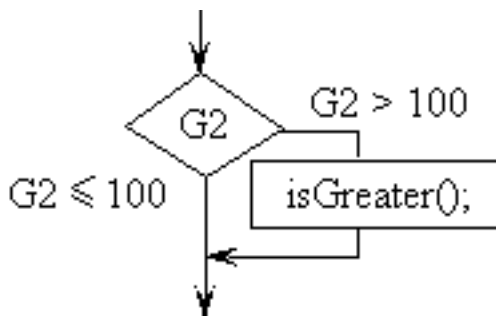


Figure 6.1: Example if statement.

A 3-wide median filter can be designed using if-else conditional statements.

```
int Median(int u1,int u2,int u3){ int result;  
    if(u1>u2)  
        if(u2>u3)    result=u2;    // u1>u2,u2>u3        u1>u2>u3  
        else  
            if(u1>u3) result=u3;    // u1>u2,u3>u2,u1>u3    u1>u3>u2  
            else    result=u1;    // u1>u2,u3>u2,u3>u1    u3>u1>u2  
    else  
        if(u3>u2)    result=u2;    // u2>u1,u3>u2        u3>u2>u1  
        else  
            if(u1>u3) result=u3;    // u2>u1,u2>u3,u1>u3    u2>u1>u3  
            else    result=u1;    // u2>u1,u2>u3,u3>u1    u2>u3>u1
```

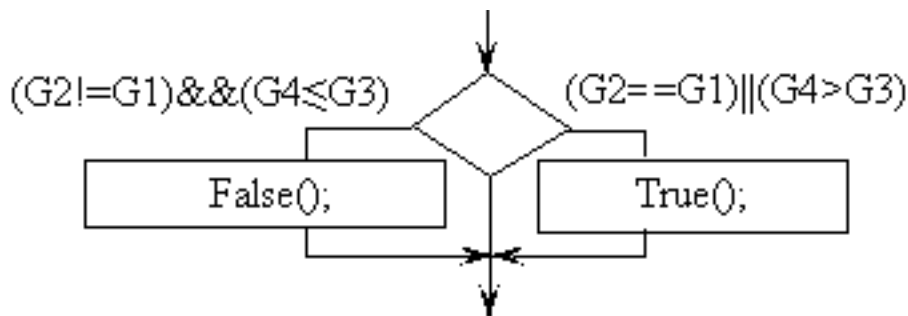
```
return(result):}
```

Listing 6.2: A 3-wide median function.

For more information on the design and analysis of digital filters, see Chapter 15 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano.

Complex conditional testing can be implemented using the [relational](#) and [boolean](#) operators described in the last chapter.

```
if ((G2==G1)|| (G4>G3)) True(); else False();
```



The Switch Statement

Switch statements provide a non-iterative choice between any number of paths based on specified conditions. They compare an expression to a set of constant values. Selected statements are then executed depending on which value, if any, matches the expression. Switch statements have the form

```
switch ( ExpressionList ) { Statement?... }
```

where ExpressionList is a list of one or more expressions. Statement?... represents the statements to be selected for execution. They are selected by means of case and default prefixes--special labels that are used only within switch statements. These prefixes locate points to which control jumps depending on the value of ExpressionList. They are to the switch statement what ordinary labels are to the goto statement. They may occur only within the braces that delimit the body of a switch statement.

The case prefix has the form

```
case ConstantExpression :
```

and the default prefix has the form

```
default:
```

The terminating colons are required; they heighten the analogy to ordinary statement labels. Any expression involving only numeric and character constants and operators is valid in the case prefix.

After evaluating ExpressionList, a search is made for the first matching case prefix. Control then goes directly to that point and proceeds normally from there. Other case prefixes and the default prefix have no effect once a case has been selected; control flows through them just as though they were not even there. If no matching case is found, control goes to the default prefix, if there is one. In the absence of a default prefix, the entire compound statement is ignored and control resumes with whatever follows the switch statement. Only one default prefix may be used with each switch.

If it is not desirable to have control proceed from the selected prefix all the way to the end of the switch block, break statements may be used to exit the block. Break statements have the form

```
break;
```

Some examples may help clarify these ideas. Assume Port A is specified as an output, and bits 3,2,1,0 are connected to a stepper motor. The switch statement will first read Port A and the data with 0x0F (PORTA&0x0F). If the result is 5, then PortA is set to 6 and control is passed to the end of the switch (because of the break). Similarly for the other 3 possibilities

```
#define PORTA *(unsigned char volatile*)(0x0000)
void step(void){ /* turn stepper motor one step */
    switch (PORTA&0x0F) {
        case 0x05:
            PORTA=0x06; // 6 follows 5;
            break;
        case 0x06:
            PORTA=0x0A; // 10 follows 6;
            break;
        case 0x0A:
            PORTA=0x09; // 9 follows 10;
            break;
        case 0x09:
            PORTA=0x05; // 5 follows 9;
            break;
        default:
            PORTA=0x05; // start at 5
    }
}
```

Listing 6.3: Example of the switch statement.

For more information on stepper motors, see Chapter 8 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano.

This next example shows that the multiple tests can be performed for the same condition.

```

// ASCII to decimal digit conversion
unsigned char convert(unsigned char letter){ unsigned char digit;
    switch (letter) {
        case 'A':
        case 'B':
        case 'C':
        case 'D':
        case 'E':
        case 'F':
            digit=letter+10-'A';
            break;
        case 'a':
        case 'b':
        case 'c':
        case 'd':
        case 'e':
        case 'f':
            digit=letter+10-'a';
            break;
        default:
            digit=letter-'0';
    }
    return digit; }

```

Listing 6.4: Example of the switch statement.

The body of the switch is not a normal compound statement since local declarations are not allowed in it or in subordinate blocks. This restriction enforces the C rule that a block containing declarations must be entered through its leading brace.

The While Statement

The while statement is one of three statements that determine the repeated execution of a controlled statement. This statement alone is sufficient for all loop control needs. The other two merely provide an improved syntax and an execute-first feature. While statements have the form

```
while ( ExpressionList ) Statement
```

where ExpressionList is a list of one or more expressions and Statement is an simple or compound statement. If more than one expression is given, the right-most expression yields the value to be tested. First, ExpressionList is evaluated. If it yields true (non-zero), then Statement is executed and ExpressionList is evaluated again. As long as it yields true, Statement executes repeatedly. When it yields false, Statement is skipped, and control continues with whatever follows.

In the example

```

i = 5;
while (i) array[--i] = 0;

```

elements 0 through 4 of array[] are set to zero. First i is set to 5. Then as long as it is not zero, the assignment statement is executed. With each execution i is decremented before being used as a subscript.

It is common to use the while statement to implement gadget loops

```

#define RDRF 0x20 // Receive Data Register Full Bit
// Wait for new serial port input, return ASCII code for key typed
char InChar(void){
    while ((SC0SR1 & RDRF) == 0){};
    return(SC0DRL);}
#define TDRE 0x80 // Transmit Data Register Empty Bit
// Wait for buffer to be empty, output ASCII to serial port
void OutChar(char data){
    while ((SC0SR1 & TDRE) == 0){};
    SC0DRL = data;
}

```

Listing 6.5: Examples of the while statement.

For more information on serial ports, see Chapter 7 of Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano.

Continue and break statements are handy for use with the while statement (also helpful for the [do](#) and [for](#) loops). The continue statement has the form

```

continue;

```

It causes control to jump directly back to the top of the loop for the next evaluation of the controlling expression. If loop controlling statements are nested, then continue affects only the innermost surrounding statement. That is, the innermost loop statement containing the continue is the one that starts its next iteration.

The break statement (described earlier) may also be used to break out of loops. It causes control to pass on to whatever follows the loop controlling statement. If while (or any loop or switch) statements are nested, then break affects only the innermost statement containing the break. That is, it exits only one level of nesting.

The For Statement

The for statement also controls loops. It is really just an embellished while in which the three operations normally performed on loop-control variables (initialize, test, and modify) are brought together syntactically. It has the form

```
for ( ExpressionList? ;  
      ExpressionList? ;  
      ExpressionList? ) Statement
```

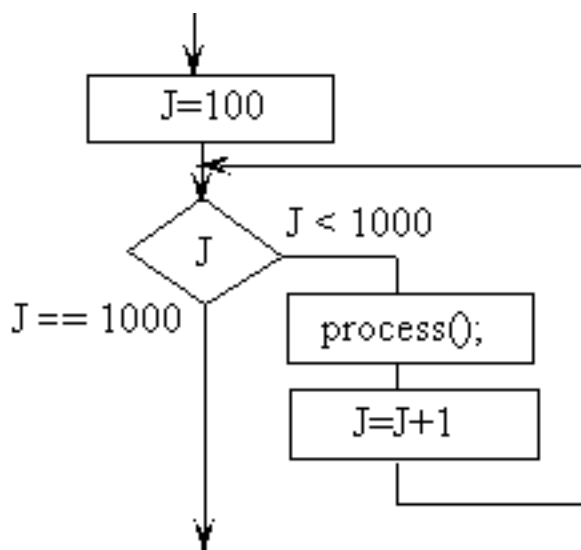
For statements are performed in the following steps:

The first ExpressionList is evaluated. This is done only once to initialize the control variable(s).

The second ExpressionList is evaluated to determine whether or not to perform Statement. If more than one expression is given, the right-most expression yields the value to be tested. If it yields false (zero), control passes on to whatever follows the for statement. But, if it yields true (non-zero), Statement executes.

The third ExpressionList is then evaluated to adjust the control variable(s) for the next pass, and the process goes back to step 2. E.g.,

```
for(J=100;J<1000;J++) { process(); }
```



A five-element array is set to zero, could be written as

```
for ( i = 4; i >= 0; --i) array[i] = 0;
```

or a little more efficiently as

```
for ( i = 5; i; array[--i] = 0) ;
```

Any of the three expression lists may be omitted, but the semicolon separators must be kept. If the test

expression is absent, the result is always true. Thus

```
for ( ;; ) { ...break; ... }
```

will execute until the break is encountered.

As with the [while](#) statement, break and continue statements may be used with equivalent effects. A break statement makes control jump directly to whatever follows the for statement. And a continue skips whatever remains in the controlled block so that the third ExpressionList is evaluated, after which the second one is evaluated and tested. In other words, a continue has the same effect as transferring control directly to the end of the block controlled by the for.

The Do Statement

The do statement is the third loop controlling statement in C. It is really just an execute-first while statement. It has the form

```
do Statement while ( ExpressionList ) ;
```

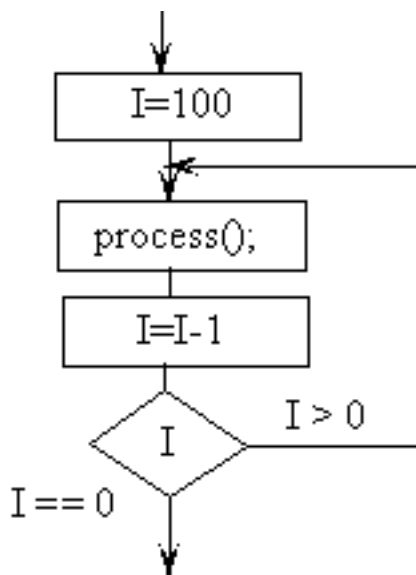
Statement is any simple or compound statement. The do statement executes in the following steps:

Statement is executed.

Then, ExpressionList is evaluated and tested. If more than one expression is given, the right most expression yields the value to be tested. If it yields true (non-zero), control goes back to step 1; otherwise, it goes on to whatever follows.

As with the while and for statements, break and continue statements may be used. In this case, a continue causes control to proceed directly down to the while part of the statement for another test of ExpressionList. A break makes control exit to whatever follows the do statement.

```
I=100; do { process(); I--;} while (I>0);
```



The example of the five-element array could be written as

```

i = 4;
do {array[i] = 0; --i;} while (i >= 0);

```

or as

```

i = 4;
do array[i--] = 0; while (i >= 0);

```

or as

```

i = 5;
do array[--i] = 0; while (i);

```

The Return Statement

The return statement is used within a function to return control to the caller. Return statements are not always required since reaching the end of a function always implies a return. But they are required when it becomes necessary to return from interior points within a function or when a useful value is to be returned to the caller. Return statements have the form

```

return ExpressionList? ;

```

ExpressionList? is an optional list of expressions. If present, the last expression determines the value to be returned by the function. If absent, the returned value is unpredictable.

Null Statements

The simplest C statement is the null statement. It has no text, just a semicolon terminator. As its name implies, it does exactly nothing. Why have a statement that serves no purpose? Well, as it turns out, statements that do nothing can serve a purpose. As we saw in [Chapter 5](#), expressions in C can do work beyond that of simply yielding a value. In fact, in C programs, all of the work is accomplished by expressions; this includes assignments and calls to functions that invoke operating system services such as input/output operations. It follows that anything can be done at any point in the syntax that calls for an expression. Take, for example, the statement

```
while ((SCSR & TDRE) == 0); /* Wait for TDRE to be set */
```

in which the `(SCSR&TDRE)==0` controls the execution of the null statement following. The null statement is just one way in which the C language follows a philosophy of attaching intuitive meanings to seemingly incomplete constructs. The idea is to make the language as general as possible by having the least number of disallowed constructs.

The Goto Statement

Goto statements break the sequential flow of execution by causing control to jump abruptly to designated points. They have the general form **goto Name** where **Name** is the name of a label which must appear in the same function. It must also be unique within the function.

```
int data[10];
void clear(void){ int n;
    n=1;
loop: data[n]=0;
    n++;
    if(n==10) goto done;
    goto loop;
done:
}
```

Listing 6.6: Examples of a goto statements

Notice that labels are terminated with a colon. This highlights the fact that they are not statements but statement prefixes which serve to label points in the logic as targets for goto statements. When control reaches a **goto**, it proceeds directly from there to the designated label. Both forward and backward references are allowed, but the range of the jump is limited to the body of the function containing the **goto** statement.

As we observed above, goto statements, cannot be used in functions which declare locals in blocks which are subordinate to the outermost block of the function.

Because they violate the structured programming criteria, **goto** statements should be used sparingly, if at all. Over reliance on them is a sure sign of sloppy thinking.

Missing Statements

It may be surprising that nothing was said about input/output, program control, or memory management statements. The reason is that such statements do not exist in the C language proper.

In the interest of portability these services have been relegated to a set of standard functions in the run-time library. Since they depend so heavily on the run-time environment, removing them from the language eliminates a major source of compatibility problems. Each implementation of C has its own library of standard functions that perform these operations. Since different compilers have libraries that are pretty much functionally equivalent, programs have very few problems when they are compiled by different compilers.

Go to [Chapter 7 on Pointers](#) Return to [Table of Contents](#)

Chapter 12: Assembly Language Programming

What's in Chapter 12?

[How to insert single assembly instructions](#)

[How to compile with a mixture of assembly and C files](#)

[Assembler Directives](#)

[How to use assembly to optimize a C function](#)

One of the main reasons for using the C language is to achieve portability. But there are occasional situations in which it is necessary to sacrifice portability in order to gain full access to the operating system or to the hardware in order to perform some interface requirement. If these instances are kept to a minimum and are not replicated in many different programs, the negative effect on portability may be acceptable. There are two approaches to writing assembly language with ICC11 and ICC12. The first method inserts a single assembly instruction directly into a C function using the `asm("string");` feature. Everything within the "string" statement is assumed to be assembly language code and is sent straight to the output of the compiler exactly as it appears in the input. The second approach is to write an entire file in assembly language, which may include global variables and functions. Entire assembly files can be inserted into our C programs using the `asm(".include 'filename' ");` feature. Entire assembly files can also be assembled separately then linked at a later time to the rest of the programs. The simple insertion method is discussed in this chapter.

How to insert single assembly instructions.

To support this capability, C provides for assembly language instructions to be written into C programs anywhere a statement is valid. Since the compiler generates assembly language as output, when it encounters assembly language instructions in the input, it simply copies them directly to the output.

A special directive delimits assembly language code. The following example inserts the assembly language instruction `cli` (enable interrupts) into the program at that point.

```
asm(" cli");
```

Some of the older versions of ICC11 require a space before the op code as shown in the examples in this chapter. ICC12 version 5.1 does not need the space before the op code. The extra space is ignored by these newer compiler versions, so experiment with your particular compiler to see whether or not the space is required. Macro substitution is not performed, but you can define macros that insert assembly. The following macros are defined in the HC11.H and HC12.H header files.

```
#define INTR_ON() asm(" cli")  
#define INTR_OFF() asm(" sei")
```

The following function runs with interrupts disabled.

```

void InitFifo(void){
    INTR_OFF();          /* make atomic, entering critical section
*/
    PutI=GetI=Size=0; /* Empty when Size==0 */
    INTR_ON();          /* end critical section */
}

```

Listing 12-1: Example of an assembly language macro

Of course, to make use of this feature, we must know how the compiler uses the CPU registers, how functions are called, and how the operating system and hardware works. It will certainly cause a programming error if your embedded assembly modifies the stack pointer, SP, or the stack frame pointer, X. On the other hand, in most situations you should be able to modify the CCR, A, B, or Y without causing a program error. It is good practice to observe the resulting assembly output of the entire function to guarantee that the embedded assembly has not affected the surrounding C code. Unfortunately, this verification must be repeated when you upgrade the compiler.

You can assess global variables directly using its equivalent assembly name (starts with an underscore). The following function adds one to the 16-bit global time.

```

short time;
void Addltime(void){
    asm(" ldy _time");
    asm(" iny");
    asm(" sty _time");
}

```

Listing 12-2: Example of an assembly language access to a global variable

You can assess local variables directly using a % before its name.

```

void InitFifo(void){ unsigned char SaveSP;
    asm(" tpa");          /* Reg A contains previous CCR */
    asm(" staa %SaveSP"); /* Save previous CCR value */
    asm(" sei");          /* make atomic, entering critical
section */
    PutI=GetI=Size=0;    /* Empty when Size==0 */
    asm(" ldaa %SaveSP"); /* Reg A contains previous CCR */
    asm(" tap");          /* end critical section */
}

```

Listing 12-3: Example of an assembly language access to a local variable

The above method of disabling interrupts is a good way to execute critical code. This is an appropriate way to execute critical code because once the critical code is started it will finish (i.e., atomic). The code becomes atomic because interrupts are disabled. At the end of the critical code, the interrupt status is restored to its previous value. This save/restore interrupt status procedure allows you to nest one critical code inside another critical code. If you disable interrupts before the critical code and enable interrupts after the critical code, you are presuming that interrupts were enabled when the critical code was started. The disable/enable method of executing critical code does not allow for one critical code to call another critical code. In the following example, **InitFifo** properly returns with interrupts still disabled.

```
void InitSystem(void){ unsigned char SaveSP;
    asm(" tpa");          /* Reg A contains previous CCR */
    asm(" staa %SaveSP"); /* Save previous CCR value */
    asm(" sei");         /* make atomic, entering critical
section */
    InitFifo();
    InitPort();
    InitTimer();
    asm(" ldaa %SaveSP"); /* Reg A contains previous CCR */
    asm(" tap");         /* end critical section */
}
```

Listing 12-4: Example of a multiple line assembly language insertion

If you don't like the above style of writing each line separately, there is a shorthand for multiple-line assembly as shown in the following implementation.

```
void InitFifo(void){ unsigned char SaveSP;
    asm(" tpa\n"          /* Reg A contains previous CCR */
        " staa %SaveSP\n" /* Save previous CCR value */
        " sei");         /* make atomic, entering critical
section */
    PutI=GetI=Size=0;    /* Empty when Size==0 */
    asm(" ldaa %SaveSP\n" /* Reg A contains previous CCR */
        " tap");         /* end critical section */
}
```

Listing 12-5: A second example of a multiple line assembly language insertion

There is yet another style of writing multiple-line assembly, but I don't recommend it because it is harder to read.

```
void InitFifo(void){ unsigned char SaveSP;
    asm(" tpa\n staa %SaveSP\n sei"); /* make atomic, entering
critical section */
    PutI=GetI=Size=0; /* Empty when Size==0 */
    asm(" ldaa %SaveSP\n tap"); /* end critical section */
}
```

Listing 12-6: A third example of a multiple line assembly language insertion

This last example suggests the macro definitions:

```
#define START_CRITICAL() asm(" tpa\n staa %SaveSP\n sei")
#define END_CRITICAL() asm(" ldaa %SaveSP\n tap")
```

The use of these two macros requires the definition of an 8-bit local variable called **SaveSP**.

How to compile with a mixture of assembly and C files

The following C program embeds an assembly language file (programs and data). In this example the C program accesses a global variable (**lowGlobal**) and calls a function (**lowSub**) defined in the assembly file, and the assembly function assesses a global variable (**highGlobal**) and calls a function (**highSub**) defined in the C file. To access an assembly function, the C program simply calls it, with the standard ICC11/ICC12 parameter passing rules. To access an assembly level global variable, the C program types it with the **extern**. Notice however that the assembly function (**lowSub**) does not need a prototype in the high level C program.

```
/* C level program      file="high.C" */
int highGlobal;
extern int lowGlobal; /* typed here but defined in low.s
asm(".include 'low.s' "); /* insert assembly here
void main(void){
    lowSub(5); /* call to assemble routine
    lowGlobal=6; /* access of assembly global
};
int highSub(int input){return(input++);}
```

Listing 12-7: A high-level C program that calls a low-level assembly function

The following assembly program is embedded into the above high level C program. The double colon, **::**, specifies the label as external and will be available in the *.map file. The **.area text** is the standard place for programs (in ROM), and the **.area bss** is the standard area for globals (in RAM). Assembly level functions (e.g., **_lowSub**) and variables (e.g., **_lowGlobal**) are defined beginning with an underscore, "_". Notice that in the assembly file the names have the underscore, but the same name in the C file do not. To access a C function, the assembly program simply calls it (the name begins with an underscore.) The assembly program has full access to high level global variables (the name begins with an underscore.)

```
; assembly language program file="low.s"
    .area text
_lowSub::                ; definition of low level subroutine
    jsr _highSub         ; call to high level function
    std _highGlobal      ; access to high level global
    rts
    .area bss
_lowGlobal::            ; definition of low level global
    .blkb 2
```

Listing 12-8: A low-level assembly program that calls a high-level C function

Again, parameter passing with both functions (the assembly calls to the C and the C calls to the assembly) must adhere to the standard ICC11/ICC12 parameter passing rules:

- The output parameter, if it exists, is passed in Register D,
- The first input parameter is passed in Register D,
- The remaining input parameters are passed on the stack,
- 8-bit parameters are promoted to 16 bits.

Chapter 10 presented [some examples](#) of the assembly code generated by the compiler when calling a function with parameters. If you are writing an assembly language function that is to be called from C, one method to get the parameter passing correct is to write a simple C function that simply passes the parameters. Compile this simple C function with your other C code, and observe the assembly language created by the compiler for the simple C function. Next draw a stack picture that exists at the

start of the function. The C compiler will do some weird things within the function (like pushing register D on the stack, and shifting some 8 bit parameters around), which you do not have to duplicate. One difficulty with mixing the assembly with C is that when the compiler is upgraded, this compatibility matching must be redone.

Assembler Directives

An assembler directive (or pseudo-op) is not executed by the 6811/6812, but rather affect the assembler in certain ways. The assembly pseudo-ops supported by the ICC11 and ICC12 assembler are described in this section.

The first set of directives affect where in memory the subsequent assembly lines will be stored. The **.org** pseudo-op takes an expression, and changes the memory storage location to the value of the expression. This directive can only be used within an absolute area. Example

```
.org 0xF000 ; put subsequent object code at $F000
```

The **.area** pseudo-op specifies into which segment the subsequent code will be loaded.

```
.area text      ; code in the ROM segment
.area data      ; code in the initialized RAM segment
.area idata     ; code in ROM used to initialize the data
segment
.area bss       ; code in the uninitialized RAM segment
.text           ; same as .area text
.data          ; same as .area data
```

When writing assembly language programs, I suggest allocating variables in the **.area bss** segment and fixed constants/programs in the **.area text**. In other words, I suggest that you not use **.area data** and **.area idata** in your assembly programs. Other names can be used for segments. If the **(abs)** follows the name, the segment is considered absolute and can contain **.org** pseudo-ops. For example to set the reset vector in an assembly file, you could write

```
.area VectorSegment(abs)
.org 0xFFFFE ; reset vector address
.word Start  ; place to begin
```

The next set of directives allocate space in memory. The **.blkb** pseudo-op will set aside a fixed number of 8-bit bytes without initialization. Similarly, the **.blkw** pseudo-op will set aside a fixed number of 16-bit words without initialization.

```
.blkb 10 ; reserve 10 bytes
.blkw 20 ; reserve 20 words
```

The next three directives load specific data into memory. The **.byte** pseudo-op will set aside a fixed

number of 8-bit bytes and initialize the memory with the list of 8-bit bytes. The size of the allocated storage is determined by the number of data values in the list. The **.word** and **.ascii** pseudo-ops work in a similar way for 16-bit words and ASCII strings. The **.asciz** pseudo-op is similar to **.ascii** except that an extra byte is allocated and set to null (0). Examples include:

```
.byte 10      ; reserve 1 byte initialized to 10
.byte 1,2,3   ; reserve 3 bytes initialized to 1,2,3
.word 20      ; reserve 1 word initialized to 20
.word 10,200  ; reserve 2 words initialized to 10,200
.ascii "JWV"  ; reserve 3 bytes initialized to "J" "W" "V"
.asciz "JWV"  ; reserve 4 bytes initialized to "J" "W" "V" 0
```

Because the 6812 is more efficient when accessing 16 bit data from even addresses, sometimes we wish to skip a memory byte to force the subsequent code to loaded into an even or odd address. To do this we can use:

```
.even ; force next code to be at an even address
.odd  ; force next code to be at an odd address
```

There are two ways to make an assembly language label global (accessible outside the file). The first way is to use double colons as in Listing 12-8. The second way is to use the **.global** pseudo-op:

```
.global Start ; make this label global
```

We can create assembly constants using the **=** pseudo-op. One application of this directive is defining symbolic names for the I/O ports. Instead of writing code list this:

```
; read a byte from the SCI port
getchar:: ldaa 0x00C4 ; wait for new character available
          bita #$20
          beq  getchar
          clra
          ldab 0x00C7 ; new character from SCI
          rts
```

Listing 12-9: A subroutine that reads a character from the 6812 SCI0 port

we can add symbols to make it more readable:

```
; read a byte from the SCI port
SC0SR1=0x00C4
SC0DRL=0x00C7
RDRF=0x20
getchar:: ldaa SC0SR1 ; wait for new character available
          bita #RDRF
```

```

    beq  getchar
    clra
    ldab SC0DRL    ; new character from SCI
    rts

```

Listing 12-10: A better subroutine that reads a character from the 6812 SCI0 port

NOTE: the assembly directive =is not a macro substitute. Rather the expression is evaluated once, and the number value is used in place of the symbol.

Conditional assembly can be implemented using the **.if <exp> .else .endif** construction. If the <exp> is true (not zero) then the assembly code up to the **.else** is included. If the <exp> is false (0) then the assembly code between the **.else** and **.endif** will be included. For example

```

IS6812=1 ; means it is a 6812
.if IS6812
SCSR=0x00C4
SCDR=0x00C7
.else
SCSR=0x102E
SCDR=0x102F
.endif
RDRF=0x20
getchar:: ldaa SCSR    ; wait for new character available
          bita #RDRF
          beq  getchar
          clra
          ldab SCDR    ; new character from SCI
          rts

```

Listing 12-11: A flexible subroutine that reads a character from the 6811 or 6812 SCI port

The last pseudo-op is used to include other assembly files. For example

```

; read a byte from the SCI port
.include "HC12.S"
getchar:: ldaa SC0SR1    ; wait for new character available
          bita #RDRF
          beq  getchar
          clra
          ldab SC0DRL    ; new character from SCI
          rts

```

Listing 12-12: The **.include** pseudo-op allows you to divide software into separate files

How to use assembly to optimize a C function

In almost all situations when faced with a time-critical constraint it would be better to solve the problem other ways than to convert C code to assembly. Those alternative ways include using a faster CPU clock speed, upgrading to a more efficient compiler, and upgrading to a more powerful processor. On the other hand, some times we need to write and link assembly functions. One good reason to code in assembly is to take advantage of computer-specific operations. The enabling and disabling of interrupts is an example of an important operation that can not be performed in standard C. Another example is the use of specialize functions on the 6812 like fuzzy logic and table look-up. Although you could develop fuzzy logic control system in standard C, there are compelling speed advantages to implementing the core fuzzy logic controller in assembly.

In this example we will optimize the **add3()** function presented previously in Chapter 10. The assembly generated by ICC11 and ICC12 for this example was discussed [back in Chapter 10](#). The C code from Listing 10-8 is repeated:

```
int x1;
static int x2;
const int x3=1000;
int add3(int z1, int z2, int z3){ int y;
    y=z1+z2+z3;
    return(y);}
void main(void){ int y;
    x1=1000;
    x2=1000;
    y=add3(x1,x2,x3);
```

Listing 10-8: Example function call with local variables

The assembly output (Listing 10-10) generated by the ImageCraft ICC12 version 5.1 is also repeated.

```
    .area text
_x3:: .word 1000
    .area text
; y -> -2,x
; z3 -> 8,x
; z2 -> 6,x
; z1 -> 2,x
_add3:: pshd
    pshx
    tfr s,x
    leas -2,sp
    ldd 2,x
```

```

    addd 6,x
    addd 8,x
    std -2,x
    ldd -2,x
    tfr x,s
    pulx
    leas 2,sp
    rts
; y -> -2,x
_main:: pshx
    tfr s,x
    leas -8,sp
    movw #1000,_x1
    movw #1000,_x2
    movw _x3,2,sp
    movw _x2,0,sp
    ldd _x1
    jsr _add3
    std -4,x
    tfr d,y
    sty -2,x
    tfr x,s
    pulx
    rts
.area bss
_x2:   .blkb 2
_x1::  .blkb 2

```

Listing 10-10: ICC12 assembly of function call with local variables

Next we draw a stack picture at the point of the first instruction of the function **add3()**.

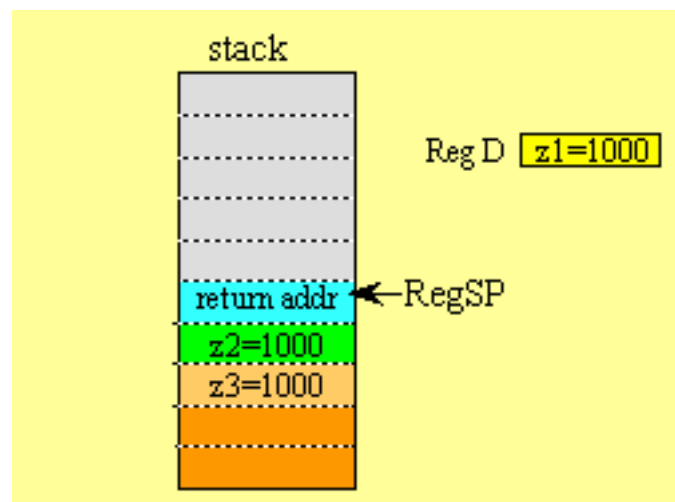


Figure 12-1 Stack frame at the start of `add3()`

The next step in optimization is to copy and paste the ICC11/ICC12 compiler code from the *.s file into a new assembly file. We will name the file add3.s. Using the stack frame picture as our guide, we optimize the function. One possible optimization is shown below. Notice that I created a new local variable stack binding based on SP instead of Reg X.

```
; ****filename is add3.s ****
; z3 -> 4,sp
; z2 -> 2,sp
; z1 in Reg D
_add3:: addd 2,sp      ; z1+z2
        addd 4,sp      ; z1+z2+z3
        rts
```

Listing 12-13 Optimized add3 function

Now this new function is linked into the original program.

```
int x1;
static int x2;
const int x3=1000;
asm(".include 'add3.s' ");
int add3(int, int, int);
void main(void){ int y;
    x1=1000;
    x2=1000;
    y=add3(x1,x2,x3);
```

Listing 12-14: Use of the new optimized function

Embedding the assembly function (**add3**) into C seems to work with or without the **int add3(int,int,int)**; prototype.

For more information about assembly language programming see the Motorola Microcomputer Manuals and the help system of the application **TEaS** that is included with the book Embedded Microcomputer Systems: Real Time Interfacing by Jonathan W. Valvano published by Brooks-Cole.

Go to [Appendix 1 on Adapt812 board](#) Return to [Table of Contents](#)

Appendix 1. Kevin Ross/Adapt812 Setup

What's in Appendix 1?

[Kevin Ross's Background Debug Module](#)

[ICC12 options menu for developing software for the Adapt812](#)

[Adapt 812 Board Jumpers](#)

[What you need to get started](#)

[Development Procedure](#)

[Web sites for more information.](#)

Kevin Ross's Background Debug Module/Adapt812

Technological Arts makes a variety of low cost 6812 products suitable for teaching laboratory classes. These boards are convenient because they can be inserted directly into a standard protoboard. See figures 1.28 and 1.47 in the book "Real time Embedded Systems" by Jonathan W. Valvano. Notice that the Technological Arts boards have a male connector that plugs into the student's female socket. If the students purchase their-own protobaord, then the mechanical "wear and tear" occurs on the inexpensive protoboard and not on the relatively-expensive microcomputer board. Technological Arts can be reached at

Technological Arts

819-B Yonge St.

Toronto, ON

M4W 2G9 Canada

toll-free: 1-877-963-8996 (USA & Canada)

Phone: (416) 963-8996 Fax: (416) 963-9179

<http://www.technologicalarts.com>

The Kevin Ross BDM-12 is a debugging interface between the PC and the Adapt812. It allows us to download programs, view/change registers, view/change memory, single step, and run software. If you have a logic analyzer for debugging, you can use the BDM-12 to modify the MC68HC812A4 mode so that the address and data bus are available on the Adapt812 H2 connector. With Windows 3.1, you will run Kevin Ross's DOS-level DOS12.EXE program. With Windows NT or 95, you will run Kevin Ross's DOS-level DB12.EXE program. For more information about the Kevin Ross BDM board contact, "Kevin Ross" kevinro@nwlinc.com

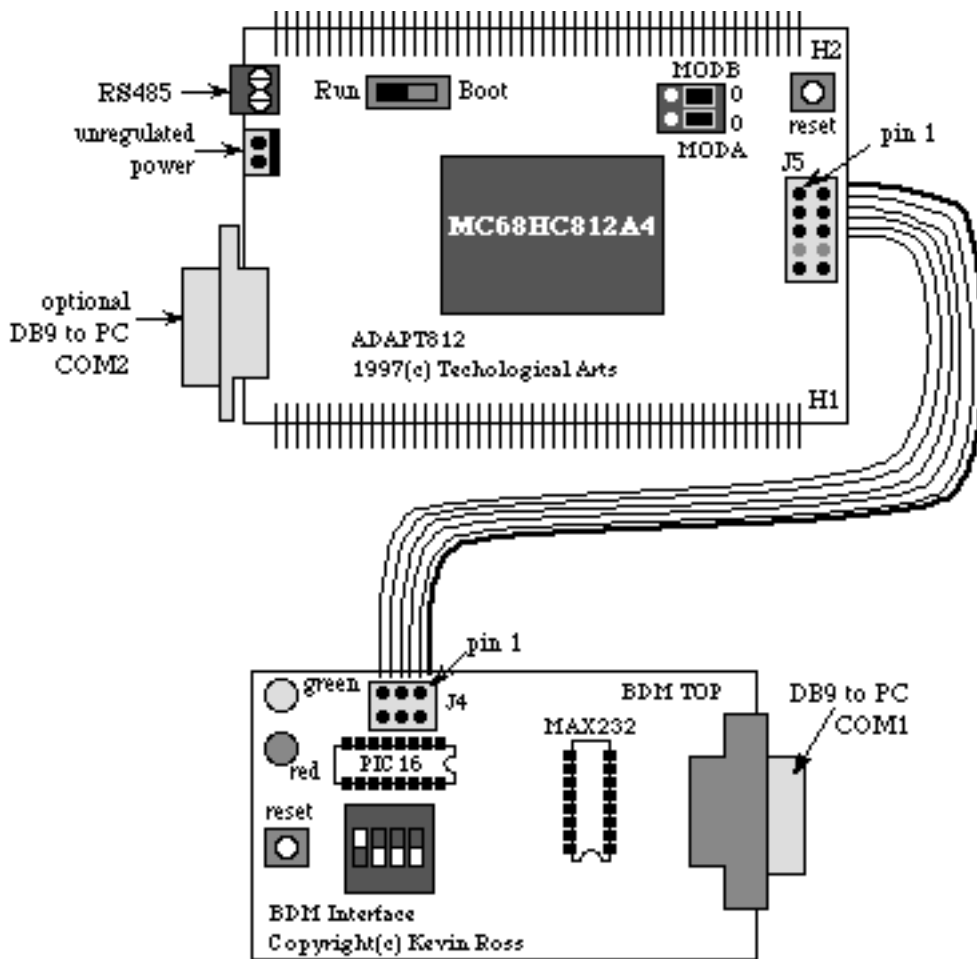


Figure 1: The Kevin Ross BDM is used to program the Adapt812

The proper DIP settings on the Kevin Ross BDM-12 board are

- 1 ON for 38400 bits/sec
- 2 OFF for debugger mode
- 3 OFF for 8 MHz 6812
- 4 OFF for 8 MHz 6812

The ribbon cable between the Kevin Ross BDM-12 and the Technological Arts Adapt812 should be attached so that Pin 1 is connected to Pin 1. It is possible to reverse the polarity so be careful. The Technological Arts APAPT812 settings:

- Run/Boot* switch should be in the *Run* position
- MODA/MODB* jumpers should both be in the *0* position for single chip mode.

The Adapt812 board can accept power from one of two sources. We will use an unregulated AC adapter (greater than 6 volts) connected to the power connector J1. The other option for the Adapt812 would be to connect a regulated +5v supply to the H1 connect. The BDM-12 helper board received its power from the Adapt812 through the BDM cable.

ICC12 options menu for developing software for the Adapt812

On the ICC12 development system, we use the options_compiler_linker menu command to specify where the start of globals (the data section grows towards higher addresses), and the start of the object code (the text section grows towards higher addresses). The initial stack pointer (stack grows towards lower addresses), is specified in options_compiler_linker menu. On our Adapt812 system,

the [static variables](#) (data section) should start at the start of RAM, 0x0800

the program (text section) should start at the start of EEPROM, 0xF000

the [automatics](#) (stack) could be set at the end of RAM, 0x0C00

the Library path is set to e:\mc6812\lib

the "Create Map File" button is selected

the "Heap size" is 0.

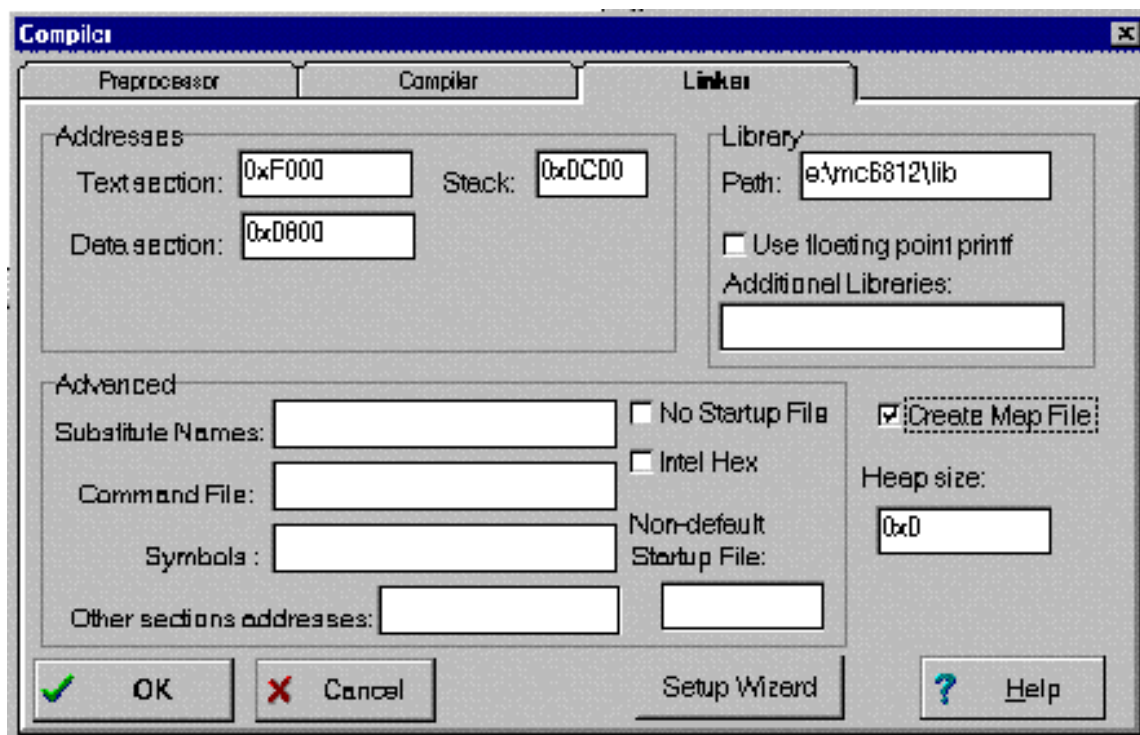


Figure 2: ICC12 linker settings when using the Adapt812

We use the options_compiler_compiler dialog to specify that C source code be added to the Asm Output on our Adapt812 system,

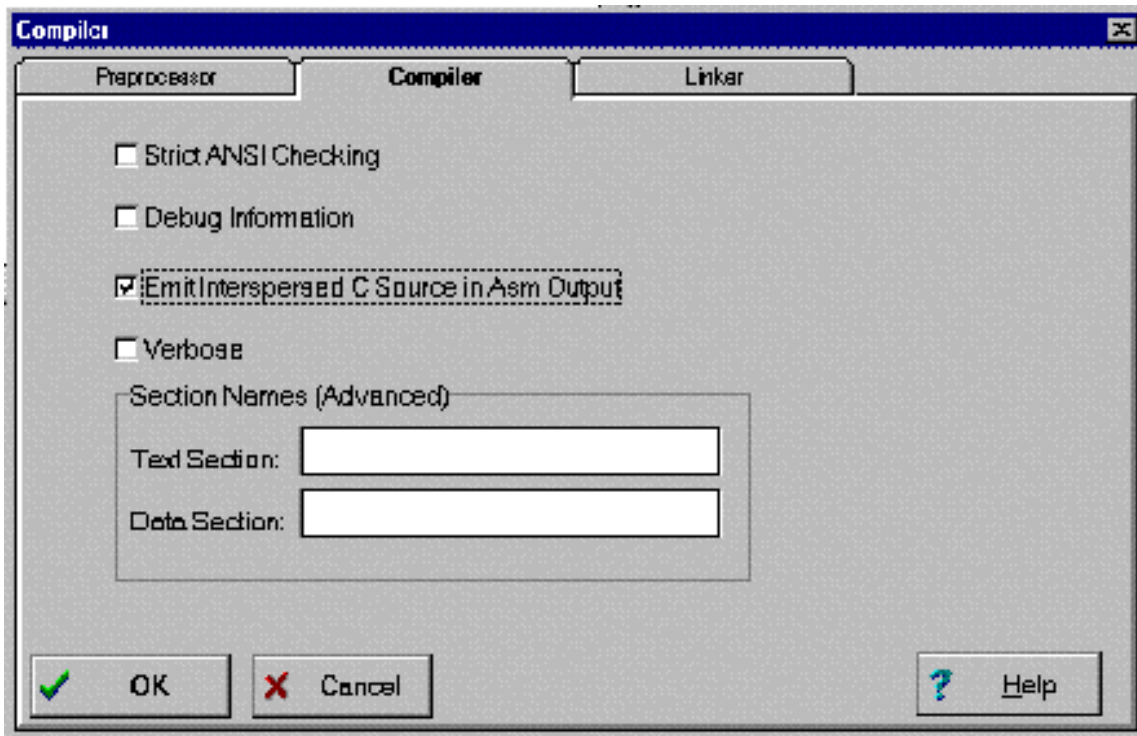


Figure 3: ICC12 compiler settings when using the Adapt812

Check the "Emit Interspersed C Source in Asm Output" button.

We use the options_compiler_preprocessor dialog to specify that C++ comments can be used. On our Adapt812 system,

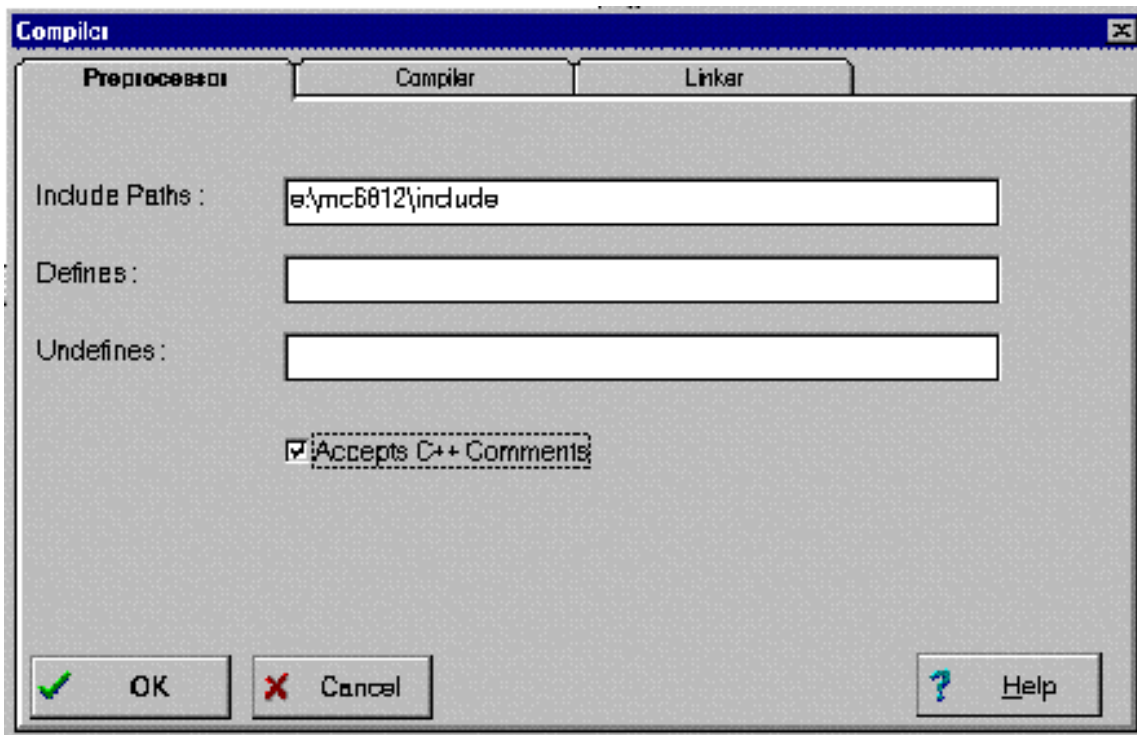


Figure 4: ICC12 preprocessor settings when using the Adapt812

Check the "Accepts C++ Comments" button.

Adapt 812 Board Jumpers

JB1 has two jumpers for MODA and MODB. They both should be in the 0 position. This will bring the MC68HC812 up in single chip mode, placing the 4K EEPROM from \$F000 to \$FFFF. There is a run/boot switch that should be in the "run" position. There is a boot loader in high EEPROM memory that can be used to program the Adapt812 from a serial port without the need for a Kevin Ross BDM. This procedure places the run/boot switch in the "boot" position.

What you need to get started

Software

The most expensive piece of software you will need is a compiler. Information about ImageCraft ICC12 can be found from the web site <http://www.imagecraft.com>. The mailing address is ImageCraft, 706 Colorado Ave. Suite 10-88, Palo Alto, CA 94303. If a compiler is unavailable, you could use the TExaS simulator and develop 6812 programs in assembly. TExaS can generate S19 records for downloading. There is a free ICC11 C compiler on the TExaS CD.

Power Adapter

To run the Adapt812, you will need 5.5 to 9 VDC unregulated power source with a current of at least 100ma, plus the additional current your external circuits need.

Serial Cables

The cables between the PC and the development boards are standard 9 pin null modem serial cables.

Downloading Hardware

There are three ways to program the Adapt812. You can purchase a Kevin Ross board (it does come in kit form). With this board you download using the DOS-level application DL12.exe. Second, you purchase a Motorola MC68HC912B32 EVB board and run it in BDM POD mode. The ICC12 compiler supports this configuration. I.e., the PC connected to a 912EVB in POD mode, connected to the 812A4 target. Lastly, the Adapt812 board has a boot loader that can accept S19 records directly from the PC. The disadvantages of this method are there is no verify and no debugging support (but it is cheap.)

Development Procedure

The following steps are required for the project development:

1. Design the software
2. Design the hardware(if any)
3. Using ICC12, write(modify) the code
4. Remove compiler or assembly errors from the code, and also any logical errors in the

- code which you think might be present, print out the MAP file to assist in debugging
5. Build the hardware(if any) on your own protoboard
 6. Eliminate as many hardware errors as possible before connecting to the 68HC812.
 7. With the power of the PC and BDM-12 off, connect a PC serial port to the BDM-12 helper board
 8. Using DL12.EXE, down load the object code of your software into the 68HC812.
while running DL12 can observe memory
while stopped DL12 can observe/modify memory and registers

Web sites for more information

Technological Arts: <http://www.technologicalarts.com>
electronic parts at: <http://www.bgmicro.com>
robot parts at: <http://www.RobotStore.com>
ICC11/ICC12 at: <http://www.imagecraft.com>

Return to [Table of Contents](#)

The opinions expressed in this book do not necessarily reflect the opinions of the University, its management or its big time financial donors. Also, there shall be no bologna, Bevis, mustard, chewing the cables, free lunch, sob stories, running & screaming, whining, hitting, spitting, kicking, biting, or tag backs. Quit it or I'm telling. (Enjoy this book.)

Go to [Chapter 1](#) Return to [Table of Contents](#)