

UbiNetics

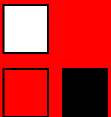
WCDMA/HSDPA physical layer design

Jon Burrell

Content

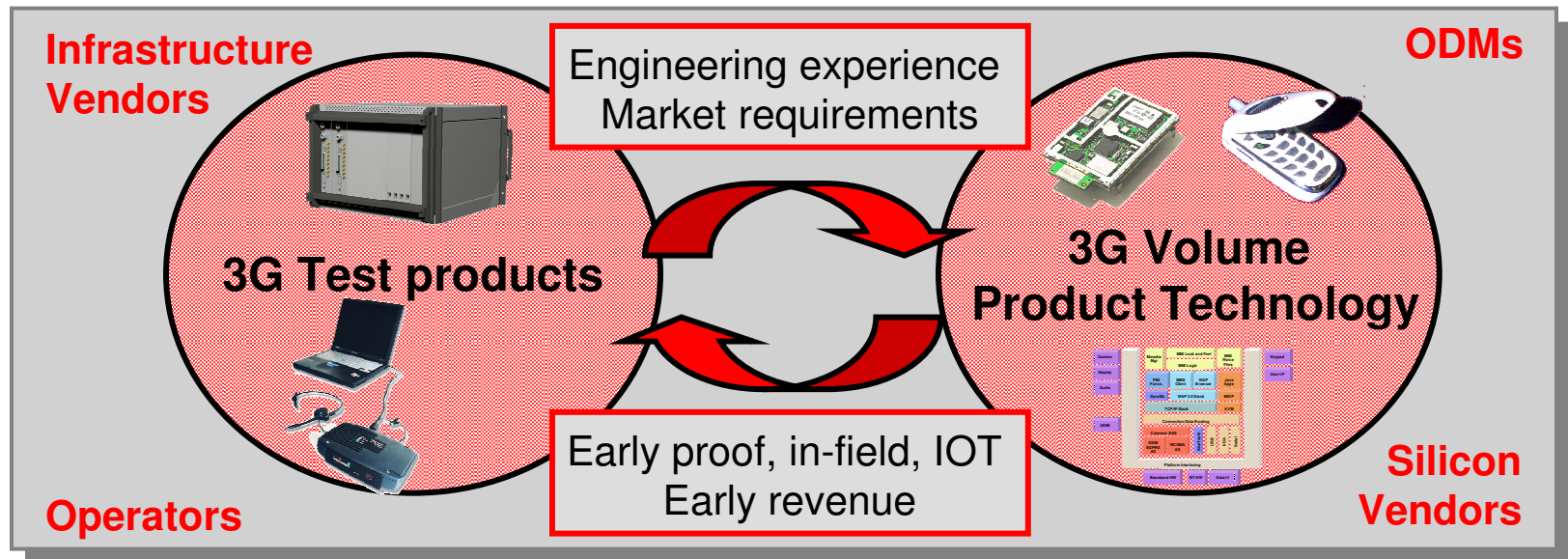
- Importance of WCDMA
- Crash course in WCDMA
- WCDMA implementation
- Why HSDPA
- Crash course in HSDPA
- HSDPA implementation

***Terminal physical layer only, downlink bias
and selective!***



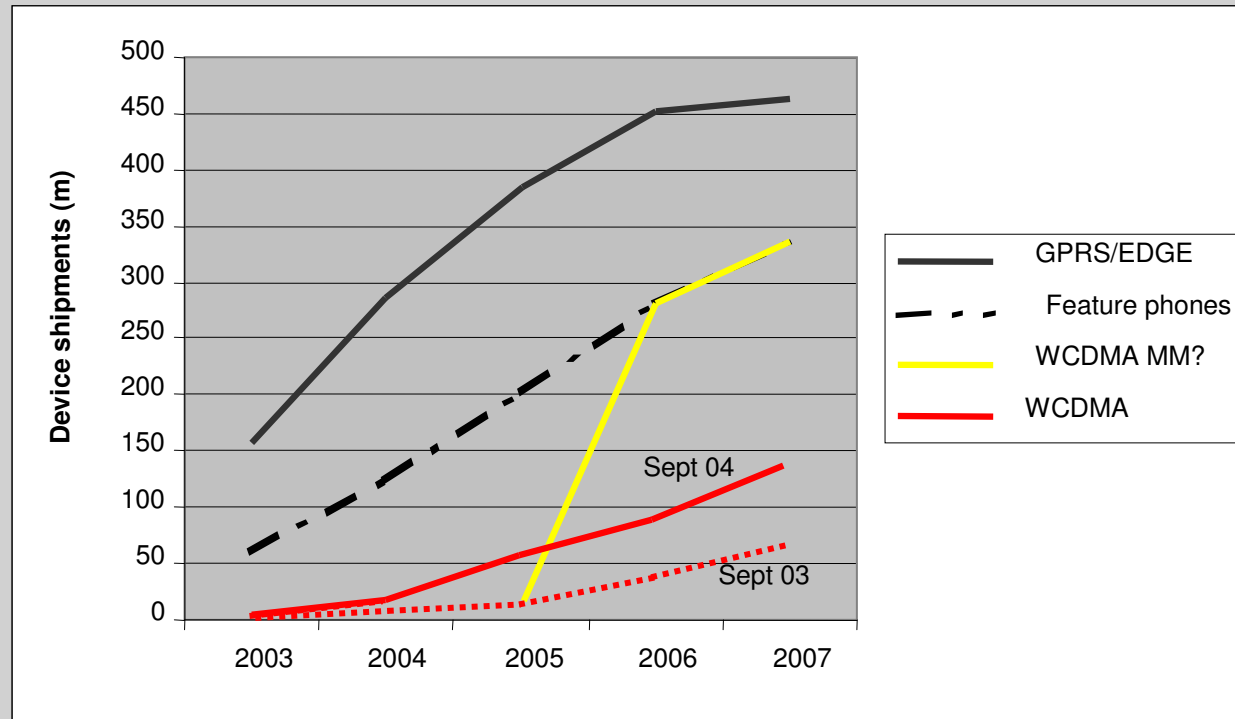
UbiNetics outline

3G (HSDPA/WCDMA/EDGE/GPRS) handset technology



- Cambridge UK based, founded January 1999 and now 400 staff
- R&D centres in Bangalore, Cambridge, Swindon and Shenzhen
- Sales and customer support centres Hong Kong, Tokyo

WCDMA Importance



Source: Ovum / DB

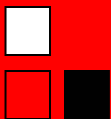
Underestimated prediction for WCDMA/EGPRS technology?

WCDMA (multimode) phone technology will dominate

What is WCDMA?

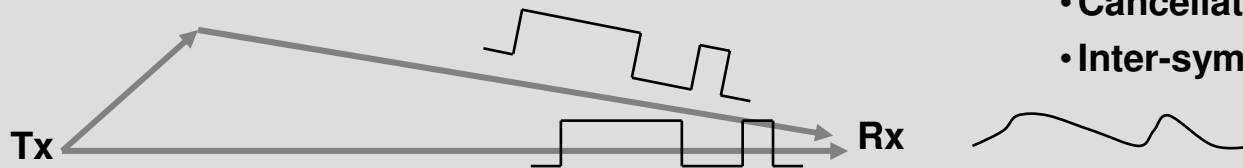
Layer 1 perspective

- Wideband 5 MHz. (For each of DL and UL)
- Single frequency all users and all cells
- DS spread spectrum
- User and cell separation by spreading code
- Fast power control
- Soft handover
- QPSK
- Turbo coding
- Multiple transport channel combinations
- 384 kbps

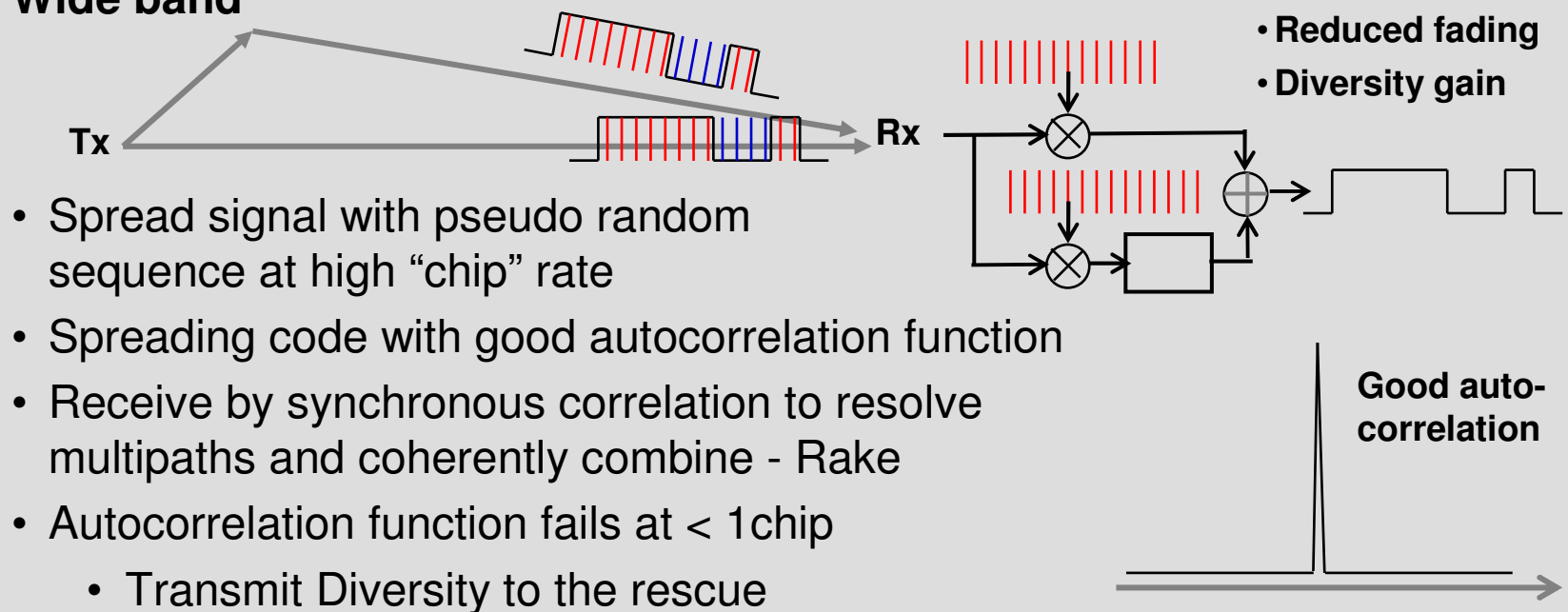


Essentials 1: Diversity gain

Narrow band



Wide band



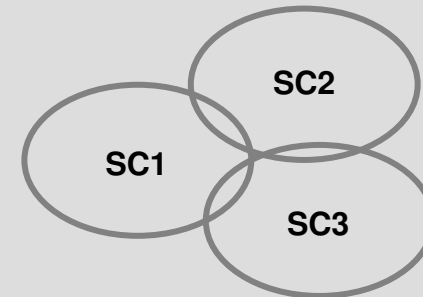
Essentials 2: Spreading Codes

Spreading is a combination (multiplication) of two codes:

- Orthogonal Variable Spreading Factor (OVSF)
 - Separates users in a cell (in DL)
 - Variable length: defines data rate
 - Perfect cross-correlation. If aligned!
 - Very poor auto-correlation

OVSF1: 00001111
OVSF2: 00110011

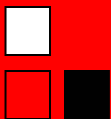
- Scrambling codes (SC)
 - 10ms long PN sequence
 - Very good correlation properties
 - Separates and identifies cells in DL
 - (Separate users in the UL)



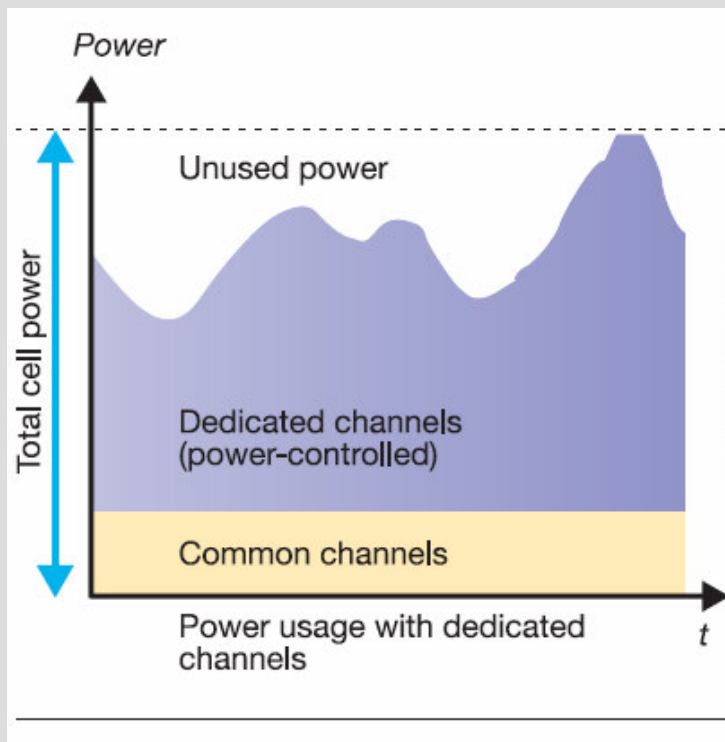
- But the codes are not perfect
 - Intra-cell interference through loss of OVSF orthogonality in multipath
 - Inter-cell interference from neighbour cell SCs



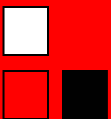
CDMA systems are interference limited, so.... **UbiNetics™**



Essentials 3: Power control



- Very important in the UL because of “near far” problem
- Reduces interference in the DL by minimising cell power
- Fast closed loop power control used in both UL and DL
- Received SIR determines TPC feedback
- Fast feedback requirements, $\sim 70\mu\text{s}$, a considerable challenge for physical layer design



Essentials 4: 3G vs 2G

WCDMA gains from:

- Better spectral efficiency of CDMA vs TDMA
- QPSK, and turbo coding

Makes *WCDMA* ~5x more efficient than *GSM*

Table 7. Comparative Costs of Providing Data

	GPRS	WCDMA
Access Revenue/User/Month ⁽¹⁾	\$40.00	\$40.00
Cost/Mbyte @ Capacity ⁽²⁾	\$0.415	\$0.069
Mbytes/Month/User	268	268
Network Cost/User/Month	\$111.22	\$18.49
Sales & Mktng and G&A/User/Month ⁽³⁾	\$16	\$16
Earnings Before Interest & Taxes	-\$87.22	\$5.51
<i>EBIT Margin</i>	<i>-218%</i>	<i>14%</i>

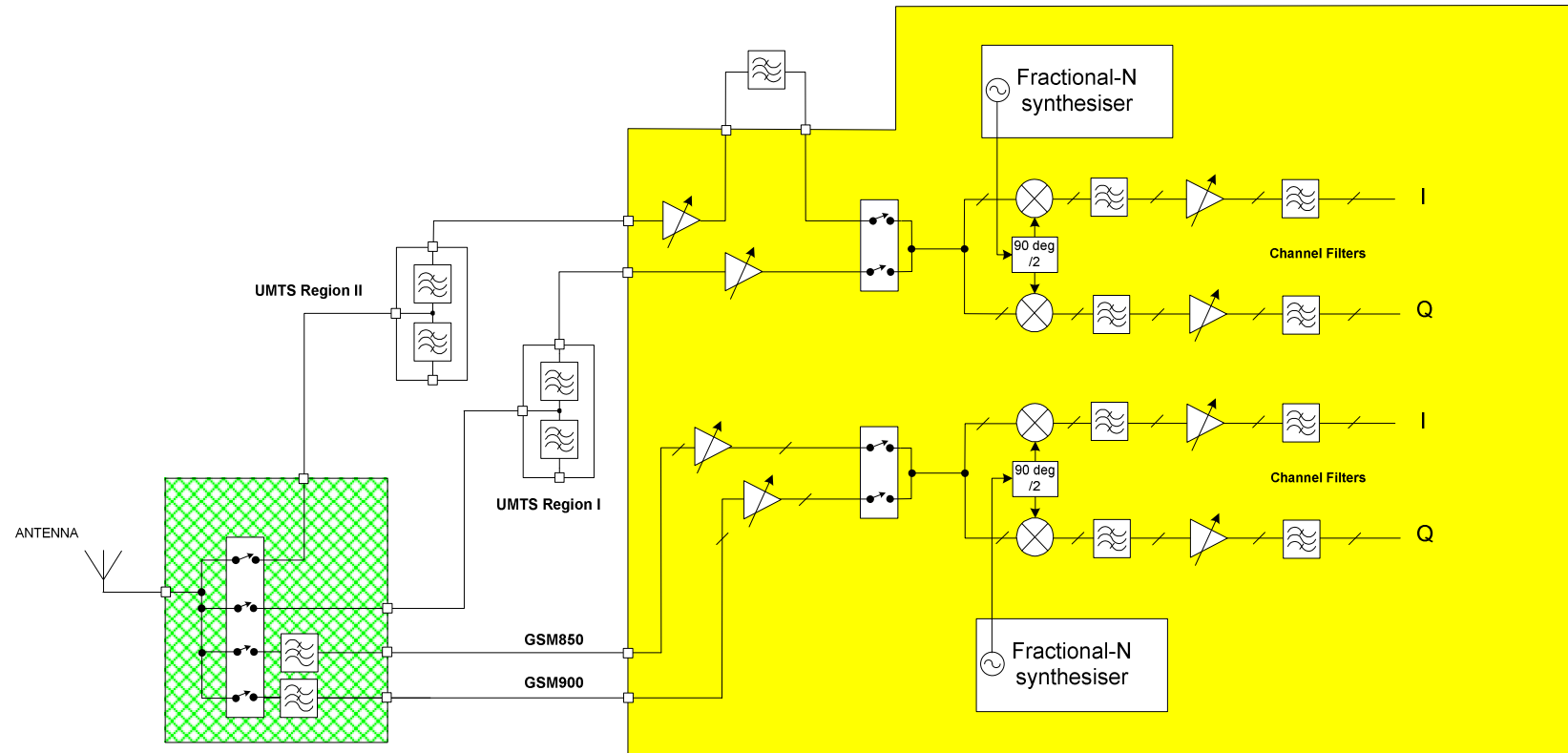
6x cheaper

⁽¹⁾ Source: Morgan Stanley Dean Witter; The Mobile Internet Report; 10/00; p.52. Estimates affordability of wireless data to be \$25–\$50 per month

⁽²⁾ Excludes amortization of spectrum costs.

WCDMA: high speed, flexible data services, low cost

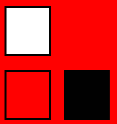
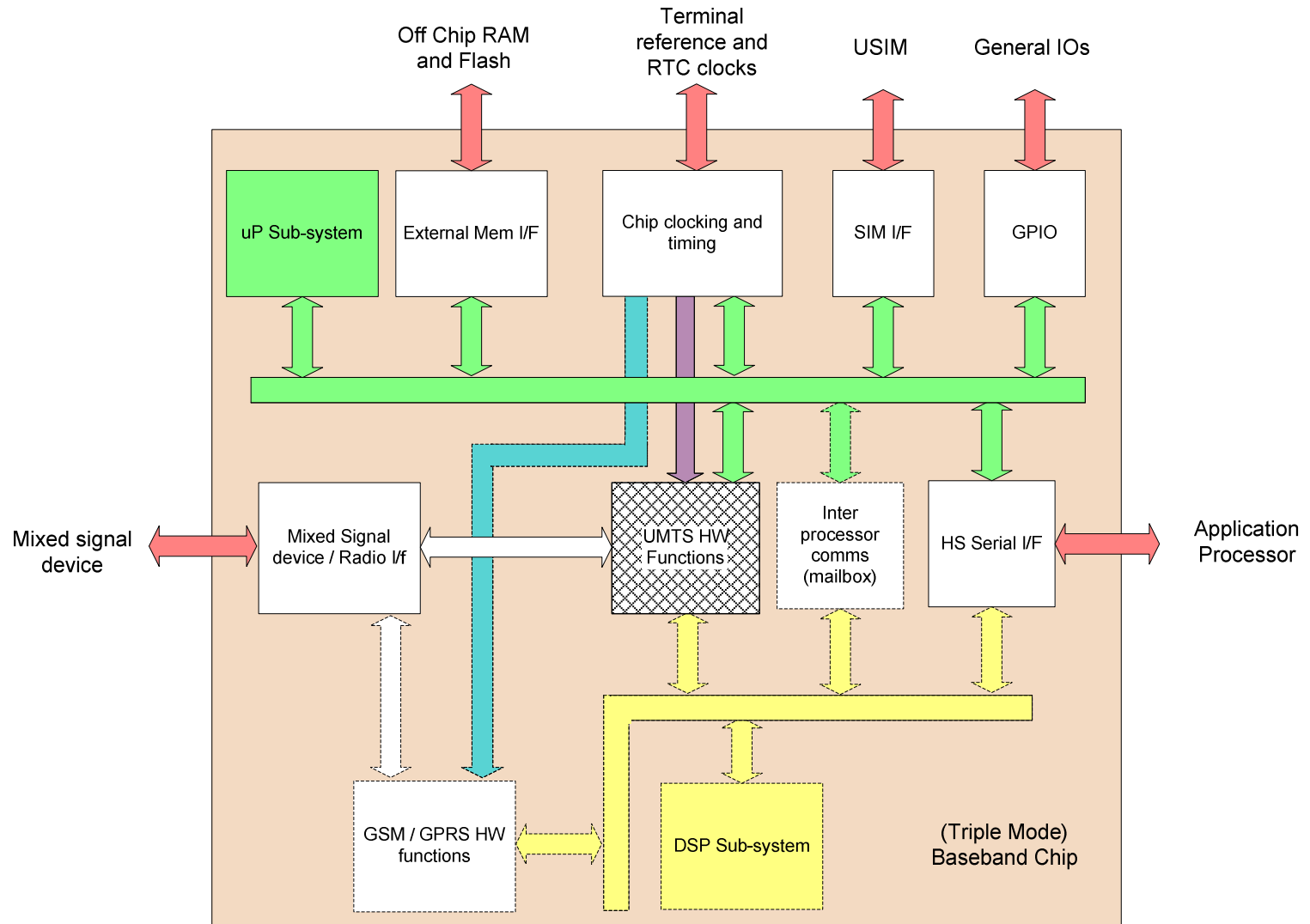
WCDMA RF Design (receiver)



- Direct conversion receiver gives competitive design – low BOM
- But must be careful to minimise DC offset
- Trick is to remove DC component but retain fast settling on gain step
- *UbiNetics has developed an WCDMA RF chip with Renesas*

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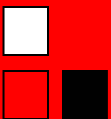
Multimode SoC Block Diagram



UbiNetics WCDMA SoC design

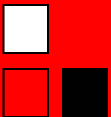
- Release 99, 384kbit/s class UMTS IP core
- Low power hardware centric design
- No DSP required for data centric operation
- ARM 9 class controller required (926 assumed)
- “Bolt-on” HSDPA support
- MM support “designed in”

Working with 2 major semis

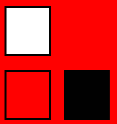
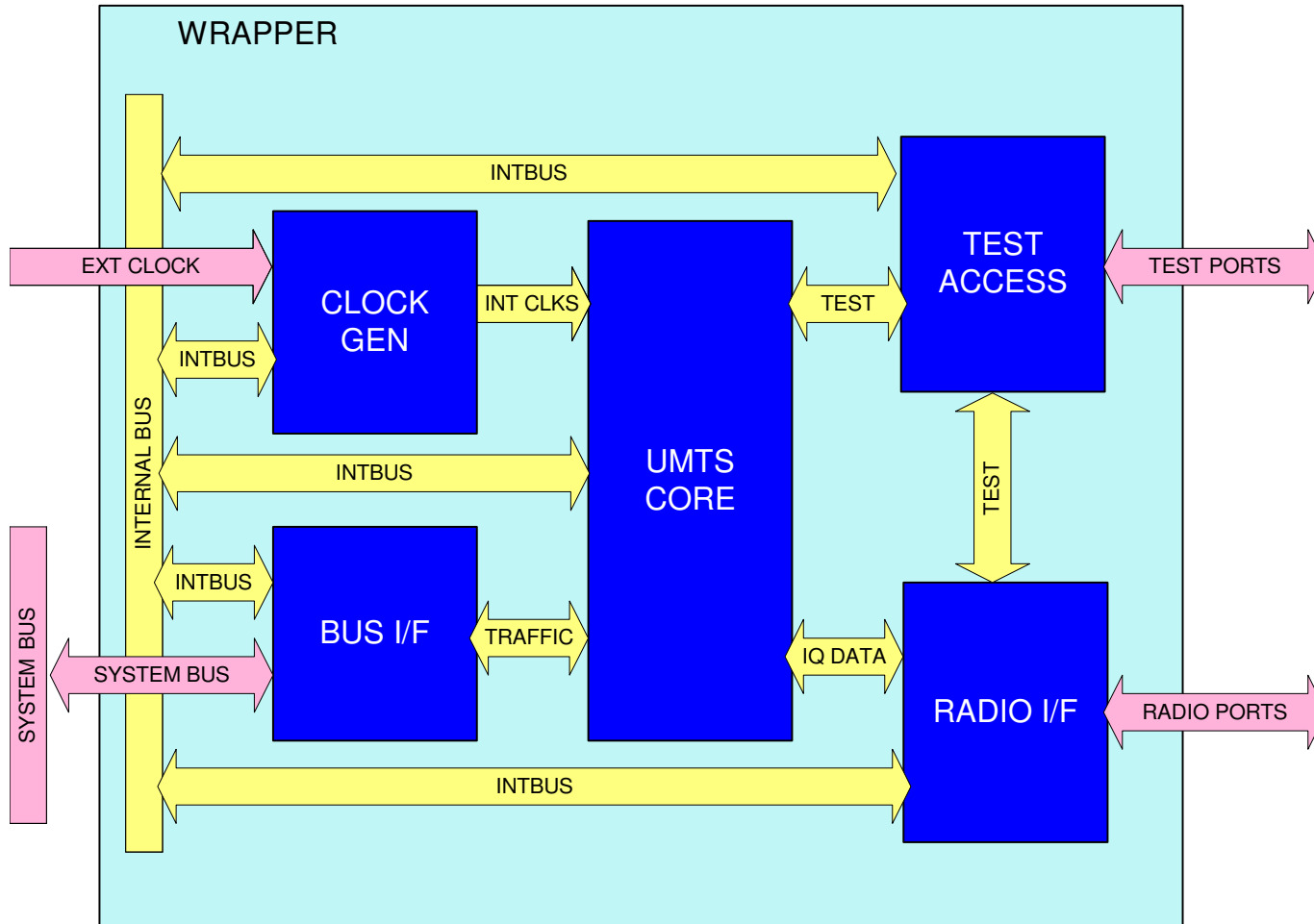


WCDMA HW/SW Partitioning

- Functions implemented in hardware
 - Traffic data path (chip and bit rate)
 - Bandwidth intensive processes (e.g. Psync peak sorting)
 - Low latency processes (e.g. power control)
- Remaining functions partitioned by
 - Size and complexity
 - ‘Intelligence’ residing in software (e.g. AGC loop)
 - Power consumption advantage
 - Bus bandwidth



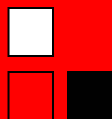
WCDMA H/W block diagram



WCDMA rolling-out, however..

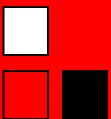
- The 'other' 3G standard CDMA2000 had a head start
- WCDMA will dominate long term, but
- CDMA2000 has a high rate, ~ 3 Mbps, DL extension 1x EV-DO in operation now
- Sprint and Verizon in US, KDDI in Japan using it
- Customers like high speed
- DoCoMo, Vodafone (Japan) and Cingular/AWE (US), WCDMA operators, are nervous

*Resultant push for **High Speed Downlink Packet Access** enhancement to WCDMA*



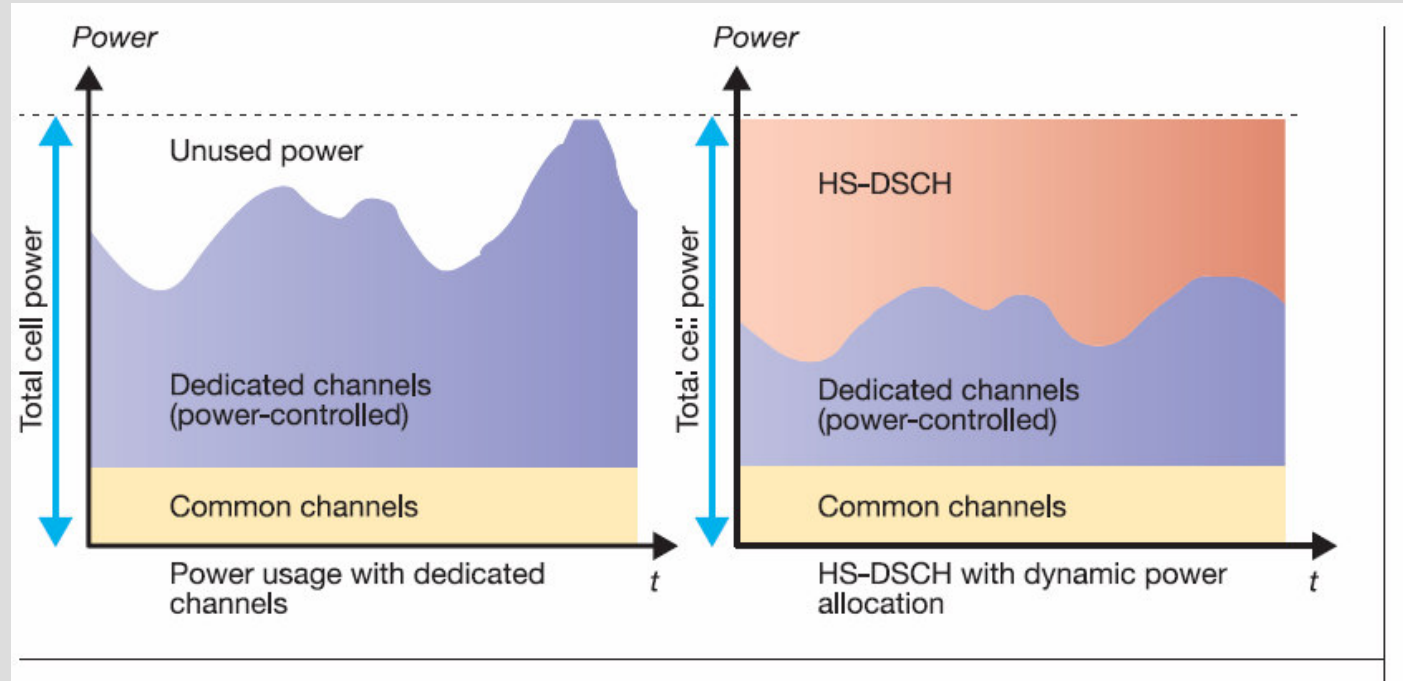
What is HSDPA?

- Increased packet data support in the DL
 - More efficient use of the available bandwidth
 - Increase maximum user throughput for downlink packet data
 - Increase the peak data rates to 14 Mbps
 - Reduce the latency for packet data
 - Minimise the need for additional control signalling
- Compatibility with R99
 - HSDPA is a straightforward enhancement to R99 architecture
 - All R99 techniques can be supported in an HSDPA network
 - R99 and HSDPA mobiles can co-exist on the same frequency
- Scalable UE Complexity
 - 12 categories of terminal capability
- Target urban and indoor environments



Essentials 1: Unused power

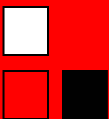
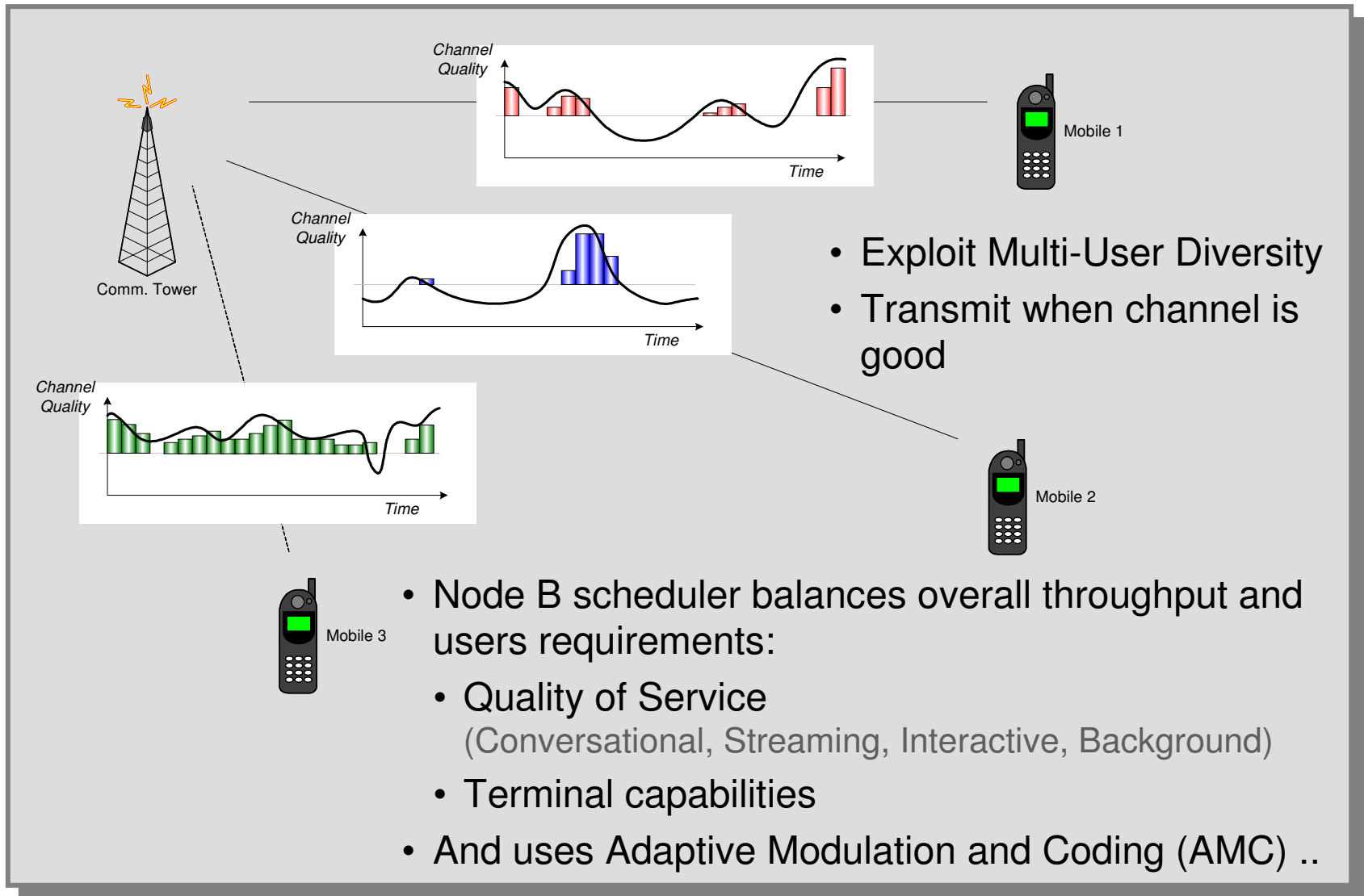
“Unused” power capacity allows greater throughput



By:

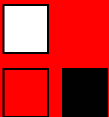
- User scheduling by Node B
- Adaptive modulation and coding (AMC)
- Hybrid automatic repeat request (HARQ)

Essentials 2: Scheduling

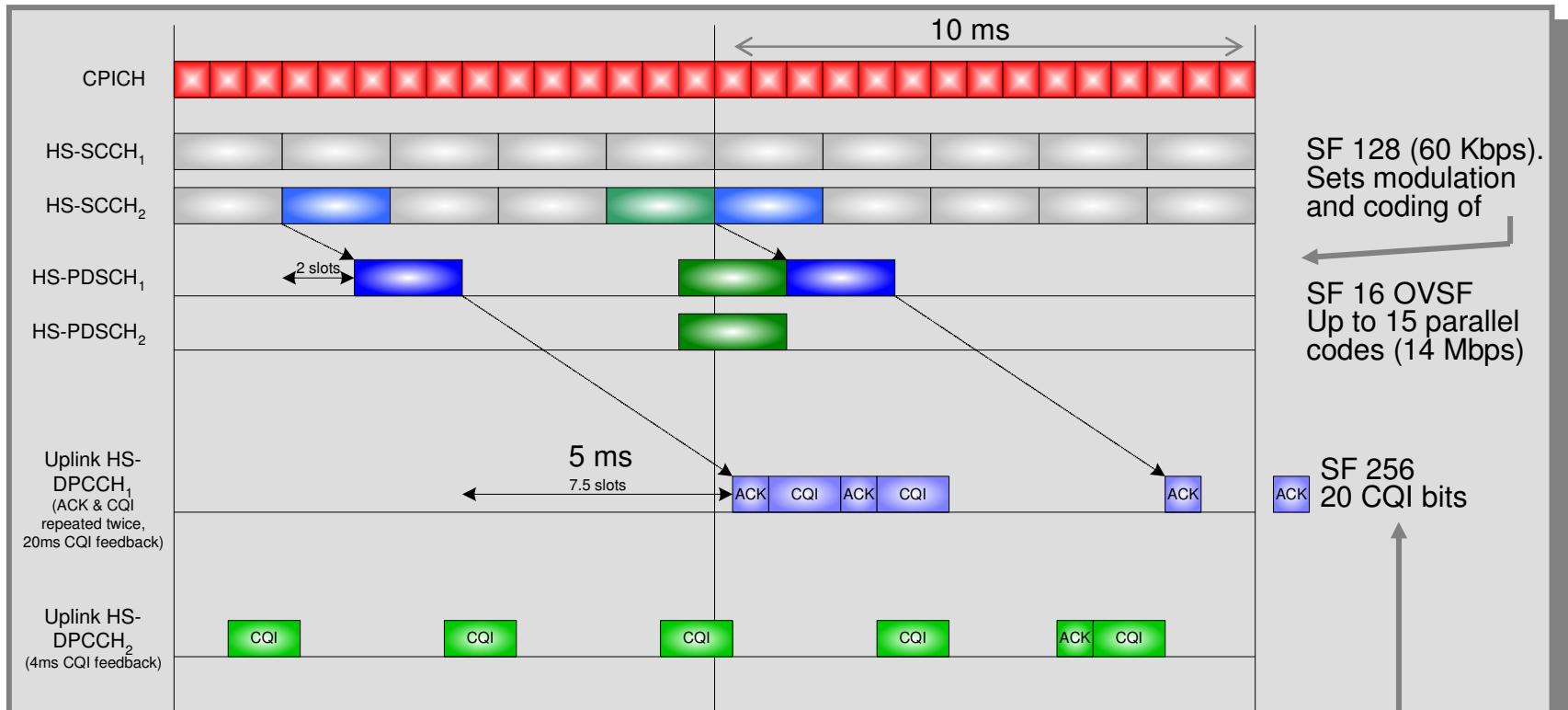


Essentials 3: AMC

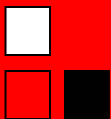
- Instead of adjusting the transmit power, change the “coding gain”
 - Send more user data and less error protection for good channels
 - Use higher order modulation to increase the data rate
 - use higher FEC coding rates
 - optional 16-QAM modulation
- Use multiple OVSF codes allocated to a single user
 - Send data over up to 15 codes simultaneously
 - Number of codes depends upon UE capability
- To schedule and set modulation Node B needs fast Channel Quality Indication (CQI) from terminal...



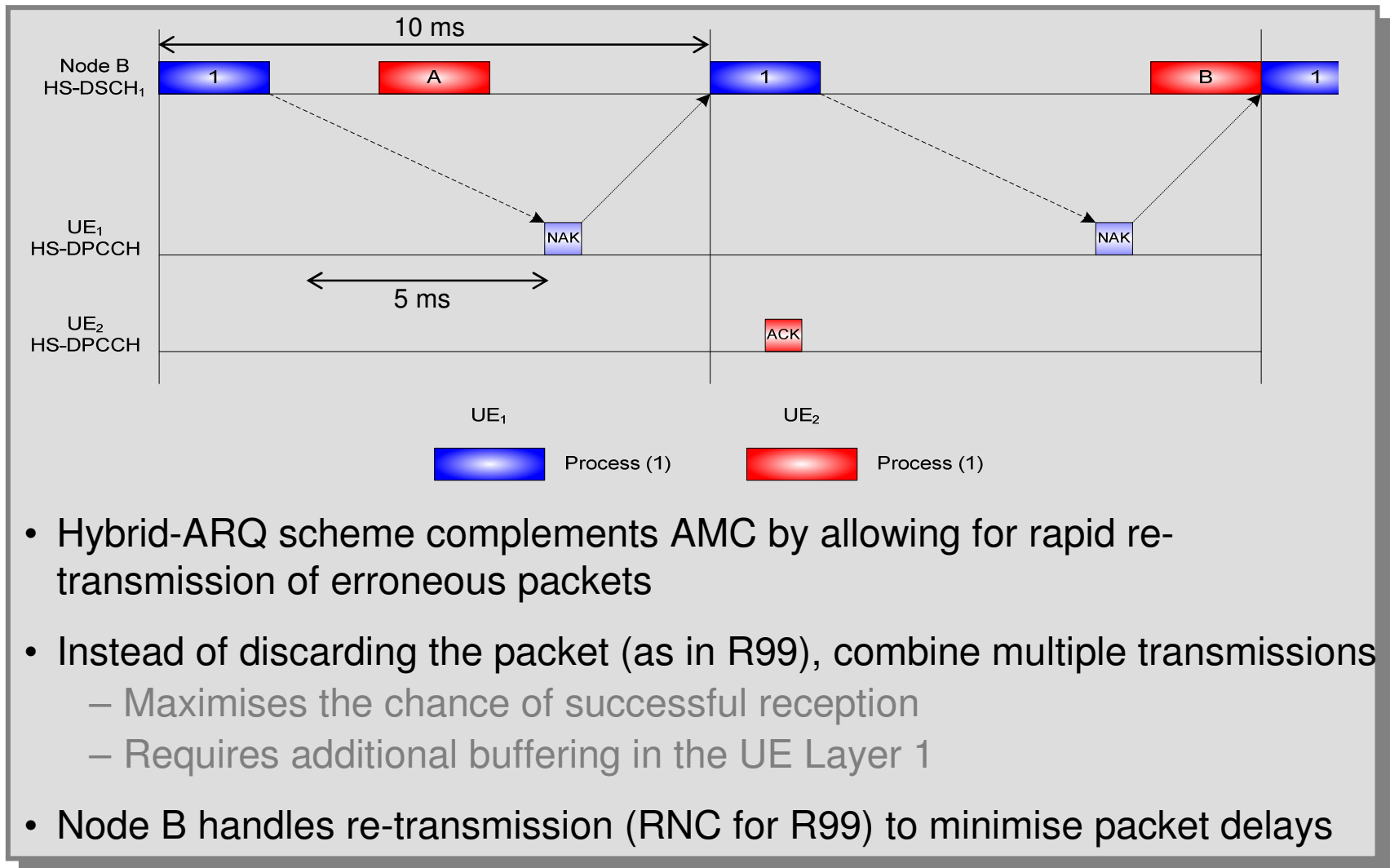
Essentials 4: CQI, and HS channels



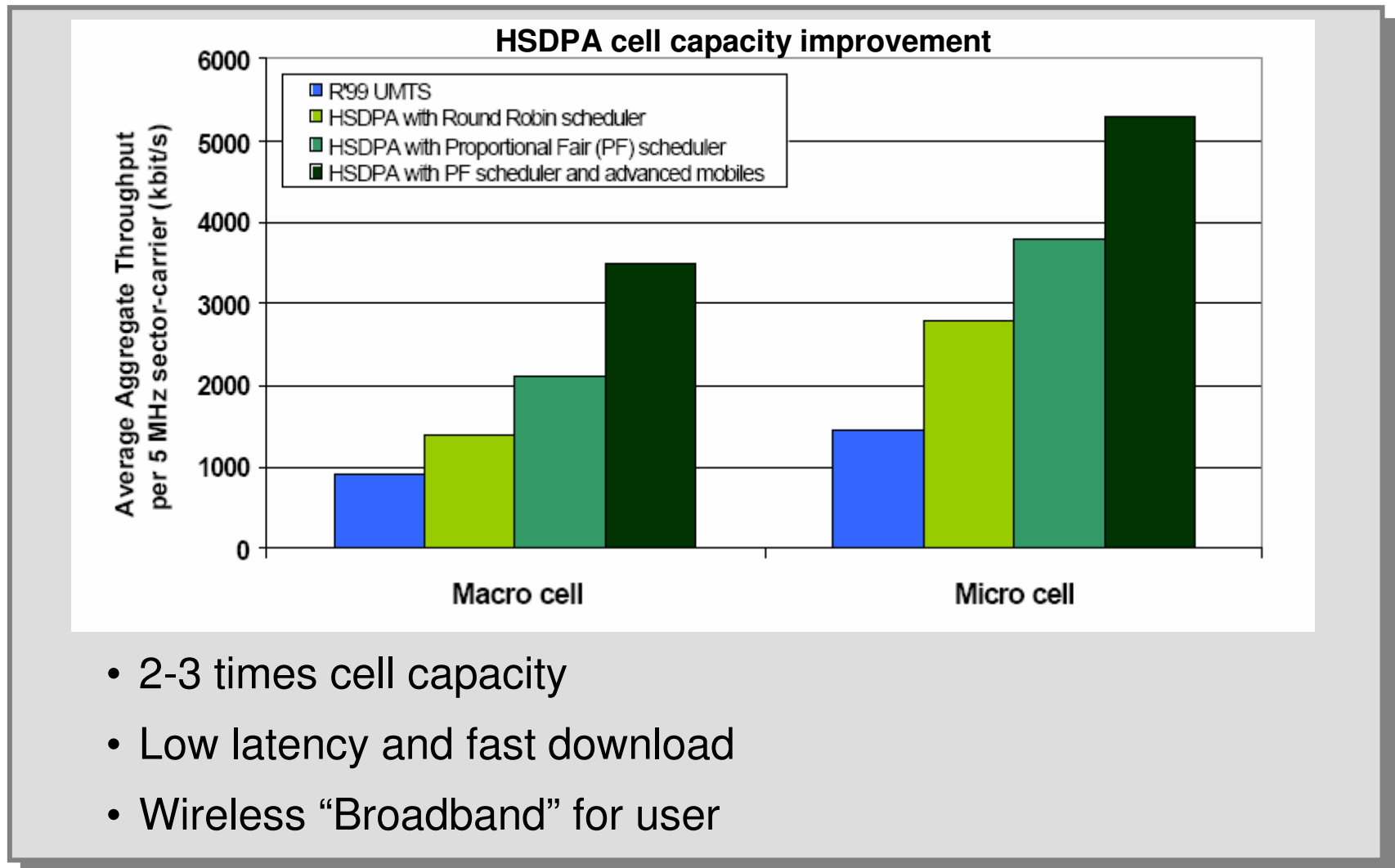
- UE measures the SIR of the CPICH and calculates best data rate
 - Aim to receive data with <10% PER
 - Consider UE capability
- CQI reported to Node B via dedicated UL channel HS-DPCCH



Essentials 5: HARQ re-transmission



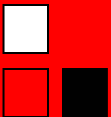
Essentials 6: HSDPA vs WCDMA



HSDPA Implementation

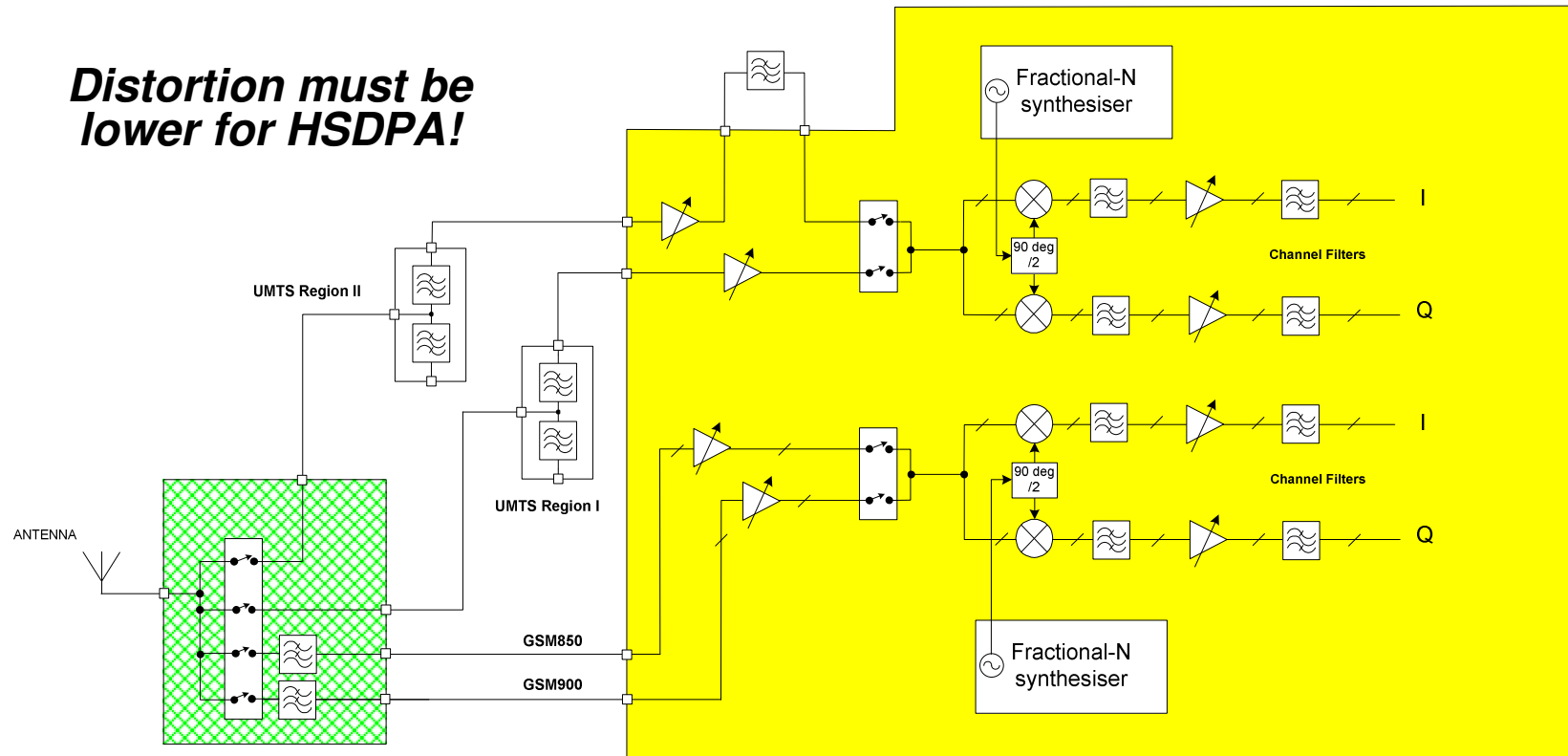
Principal Issues:

- Need better SNR for higher rates
- Better RF
- Equaliser not Rake
- HARQ buffers
- HS-MAC



RF considerations

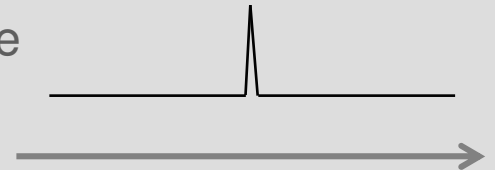
Distortion must be lower for HSDPA!



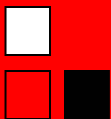
- Direct conversion receiver gives competitive design – low BOM
- But must minimise DC offset – **even more important with HSDPA**
- Trick is to remove DC component but retain fast settling on gain step
- ***UbiNetics has just started an HSDPA RF chip development with Renesas***

Equaliser

- Rake receiver combines multiple paths
 - Each path adds noise – Multi-Access Interference
 - Worse at low SF – OVSF correlation worse
- Equalisers particularly suited to HSDPA
 - HSDPA targeted at low mobility, high bandwidth users
 - Relatively static channel – plenty of time for equaliser to adapt
 - QAM-16 requires good SNR
- Equaliser measures the channel profile
 - Applies an inverse of the channel to the received data
 - No multi-access interference – improved Rx SNR!
- Increased UE complexity
 - Complex calculations to evaluate equaliser tap weights

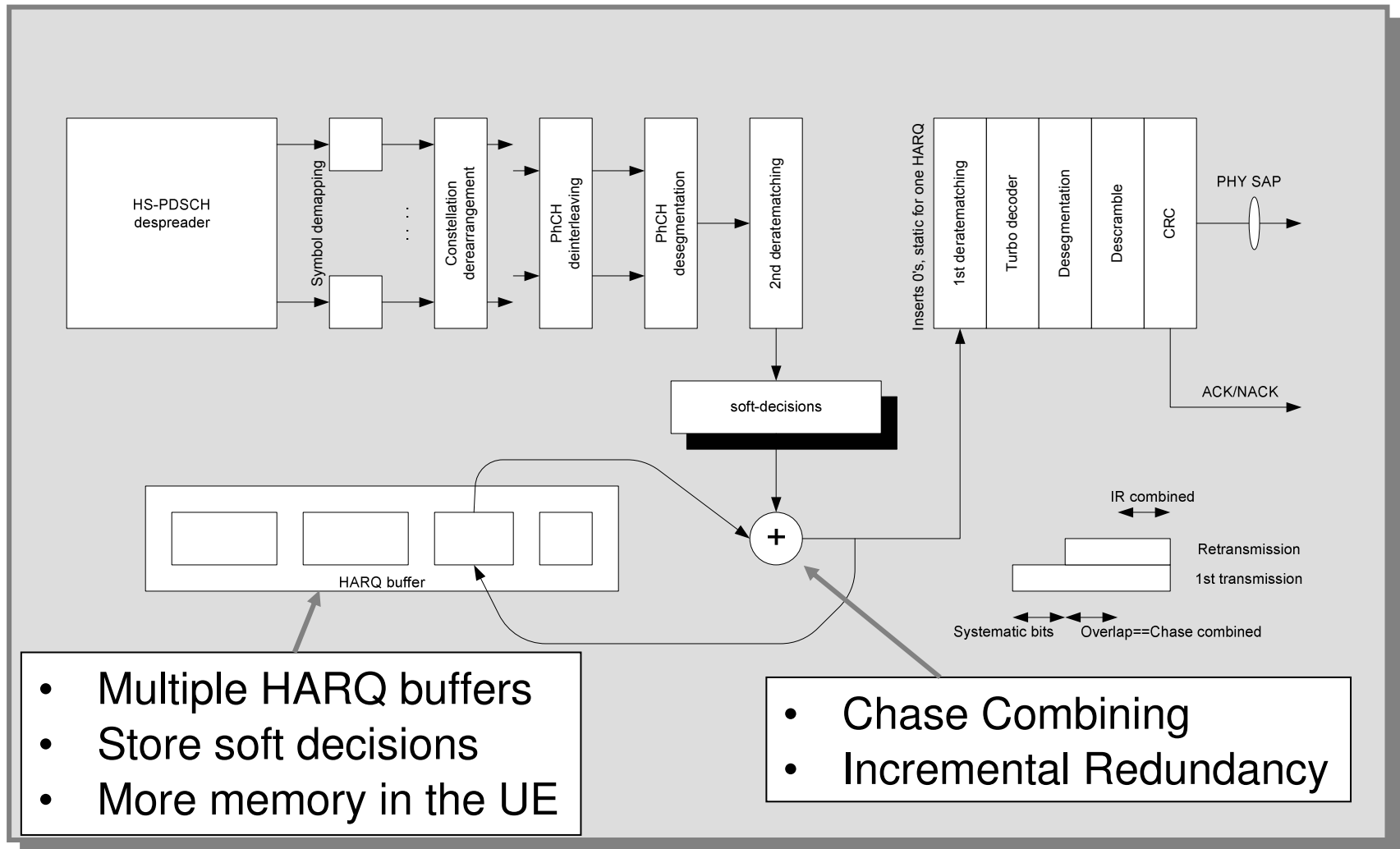


- G-RAKE (?)
- Linear filter HS-PDSCH receivers:
 - LMS-based equaliser
 - MMSE based equalisers



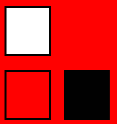
H-ARQ Combining

Bit rate processing (Turbo) block with soft-decision combiner

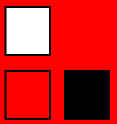
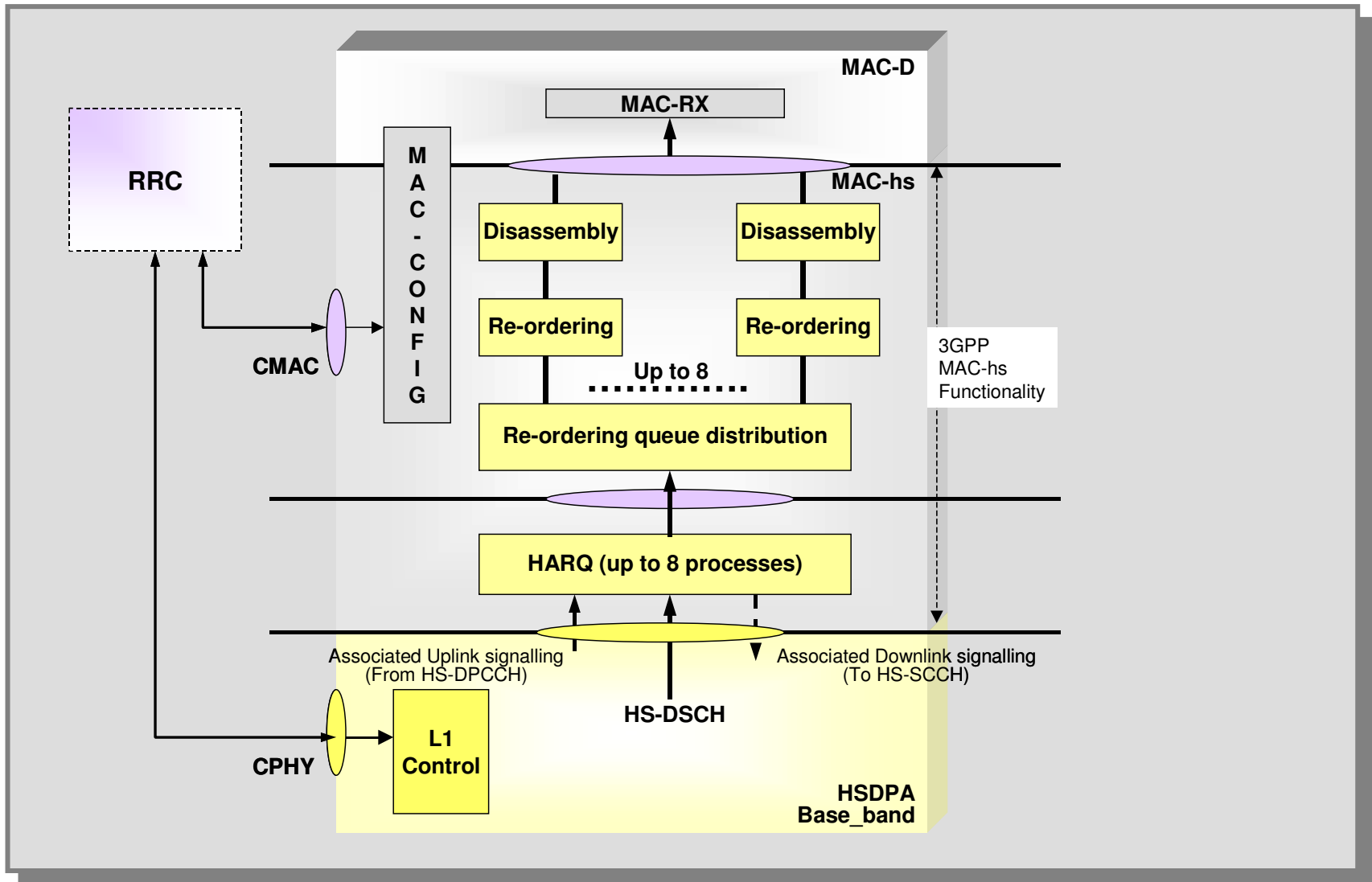


- Multiple HARQ buffers
- Store soft decisions
- More memory in the UE

- Chase Combining
- Incremental Redundancy

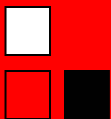
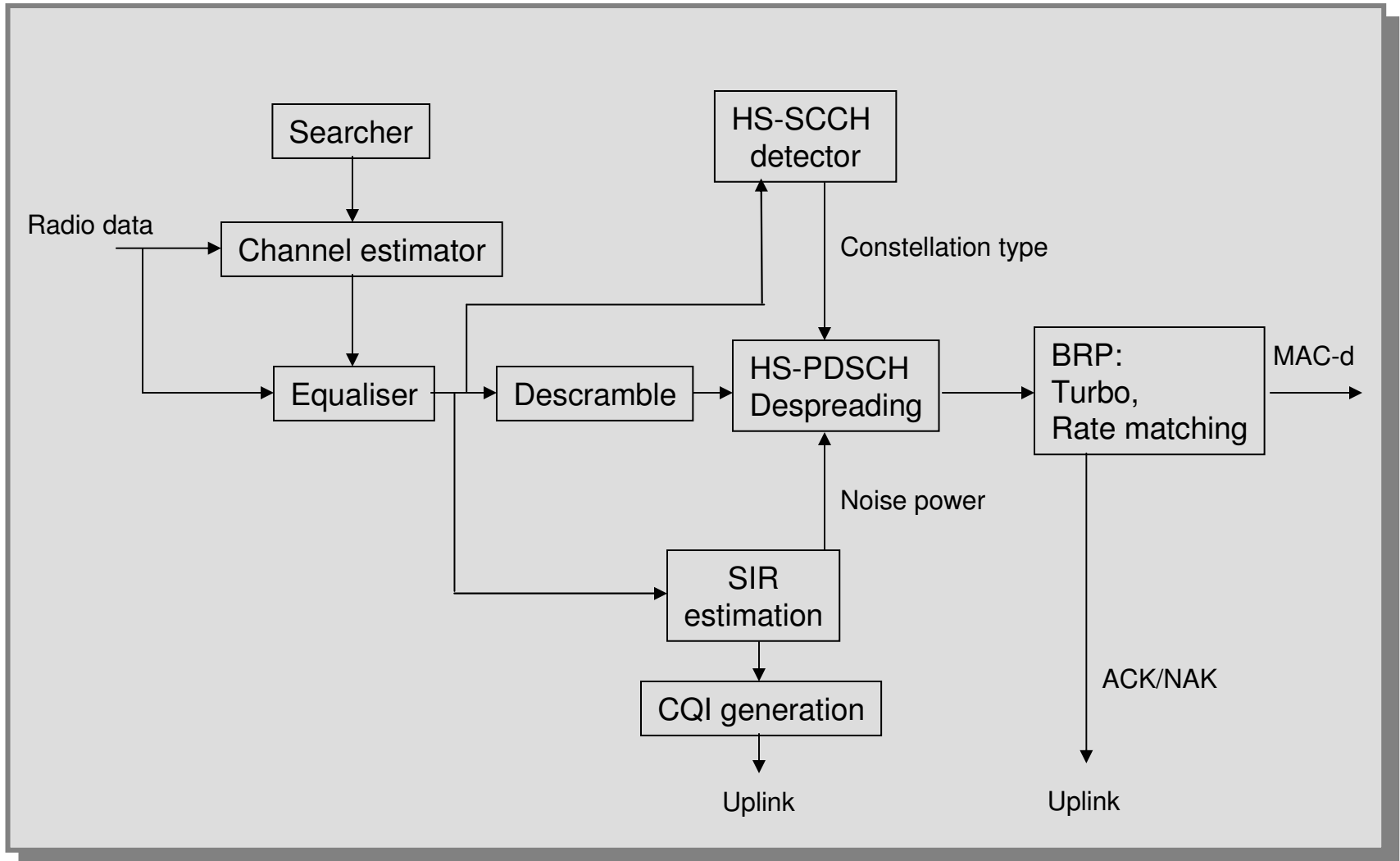


HS-MAC



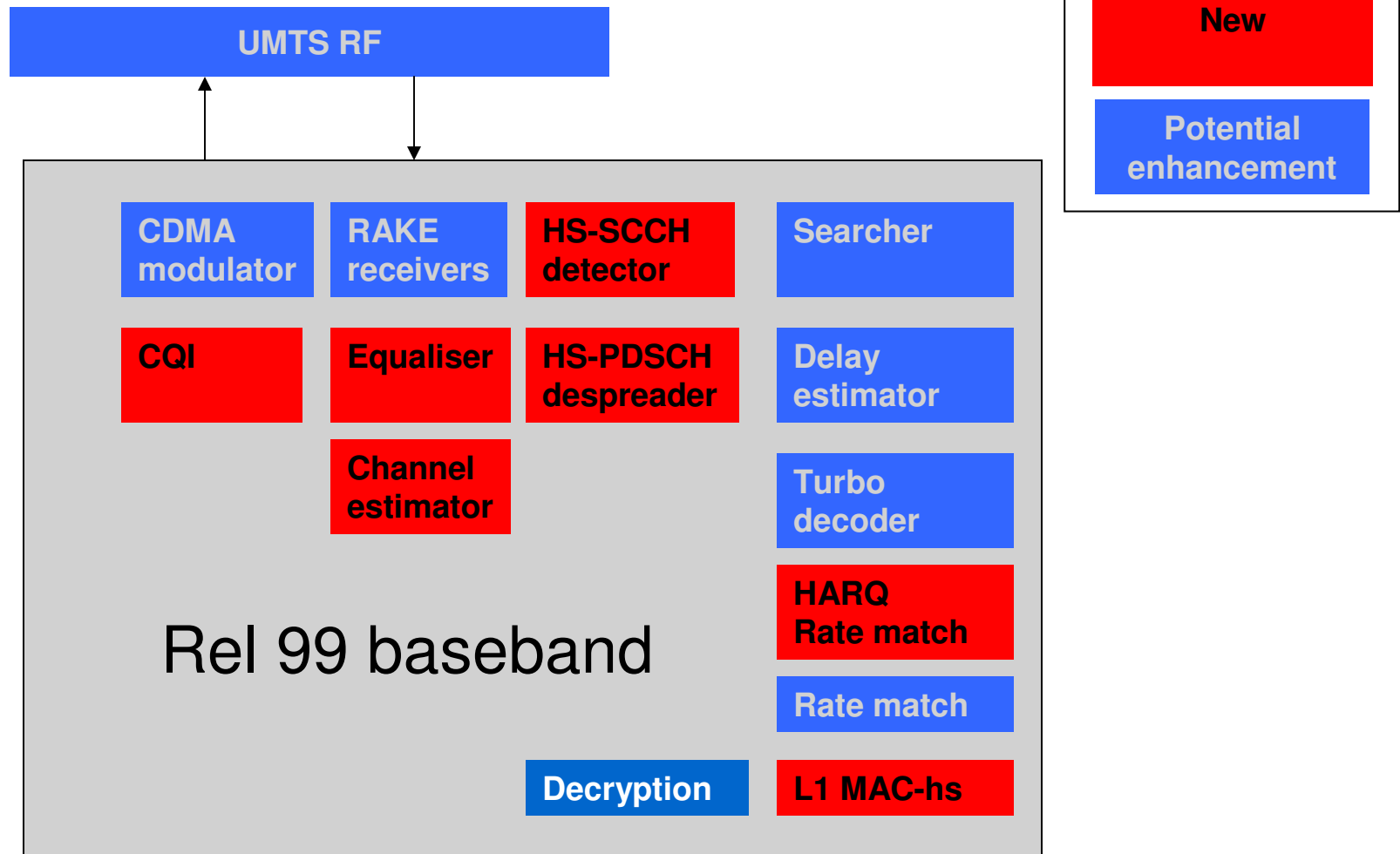
HSDPA receivers

Generic HSDPA L1 architecture



WCDMA Baseband migration

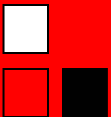
Typical block diagram for HSDPA entities



WCDMA Baseband migration

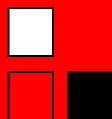
Typical HW/FW and SW split options

Entity	HW	FW	SW
Channel estimator	X		
Equaliser	X	X	
HS-SCCH detector	X	X	
HS-PDSCH de-spreader	X		
CQI generator		X	
CPICH SIR calculator	X	X	
Uplink HS-PDCCH	X		
BRP & HARQ	X	X	
MAC-hs	X	X	X



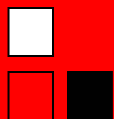
And the Up Link?

*That is another
story!*



Thank you

Questions?



Rel 5 Capability Classes

TABLE 1 TWELVE CATEGORIES OF HS-DSCH-CAPABLE TERMINALS.

Category	Maximum number of supported HS-DSCH codes	Minimum inter-TTI interval	Number of soft values in terminal's hybrid ARQ buffer	L1 peak rate [Mbit/s]	Modulation schemes
Category 1	5	3	19,200	1.2	16QAM, QPSK
Category 2	5	3	28,800	1.2	16QAM, QPSK
Category 3	5	2	28,800	1.8	16QAM, QPSK
Category 4	5	2	38,400	1.8	16QAM, QPSK
Category 5	5	1	57,600	3.6	16QAM, QPSK
Category 6	5	1	67,200	3.6	16QAM, QPSK
Category 7	10	1	115,200	7.3	16QAM, QPSK
Category 8	10	1	134,400	7.3	16QAM, QPSK
Category 9	15	1	172,800	10.0	16QAM, QPSK
Category 10	15	1	172,800	14.0	16QAM, QPSK
Category 11	5	2	14,400	0.9	QPSK
Category 12	5	1	28,800	1.8	QPSK

