

BIO - DATA

1. Name : Dr.M.JOSEPH

2. Date of Birth : 19.05.1969

3. Designation : Professor

4. Education:

College	Degree	Field of Specialization	Year	Class & % of Marks
National Institute of Technology Karnataka - Surathkal	PhD	Computer Engineering	Jan' 09	-
Govt. College of Engineering – Tirunelveli.	M.E	Computer Science & Engineering	Nov' 95	First & 69.0
Karunya Institute of Technology - Coimbatore	B.E	Electronics & Communication Engineering	Apr' 90	First & 73.5

5. Employment record in Institution and so on (starting from present position):

Universities/College	Designation	Department	Period
St. Joseph's College of Engineering and Technology, Vallam – 613 403	Professor and HOD	CSE	July 2008 to Till Date
Periyar Maniammai University, Vallam - 613403.	Asst. Professor & HOD	IT	Jan' 2008 to Jun' 2008
National Institute of Technology Karnataka - Surathkal	Research Scholar	Computer Engineering	Aug' 2004 to Jan' 2008
Periyar Maniammai College of Technology for Women, Vallam -613403	Asst. Professor & HOD	IT	Dec' 2002 to Aug' 2004
Periyar Maniammai College of Technology for Women, Vallam - 613403.	Asst. Professor	CSE	Jan'2001 to Nov' 2002
Periyar Maniammai College of Technology for Women, Vallam - 613403.	Sr. Lecturer	CSE	July'97 to Dec'2000
Periyar Maniammai College of Technology for Women, (PMCTW), Vallam - 613403.	Lecturer	CSE	Mar'96 to June'97
PSNA College of Engineering and Technology - Dindigul.	Lecturer	ECE	Dec'91 to Sep'94

5.1 Teaching Experience: 12 ½ years; **5.2 Research Experience:** 3 ½ years

5.3 Total Experience: 16 years.

6. Publications

6.1 Books Published

1. M. Joseph, Elements of Compiler Design, Dhanam publications – Chennai, June 2001.
2. M. Joseph, System Software: A Simplified Approach, FireWall Media - New Delhi, July 2006.

6.2 Publications in International Journals (Refereed)

1. M.Joseph, Narasimha B.Bhat and K.Chandra Seakran, Survey of HDL Compiler Optimization Techniques, International Journal on Information Processing, Volume 1, Number 1, April 2007, pages 51 - 62.
2. Technology Driven High-Level Synthesis (Journal Version) *Submitted to IEEE Design and Test of Computers – (DTSI-2008-10-0062), for the special issue in HLS.*

6.3 Publications in International Conferences

1. M.Joseph, Narasimha B.Bhat and K.Chandra Sekaran, Technology driven High-Level Synthesis, Proceedings of International Conference on Advanced Computing & Communication - ADCOM 2007, IEEE, Indian Institute of Technology Guwahati, INDIA, December - 2007, pages 285 - 490.
2. M.Joseph, Narasimha B.Bhat and K.Chandra Sekaran, Right Inference of Hardware in High-Level Synthesis, Proceedings of International Conference on Information processing - ICIP – 2007, Bangalore, August – 2007, pages 381 - 390.
3. Floating-Point Adder in Technology Driven High-Level Synthesis, 1st Microsystems and Nanoelectronics Research Conference (MNRC 2008) – IEEE, Canada, Oct – 15, 2008. (Accepted).

6.4 Publications in Other Journals

1. M.Joseph, Inter Procedural Analysis and Optimization, Periyar Research Journal (Research Journal from Periyar Maniammai University, Thanjavur, India), Volume 2, Issue 2, June – 2004.

6.5 Publications in National Conferences

1. M.Joseph and J.Niraimathi, An Analytical Study of Data Structures for symbol Tables in Compilers, Proceedings of National Technical Symposium IT Quest 2001, Karunya Institute of Technology – Coimbatore , Sep' 2001.
2. M.Joseph and S.Sumathi, Internet Based Teaching, Proceedings of 10th science conference on Educational Technology & Information Technologies, Bharathidasan University – Trichy , Feb 2001.
3. M.Joseph and S.Sumathi, Role of IT in Biotechnology, Proceedings of the 11th science conference on Bio-Technology & Information Technology, PMCTW - Vallam on Feb 2002.
4. M.Joseph, Code Improving Transformations in Compilers, Proceedings of Vth ISTE National Convention, CSI Polytechnic College – Salem, Sep-2003.

6. 6 Key Contributions

Developed Technology driven High-Level Synthesis (THLS) strategy and tool (compiler) in C++, modifying Icarus Verilog Compiler, an open source HLS tool for IEEE standard 1364 HDL Verilog.

7. Membership in Scientific and Professional Societies

1. ISTE - INDIAN SOCIETY FOR TECHNICAL EDUCATION - Membership No: LM 14334
2. CSI – COMPUTER SOCIETY OF INDIA - Membership No: N9069701.

8. Honors and Awards

1. **IEEE Reviewer** for the VLSI Design 2006 International conference, sponsored by the VLSI Society of India, the ACM SIGDA, the IEEE Circuits and Systems Society, and the IEEE Computer Society (DATC and VLSI -TC), held in Hyderabad, India, from January 3-7, 2006.
2. **IEEE Reviewer** for ICIS 2008, IEEE Region and colloquium and Third International Conference on Industrial and Information Systems, December 2008, IIT Kharagpur, India.
3. **Collaborating Colleague** for Novel Techniques for High Performance Filed Programmable Logic Devices, Narasimha B Bhat, an ACM Publication 1993.
4. **Member in Academic Council of** Periyar Maniammai University, Thanjavur, from August 2007 to August 2008.
5. **Chairman, Board of Studies of IT** Department of Periyar Manimmai University, Thanjavur, from August 2007 to August 2008.
6. **Member in the Board of Studies of IT** Department of Bharathidasan University from 1.10.2002 to 30.9.2005 for UG & PG Courses.
7. Received **NITK Research Fellowship** for doing PhD since 2004 to 2008.
8. **Chairman for CSE board** for November 2009 exams of Anna University, Trichirapalli.
9. **Chairman for IT board** for November 2003 exams of Anna University – Zone 8 (Trichirapalli).
10. **Member in the IT Syllabus Committee- ANNA University** - Chennai, for the period 2004 – 2008.
11. **Member in the IT Syllabus Committee- Bharathidasan University**, Trichy, for the period 1998-1999.
12. **Best Teacher Award** for the academic year 1998 - 99 – PMCTW, Vallam.
13. CSI - Student Branch Counselor of Periyar Vallam Chapter for the term 2001-2002.
14. Secretary of CSI -Thanjavur Chapter for the terms 2002-2003 & 2003-2004.
15. Secured **37th rank** in SSLC exams conducted by the Board of Secondary School Education, Tamil Nadu in 1984.
16. **Received Rural Merit Scholarship** from IX to XII by Government of Tamil Nadu.

9. List of Conferences, Short-term Courses etc. attended/organized

9.1 Attended

1. Attended Induction Training Programme at RVS College of Engg in Dec'92, by ISTE Chapter of RVSCET – Dindigul.
2. Attended CSI National conference BIG'97 at Vizagapatnam in Aug'97.
3. Attended Vth International Conference on High Performance Computing at Chennai in Dec'98 organized by IEEE & CSI at Taj Coromandal.
4. Attended conversion training program at Foreman Training Institute, Bangalore on May'99
5. Visited DRDL Laboratories Hyderabad in Nov'99.
6. Attended the workshop SEETAC 2000 organized by SQI Chennai on Feb 2000, at Chennai.
7. Attended Internal Auditor Course for ISO organized by Germanisher Llyod - Chennai, Industrial services during Mar'27 - Mar'29, 2000
8. Attended IEEE Student Congress 2000 organized by IEEE Chennai on Aug'2000
9. Attended one day workshop on Business English Course (BEC) on 10-2-2001 by British Council, Chennai.
10. Attended the 10th Conference on Educational Technology Organised by National Science Congress Tamil University, Tanjavur & Bharathidasan University Trichy, on 24th, 25th Feb'2001 at Bharathidasan University, Trichy.
11. Attended ISO 9000: 2000 one day workshop at Trichy organized by Germanisher Llyod Industrial services – Chennai, and BHEL – Trichy on 20-07-2001
12. Attended a workshop on Result Based Management at PMCTW, Vallam, Organized by PMCTW, during 10-11 August '2001.
13. Attended 11th science Conference on 'Bio Technology and Information Technology' on 9,10-Feb'2002, organized by Tamil University -Thanjavur, Periyar Maniammai College of Technology for Women – Vallam.
14. Attended the Vth ISTE National convention at CSI Polytechnic College – Salem on 18.9.2003 and 19.8.2003.
15. Attended the AICTE - ISTE sponsored Short Term Training Program on Cryptography and Network Security from 5.7.2004 to 16.7.2004, at PMCTW – Vallam.
16. Attended the World Semiconductor Forum 2004 – GOSPL organized ST Microelectronics at New Delhi from 30.10.2004 to 31.10.2004.
17. Attended International Conference on Information processing - ICIP – 2007, Bangalore, August – 2007.

18. Attended International Conference on Advanced Computing & Communication - ADCOM 2007, IEEE, Indian Institute of Technology Guwahati, INDIA, December - 2007.

9.2 Organized

1. **Convener - Fusion'98**, National Level Technical Symposium at PMCTW, Vallam, conducted during September 1998.
2. Co-Convener - I-fiesta 2000, National Level Technical Symposium at PMCTW, Vallam, conducted during September 2000.
3. **Organizing Chair – CONMICRO – 2002**, a National Conference on Microcomputers, organized by CSI Thanjavur Chapter and PMCTW - Vallam on 13, 14 Dec' 2002.
4. **Convener – InfoVentura 2003**, a National Technical Symposium at PMCTW – Vallam, conducted during August 2003.
5. **Convener for the ISTE sponsored workshop on VLSI Design and Embedded systems** conducted on 24 and 25 August 2004.
6. **Convener - First Annual Day Celebrations of Periyar Manimmai University**, Vallam held in May 2008.

10. Sponsored Projects

1. Neural Networks as Decoders in Error correcting codes.

Year	:	1998 - 2000
Scheme	:	R&D
Sponsoring Authority	:	AICTE - New Delhi
Designation	:	Expert Member
Amount	:	Rs.5 Lakhs

2. Establishment of Internet programming lab

Year	:	2000 - 2001
Scheme	:	MODROBS
Sponsoring Authority	:	AICTE - New Delhi
Designation	:	Co-Co-coordinator
Amount	:	Rs.5 Lakhs

3. Establishment of Multimedia Lab

Year	:	2001 - 2002
Scheme	:	MODROBS
Sponsoring Authority	:	AICTE - New Delhi
Designation	:	Co-Coordinator
Amount	:	Rs.4 Lakhs

11. Consultancy Works

1. Developed Software for computer billing in Tamil for Mobile Book Depot – Periyar Educational and Charitable Society – Chennai during 1997, through PECS of PMCTW.
2. Developed a Software for computer Billing in Tamil for Puthaga Poonga – Kumbakonam during 1997, through PECS of PMCTW
3. Developed software for BEST Entrance Exam coaching center- Thanjavur during 1997 through PECS of PMCTW.