

FULL NAME Jugantor Chetia

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OBJECTIVE:

Seeking an opportunity to utilize my skills and knowledge and go into research work in the field of VLSI and embedded systems.

PERSONAL SKILLS :

Sincere, comprehensive problem solving abilities, good communication skills, Ability to deal with people diplomatically, willingness to learn, Team spirit.

EDUCATIONAL QUALIFICATION:

	School	Board	Subjects	%Marks
10 th	Kendriya Vidyalaya Namrup, Namrup, Assam	C.B.S.E.	Hindi, English, Mathematics, Science, Social Science	86.6
12 th	Kendriya Vidyalaya Namrup, Namrup, Assam	C.B.S.E	English, Physics, Chemistry, Mathematics, Biology	87.8

- Awarded Certificate of Merit by CBSE for being among the top 0.1% of successful students in AISSE in Science with practicals

College: National Institute Of Technology, Durgapur
Deptt. : Electronics and Communication Engineering

Sl. No.	Semester	Marks (SGPA)
1	1 st	9.03
2	2 nd	9.07
3	3 rd	8.93
4	4 th	9.21
5	5 th	9.14

CGPA: 9.08
Departmental rank (upto 5th semester) 3rd

DEPARTMENTAL SUBJECTS STUDIED

1. Computer Architecture and Organisation
2. Digital Electronics
3. Integrated Electronics
4. Analog Circuits
5. Analog Communication
6. Power And Industrial Electronics
7. Engineering Electromagnetics.
8. Network Theory
9. Microprocessors and its applications
10. Digital Communication

AREA OF INTEREST

1. **Embedded Systems on a Chip.**
2. **VLSI**
3. **Electronics Design and Automation**

Technical Skills:-

- Operating Systems Windows98, Windows XP
- Programming Languages: 1) C
2) VHDL, Verilog –HDL with simulation, synthesization and implementation in FPGA and CPLD kits
- Application Softwares : 1) MATLAB
2) Circuitmaker
3) Electronics Workbench
4) Multisim
5) Maxplus II

- Data Structures
- Microprocessors – 8085 and microcontrollers -8051

PROJECT DETAILS:

1) PROJECT NAME (ON GOING PROJECT):

Design of On chip ASCII Debugger using VHDL/Verilog

UNDER GUIDANCE

: Mr. Anirban Ghosh
Member , Technical Staff.
MAGMA Design Automation India Pvt. Ltd.
e-mail ID: anirban.theghosh@gmail.com

or

anirban@Magma-DA.com

Mobile Number: 09343217454

DESCRIPTION:

The Project started in January'07 and is still going on .It is being done on my college FPGA kits. The project is being done on its frontend part using Verilog/VHDL. DW_debugger provides a low-gate-count implementation of an on-chip ASCII debugger. This component communicates through the use of two pins in serial ASCII, and allows you to read the `rd_bits` and write the `wr_bits` wires.

The tasks that are being done are :

- 1) Developing a testbench using VHDL/ Verilog.
- 2) Verifying in a simulator.
- 3) Implementing into FPGA kits.

2) PROJECT NAME :

Various Modulation and Demodulation techniques employed in Communication systems.

UNDER GUIDANCE:

Dr. P.R. Sahu,
Assistant Professor,
Department of Electronics and Communication Engineering
Indian Institute of Technology
Guwahati 781039, INDIA

Phone:+91-361-258-2517(O)
Fax: +91-361-2582542, 2690762
Email: prs@iitg.ernet.in

DESCRIPTION: The project was done during May'06. Tasks that were done during this project were:

1. Modulation and demodulation of AM and FM using different techniques and ICs and in MATLAB .
2. Realization of Pulse Code Modulation Technique using digital communication Trainer Kit ST 2101 .

ACHIEVEMENTS:

1. Third rank in University and first in Branch in first year of B.Tech.
2. Awarded University scholarship and Merit Certificate in first and second year of B.Tech.
3. Awarded Merit Certificate in Kendriya Vidyalaya Sangathan Fourth Junior Mathematics Olympiad 2001-02 (ranked 40th in merit among the first 1000 students).
4. Awarded Merit Certificate in 2nd Chemistry Olympiad Organised by Society for Chemical Education ,Assam (Ranked 10th in merit).
5. Won 2nd prize in Robowars during Aarohan 2k6– A National Level Technical Festival of NIT Durgapur.
6. One of the finalists of the International Project Demonstration Event organized by BITM, Jamshedpur,2006-07 , but could not attend due to problems at home .

EXTRA CURRICULAR ACTIVIES AND HOBBIES:

- Technical coordinator of CCA (Center for Cognitive Activities), The Science Club of NIT Durgapur .
- Active member of NSS(National Service Scheme) , Durgapur Chapter
- Active member of Indian Society for Technical Education

PERSONAL DETAILS :

Name	:	JUGANTOR CHETIA
Age & DOB	:	19 years, 01-03-1987
Sex	:	Male.
Marital Status :		Single
Nationality	:	Indian
Address	:	c/o U.N. Chetia South Colony , P.O. N.T.P.S. Dist. Dibrugarh. ,Assam-786623 India.
Languages known	:	English, Hindi,Assameese

References :

Will be provided on request.

Declaration

I hereby declare that the above written particulars are true to the best of my knowledge and belief.

JUGANTOR CHETIA

Place: Durgapur

Date:22/01/2007