

Inter American University of Puerto Rico, Bayamón Campus
School of Engineering
Electrical Engineering Department

ELEN 3311 — Electronics I
Class Calendar

Professor: José Oscar Mur Miranda, jomur@bc.inter.edu
Office: G131, office hours will be held Monday 1:30pm-3pm and Tuesday 2:30pm-4pm.
Lecture: Wednesday, 6pm-7:20pm, 7:30pm-8:50pm, G149
Laboratory: Monday, 6pm-8:50pm, G247
Webpage: <http://geocities.com/jomurmiranda/>
Problem sets: Problem sets will be posted on the website of the class or given out in class on selected Wednesdays and the due date will be specified. The final grade will be based heavily on problem sets. Collaboration is allowed, but not encouraged. **The work submitted must be your own.**
Laboratories: A final design laboratory will be graded pass/fail.
Grading: Assignment of grades will be based on the final score and the professor's assessment of the effort and understanding of the material in a case-by-case basis.

Calendar:

February	9	Wednesday	Three terminal devices, op amp definition; ideal op amp resistive circuits
	14	Monday	Non-ideal op amp; Ideal op amp linear circuits
	16	Wednesday	Piece-wise linear op amp model, PS1 out
	21	Monday	No class
	23	Wednesday	Semiconductor Physics, PS1 in
	28	Monday	Op amp lab
March	2	Wednesday	Piece-wise linear diode, PS2 out
	7	Monday	Piece-wise linear zener, PS1 rework out
	9	Wednesday	Load line, nonlinear diode, logarithmic circuits, PS2 in (Fri), PS3 out
	14	Monday	Diode switch, piece-wise linear op-amp
	16	Wednesday	Linearization, large & small signal PS3 in (Fri)
	21	Monday	No class
	23	Wednesday	No class
	28	Monday	Relaxation oscillations
	30	Wednesday	No class
April	4	Monday	BJT physics, linear BJT model
	6	Wednesday	General BJT circuit, common emitter, input & output impedance
	11	Monday	Review
	13	Wednesday	Midterm
	18	Monday	No class
	20	Wednesday	BJT follower, saturation & cut-off, bias, PS4 out
	25	Monday	MOSFET model, common emitter, saturation & cut-off, bias
	27	Wednesday	MOSFET follower, input & output impedance, PS4 in, PS5 out
May	2	Monday	Small signal, common emitter and follower gain (MOSFET and BJT)
	4	Wednesday	Differential pair, PS5 in, Design laboratory out
	9	Monday	Laboratory
	11	Wednesday	Review/Laboratory
	16	Monday	Laboratory
	18	Wednesday	Review/Laboratory, last day of classes
May 21 – 27		Finals' Week	