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SNEHAL GOR

Objective :

To excel in the field of embedded system design and putting into productive use of the insight gained in Electronics and software to the growth of company while simultaneously continue to enhance my caliber.

Experience : 10+ months

Key Skills & Competence :

Programming Language : C\C++, VC++, Assembly, Embedded C, Different flavors of Linux, Shell Scripting, RTOS Basics
Development Tools : Rational Rose, Matlab, Octave, WaveLab, Resonance Cross Compiler, MPLAB Cross C Compiler, Code Composer Studio.
Processor Architecture : TMS320C67x DSP processor, Intel 8085/8051 Microcontroller based systems, PIC Microcontroller (PIC16 and PIC18 series), and FPGA/CPLD based Systems.
Operating Systems : Linux, Windows

Education :

Qualification	College/School	Year	Marks
Master of Science in Advanced Embedded Systems Design	International Institute of Information Technology, Pune	2006	3.5/4.0
Bachelor of Engineering (Electronics)	Sarvajanik college of Engineering and Technology, affiliated to South Gujarat University, Gujarat	2004	66.3 % (First class with Distinction)
12 TH	T & T. V. Sarvajanik High School, Surat	2000	78.92%
10 TH	Sir J. J. Mishra Shala, Surat	1998	83.43%

Achievements :

- Received Valuable contribution award from Patni in first six months of service.
- Overall Second Topper in MS-Advanced Embedded System Design.
- Received Scholarship for academic excellence in first semester of MS-Advanced Embedded System Design.
- **GATE-2004 95.97 percentile** in Electronics and Communication Engg.
- Presented **Paper (IEEE Conference Publication) in ADCOM 2006** – International Conference on Advanced Computing and Communication – been held at **NITK** during 20 – 23 Dec 2006.
TOPIC: “Quantitative Analysis of Finite word length effect in Wavelet Transform and Filter Banks”

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Work Experience

- 1) **Organization** : Patni Computer System Ltd.
Base Location : Pune
Job Profile : Software Engineer (Medical Image Processing)
Experience : 10 months (Nov 2006 - Till date)

Job Profile :

- Preparing technical architecture and design
- Developing algorithms in the field of Digital Image Processing and Mathematics using C/C++ language
- Developing Graphical User interface
- Preparing Project plan

- 2) **Organization** : International Institute of Information Technology
Base Location : Pune
Job Profile : Student Lab Assistant (**Part time job**)
Experience : 21 months (Jan'05 – Sept'06)

Job Profile :

- Develop and debug algorithms in the field of Digital Signal processing and Mathematics using C language
- Preparing documents for Lab
- Conducting and taking care Lab

Projects

List of Some Significant Projects

01. Contrast Agent Enhancement(CE) Artifact Removal from DICOM Mu map images
02. CT/PET Image Processing Tool
03. Implementation of JPEG 2000 Encoder and decoder
04. Finite Word length effect in Filter banks & Wavelet Transform
05. Temperature Controller using Fuzzy Logic
06. Creation of Octave based computing package
07. Secure Data communication using Cipher Technology

Title	Contrast Agent Enhancement(CE) Artifact Removal from DICOM Mu map images		
H/W & S/W	VC++	Team	3 member
Description	Objective of project is to identify contrast enhanced area in DICOM Mu map images and correcting the same. Currently we are working on segmentation and region growing part of algorithm. At later stage we may need to build intelligence to classify CE area. As a one part of project we have developed Multiplanner Reconstruction (MPR) and Maximum Intensity Projection (MIP) functionality.		

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Title	CT/PET Image Processing Tool		
H/W & S/W	VC++	Team	4 member
Description	Objective of project was to develop Image Processing tool with User Interface. User can perform Image processing function like filtering, contrast enhancement and some basic mathematical operations on DICOM images. One of the main activity of the project was to mapping of images as per patient body coordinates		

Title	Implementation of JPEG 2000 Encoder and decoder		
H/W & S/W	C	Team	1 member
Description	Objective of this project was to perform image compression and decompression using JPEG 2000 standard. I've implemented main blocks only like preprocessing, wavelet coding, arithmetic coding and some post processing steps.		

Title	Finite Word length effect in Filter banks & Wavelet Transform (MS Thesis)		
H/W & S/W	C	Team	1 member
Description	Objective of thesis is to do inductive analysis of finite word length effect in filters, filter banks and wavelet transform and present paper on same. As embedded technology becoming more popular we have to face rigid requirements of area, power and cost. My thesis looks into effects of reducing bit length to represent any number on the response of filters, filter banks and wavelet transform. Also wavelet transform and filter banks are one of the main blocks in now a days popular Audio/Video standards like, JPEG 2000, MPEG etc.		

Title	Temperature Controller using Fuzzy Logic		
H/W & S/W	C and Assembly, PIC18f452	Team	2 members
Description	Objective of this project was to understand the concept of fuzzy logic for the development of microcontroller based automation, especially for controlling temperature.		

Title	Creation of Octave based computing package		
Environment	Linux OS, C and Octave	Team	2 members
Description	Objective of this project was to create computing package for I ² T on Linux operating system. We have used GNU Octave, which is a high-level language, primarily intended for numerical computations.		

Title	Secure Data communication using Cipher Technology		
Language	C and Assembly x86	Team	4 members
Description	Objective of this project was to realize Cryptography algorithms - Data Encryption Standard (DES) and RSA using C and assembly language.		

