

# LM1575/LM1575HV/LM2575/LM2575HV Series SIMPLE SWITCHER® 1A Step-Down Voltage Regulator

# **General Description**

The LM2575 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving a 1A load with excellent line and load regulation. These devices are available in fixed output voltages of 3.3V, 5V, 12V, 15V, and an adjustable output version.

Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensation and a fixed-frequency oscillator.

The LM2575 series offers a high-efficiency replacement for popular three-terminal linear regulators. It substantially reduces the size of the heat sink, and in many cases no heat sink is required.

A standard series of inductors optimized for use with the LM2575 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies.

Other features include a guaranteed  $\pm4\%$  tolerance on output voltage within specified input voltages and output load conditions, and  $\pm10\%$  on the oscillator frequency. External shutdown is included, featuring 50  $\mu$ A (typical) standby current. The output switch includes cycle-by-cycle current limiting, as well as thermal shutdown for full protection under fault conditions.

Features

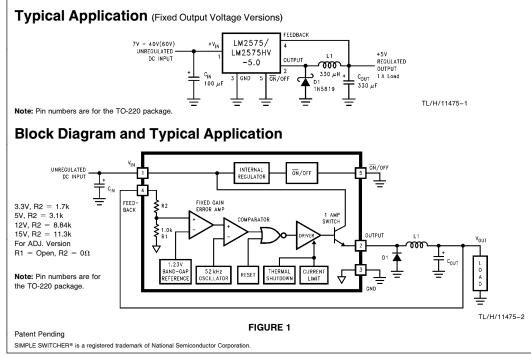
- 3.3V, 5V, 12V, 15V, and adjustable output versions
   Adjustable version output voltage range,
- 1.23V to 37V (57V for HV version)  $\pm4\%$  max over line and load conditions
- Guaranteed 1A output current
- Wide input voltage range, 40V up to 60V for HV version
- Requires only 4 external components
- 52 kHz fixed frequency internal oscillator
- TTL shutdown capability, low power standby mode
- High efficiency
- Uses readily available standard inductors
- Thermal shutdown and current limit protection
- P<sup>+</sup> Product Enhancement tested

# Applications

- Simple high-efficiency step-down (buck) regulator
- Efficient pre-regualtor for linear regulators
- On-card switching regulators
- Positive to negative converter (Buck-Boost)



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| If Military/Aerospace specified   |                             | Minimum ESD Rating                              |   |
|-----------------------------------|-----------------------------|---|---|
| please contact the National       |                             | $(C = 100 \text{ pF}, R = 1.5 \text{ k}\Omega)$ | 2 kV  |
| Office/Distributors for availabil | ity and specifications.     | Lead Temperature                                |   |
| Maximum Supply Voltage            |                             | (Soldering, 10 sec.)                            | 260°C                                       |
| LM1575/LM2575                     | 45V                         | Maximum Junction Temperature                    | 150°C                                       |
| LM1575HV/LM2575HV                 | 63V                         |   |   |
| ON/OFF Pin Input Voltage          | $-0.3V \leq V \leq +V_{IN}$ | Operating Ratings                               |   |
| Output Voltage to Ground          |                             | Temperature Range                               |   |
| (Steady State)                    | -1V                         | LM1575/LM1575HV                                 | $-55^{\circ}C \le T_{.1} \le +150^{\circ}C$ |
| Power Dissipation                 | Internally Limited          | LM2575/LM2575HV                                 | $-40^{\circ}C \le T_{J} \le +125^{\circ}C$  |
| Storage Temperature Range         | -65°C to +150°C             | Supply Voltage                                  |   |
|                                   |                             | LM1575/LM2575                                   | 40V   |
|                                   |                             | LM1575HV/LM2575HV                               | 60V   |

# LM1575-3.3, LM1575HV-3.3, LM2575-3.3, LM2575HV-3.3

**Electrical Characteristics** Specifications with standard type face are for  $T_J = 25^{\circ}C$ , and those with **boldface type** apply over **full Operating Temperature Range**.

| 0h.el            | Barrantan                           | 0 - m diking -   | <b>T</b> | LM1575-3.3<br>LM1575HV-3.3                 | LM2575-3.3<br>LM2575HV-3.3                 | Units                 |
|------------------|-------------------------------------|--|----------|--|--|-----------------------|
| Symbol           | Parameter                           | Conditions   | Тур      | Limit<br>(Note 2)                          | Limit<br>(Note 3)                          | (Limits)              |
| SYSTEM I         | PARAMETERS (Note 4) Te              | est Circuit <i>Figure 2</i>  |          | -  |  |                       |
| V <sub>OUT</sub> | Output Voltage                      | $V_{IN} = 12V$ , $I_{LOAD} = 0.2A$<br>Circuit of <i>Figure 2</i>             | 3.3      | 3.267<br>3.333                             | 3.234<br>3.366                             | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub> | Output Voltage<br>LM1575/LM2575     | $4.75V \leq V_{IN} \leq 40V, 0.2A \leq I_{LOAD} \leq 1A$ Circuit of Figure 2 | 3.3      | 3.200/ <b>3.168</b><br>3.400/ <b>3.432</b> | 3.168/ <b>3.135</b><br>3.432/ <b>3.465</b> | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub> | Output Voltage<br>LM1575HV/LM2575HV | $4.75V \leq V_{IN} \leq 60V, 0.2A \leq I_{LOAD} \leq 1A$ Circuit of Figure 2 | 3.3      | 3.200/ <b>3.168</b><br>3.416/ <b>3.450</b> | 3.168/ <b>3.135</b><br>3.450/ <b>3.482</b> | V<br>V(Min)<br>V(Max) |
| η                | Efficiency                          | $V_{IN} = 12V$ , $I_{LOAD} = 1A$   | 75       |  |  | %                     |

# LM1575-5.0, LM1575HV-5.0, LM2575-5.0, LM2575HV-5.0

**Electrical Characteristics** Specifications with standard type face are for  $T_J = 25^{\circ}C$ , and those with **boldface type** apply over **full Operating Temperature Range**.

| 0                | Demension                           | Que d'Aleres   | <b>T</b> | LM1575-5.0 LM2575-5.0<br>LM1575HV-5.0 LM2575HV-5.0 |  | Units                 |  |
|------------------|-------------------------------------|--|----------|--|--|-----------------------|--|
| Symbol           | Parameter                           | Conditions   | Тур      | Limit<br>(Note 2)                                  | Limit<br>(Note 3)                          | (Limits)              |  |
| SYSTEM PA        | ARAMETERS (Note 4) Test C           | Circuit <i>Figure 2</i>  |          |  |  |                       |  |
| V <sub>OUT</sub> | Output Voltage                      | $V_{IN} = 12V$ , $I_{LOAD} = 0.2A$<br>Circuit of <i>Figure 2</i>   | 5.0      | 4.950<br>5.050                                     | 4.900<br>5.100                             | V<br>V(Min)<br>V(Max) |  |
| V <sub>OUT</sub> | Output Voltage<br>LM1575/LM2575     | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 8V \leq V_{IN} \leq 40V \\ Circuit of Figure 2 \end{array}$ | 5.0      | 4.850/ <b>4.800</b><br>5.150/ <b>5.200</b>         | 4.800/ <b>4.750</b><br>5.200/ <b>5.250</b> | V<br>V(Min)<br>V(Max) |  |
| V <sub>OUT</sub> | Output Voltage<br>LM1575HV/LM2575HV | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 8V \leq V_{IN} \leq 60V \\ Circuit of Figure 2 \end{array}$ | 5.0      | 4.850/ <b>4.800</b><br>5.175/ <b>5.225</b>         | 4.800/ <b>4.750</b><br>5.225/ <b>5.275</b> | V<br>V(Min)<br>V(Max) |  |
| η                | Efficiency                          | $V_{IN} = 12V, I_{LOAD} = 1A$  | 77       |  |  | %                     |  |

| Symbol                        | Parameter                           | Conditions   | Turn | LM1575-12<br>LM1575HV-12                     | LM2575-12<br>LM2575HV-12                   | Units                 |
|-------------------------------|-------------------------------------|--|------|--|--|-----------------------|
| Symbol                        | Parameter                           | Conditions   | Тур  | Limit<br>(Note 2)                            | Limit<br>(Note 3)                          | (Limits               |
| SYSTEM F                      | PARAMETERS (Note 4) Tes             | at Circuit <i>Figure 2</i>   |      |  |  |                       |
| V <sub>OUT</sub>              | Output Voltage                      | $V_{IN} = 25V, I_{LOAD} = 0.2A$<br>Circuit of <i>Figure 2</i>  | 12   | 11.88<br>12.12                               | 11.76<br>12.24                             | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub>              | Output Voltage<br>LM1575/LM2575     | $\begin{array}{l} \text{0.2A} \leq \text{I}_{LOAD} \leq \text{1A}, \\ \text{15V} \leq \text{V}_{IN} \leq \text{40V} \\ \text{Circuit of } \textit{Figure 2} \end{array}$ | 12   | 11.64/ <b>11.52</b><br>12.36/ <b>12.48</b>   | 11.52/ <b>11.40</b><br>12.48/ <b>12.60</b> | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub>              | Output Voltage<br>LM1575HV/LM2575H  | $\begin{array}{c c} 0.2A \leq I_{LOAD} \leq 1A, \\ 15V \leq V_{IN} \leq 60V \\ Circuit of Figure 2 \end{array}$  | 12   | 11.64/ <b>11.52</b><br>12.42/ <b>12.54</b>   | 11.52/ <b>11.40</b><br>12.54/ <b>12.66</b> | V<br>V(Min)<br>V(Max  |
| η                             | Efficiency                          | V <sub>IN</sub> = 15V, I <sub>LOAD</sub> = 1A  | 88   |  |  | %                     |
| Symbol                        | Parameter                           | Conditions   | Тур  | Limit<br>(Note 2)                            | Limit<br>(Note 3)                          | (Limit                |
| SYSTEM F                      | PARAMETERS (Note 4) Tes             | st Circuit <i>Figure 2</i>   |      |  |  |                       |
| V <sub>OUT</sub>              | Output Voltage                      | $V_{IN} = 30V, I_{LOAD} = 0.2A$<br>Circuit of Figure 2   | 15   | 14.85<br>15.15                               | 14.70<br>15.30                             | V<br>V(Min<br>V(Max   |
| V <sub>OUT</sub>              | Output Voltage<br>LM1575/LM2575     | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 18V \leq V_{IN} \leq 40V \\ Circuit of \textit{Figure 2} \end{array}$   | 15   | 14.55/ <b>14.40</b><br>15.45/ <b>15.60</b>   | 14.40/ <b>14.25</b><br>15.60/ <b>15.75</b> | V<br>V(Min<br>V(Max   |
| V <sub>OUT</sub>              | Output Voltage<br>LM1575HV/LM2575HV | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 18V \leq V_{IN} \leq 60V \\ Circuit of \textit{Figure 2} \end{array}$   | 15   | 14.55/ <b>14.40</b><br>15.525/ <b>15.675</b> | 14.40/ <b>14.25</b><br>15.68/ <b>15.83</b> | V<br>V(Min<br>V(Max   |
| η                             | Efficiency                          | $V_{IN} = 18V, I_{LOAD} = 1A$  | 88   |  |  | %                     |
| Elec <sup>®</sup><br>Specific | trical Characteris                  | <b>5HV-ADJ, LM2575-A</b><br>stics<br>ace are for $T_J = 25^{\circ}$ C, and those   |      | Idface type apply                            | over full Operatio                         |                       |
|                               | Parameter                           | Conditions   | Ту   | p LM1575HV-AD                                | J LM2575HV-ADJ<br>Limit                    | Unit<br>Unit          |
| Symbol                        |                                     |  |      | (Nata 0)                                     | (Nata 2)                                   |                       |
| Symbol                        |                                     | t Circuit <i>Figure 2</i>  |      | (Note 2)                                     | (Note 3)                                   |                       |
| SYSTEM F                      | PARAMETERS (Note 4) Tes             |  | 12   |  | (Note 3)                                   | v                     |
| -                             | Feedback Voltage                    | st Circuit <i>Figure 2</i><br>$V_{IN} = 12V, I_{LOAD} = 0.2A$<br>$V_{OUT} = 5V$<br>Circuit of <i>Figure 2</i>  | 1.2  |  | (Note 3)                                   | V<br>V(Mi<br>V(Ma     |

|                  |                                       |  |       | (Note 2)                                   | (Note 3)                                   |                       |
|------------------|---------------------------------------|--|-------|--|--|-----------------------|
| SYSTEM           | PARAMETERS (Note 4) Te                | est Circuit <i>Figure 2</i>  |       |  |  |                       |
| V <sub>OUT</sub> | Feedback Voltage                      | $V_{IN} = 12V, I_{LOAD} = 0.2A$<br>$V_{OUT} = 5V$<br>Circuit of <i>Figure 2</i>  | 1.230 | 1.217<br>1.243                             | 1.217<br>1.243                             | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub> | Feedback Voltage<br>LM1575/LM2575     | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 8V \leq V_{IN} \leq 40V \\ V_{OUT} = 5V, \mbox{ Circuit of } \textit{Figure 2} \end{array}$ | 1.230 | 1.205/ <b>1.193</b><br>1.255/ <b>1.267</b> | 1.193/ <b>1.180</b><br>1.267/ <b>1.280</b> | V<br>V(Min)<br>V(Max) |
| V <sub>OUT</sub> | Feedback Voltage<br>LM1575HV/LM2575HV | $\begin{array}{l} 0.2A \leq I_{LOAD} \leq 1A, \\ 8V \leq V_{IN} \leq 60V \\ V_{OUT} = 5V, \mbox{ Circuit of } \textit{Figure 2} \end{array}$ | 1.230 | 1.205/ <b>1.193</b><br>1.261/ <b>1.273</b> | 1.193/ <b>1.180</b><br>1.273/ <b>1.286</b> | V<br>V(Min)<br>V(Max) |
| η                | Efficiency                            | $V_{\text{IN}} = 12V, I_{\text{LOAD}} = 1A, V_{\text{OUT}} = 5V$   | 77    |  |  | %                     |
|                  |                                       |  |       |  |  |                       |

# All Output Voltage Versions

**Electrical Characteristics** Specifications with standard type face are for  $T_J = 25^{\circ}C$ , and those with **boldface type** apply over **full Operating Temperature Range**. Unless otherwise specified,  $V_{IN} = 12V$  for the 3.3V, 5V, and Adjustable version,  $V_{IN} = 25V$  for the 12V version, and  $V_{IN} = 30V$  for the 15V version.  $I_{LOAD} = 200$  mA.

| Symbol   | Parameter                    | Conditions  | Tur   | LM1575-XX<br>LM1575HV-XX           | LM2575-XX<br>LM2575HV-XX           | Units                       |  |
|--|------------------------------|---|---|------------------------------------|------------------------------------|-----------------------------|--|
| Symbol   | Parameter                    | Conditions  | Тур   | Limit<br>(Note 2)                  | Limit<br>(Note 3)                  | (Limits)                    |  |
| DEVICE F   | PARAMETERS                   |   |   |                                    |                                    |                             |  |
| I <sub>b</sub>   | Feedback Bias Current        | $V_{OUT} = 5V$ (Adjustable Version Only)  | 50  | 100/500                            | 100/ <b>500</b>                    | nA                          |  |
| fO   | Oscillator Frequency         | (Note 13)   | 52  | 47/ <b>43</b><br>58/62             | 47/ <b>42</b><br>58/ <b>63</b>     | kHz<br>kHz(Min)<br>kHz(Max) |  |
| V <sub>SAT</sub>   | Saturation Voltage           | I <sub>OUT</sub> = 1A (Note 5)  | 0.9   | 1.2/ <b>1.4</b>                    | 1.2/ <b>1.4</b>                    | V<br>V(Max)                 |  |
| DC   | Max Duty Cycle (ON)          | (Note 6)  | 98  | 93                                 | 93                                 | %<br>%(Min)                 |  |
| I <sub>CL</sub>  | Current Limit                | Peak Current (Notes 5 and 13)   | 2.2   | 1.7/ <b>1.3</b><br>3.0/ <b>3.2</b> | 1.7/ <b>1.3</b><br>3.0/ <b>3.2</b> | A<br>A(Min)<br>A(Max)       |  |
| ΙL   | Output Leakage Current       | $\begin{array}{ll} \mbox{(Notes 7 and 8)} & \mbox{Output} = 0 V \\ \mbox{Output} = -1 V \\ \mbox{Output} = -1 V \\ \mbox{Output} = -1 V \end{array}$  | 7.5   | 2<br>30                            | 2<br>30                            | mA(Max)<br>mA<br>mA(Max)    |  |
| lQ   | Quiescent Current            | (Note 7)  | 5   | 10/12                              | 10                                 | mA<br>mA(Max)               |  |
| I <sub>STBY</sub>  | Standby Quiescent<br>Current | $\overline{ON}/OFF Pin = 5V (OFF)$  | 50  | 200/500                            | 200                                | μΑ<br>μΑ(Max)               |  |
| $ \begin{array}{c} \theta_{JA}\\ \theta_{JC}\\ \theta_{JA}\\ \theta_{JA}\\ \theta_{JC}\\ \theta_{JA}\\ \theta_{JA}\\ \theta_{JA} \end{array} $ | Thermal Resistance           | K Package, Junction to Ambient<br>K Package, Junction to Case<br>T Package, Junction to Ambient (Note 9)<br>T Package, Junction to Ambient (Note 10)<br>T Package, Junction to Case<br>N Package, Junction to Ambient (Note 11)<br>M Package, Junction to Ambient (Note 12) | 35<br>1.5<br>65<br>45<br>2<br>85<br>100<br>37 |                                    |                                    | °C/W                        |  |
| ON/OFF   | CONTROL Test Circuit Fig     |   |   | 1                                  |                                    |                             |  |
| VIH  | ON/OFF Pin Logic             | $V_{OUT} = 0V$  | 1.4   | 2.2/ <b>2.4</b>                    | 2.2/ <b>2.4</b>                    | V(Min)                      |  |
| VIL  | Input Level                  | V <sub>OUT</sub> = Nominal Output Voltage   | 1.2   | 1.0/ <b>0.8</b>                    | 1.0/ <b>0.8</b>                    | V(Max)                      |  |
| IIH  | ON/OFF Pin Input<br>Current  | $\overline{ON}/OFF Pin = 5V (OFF)$  | 12  | 30                                 | 30                                 | μΑ<br>μΑ(Max)               |  |
| IIL  |                              | $\overline{ON}/OFF Pin = 0V (ON)$   | 0   | 10                                 | 10                                 | μΑ<br>μΑ(Max)               |  |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. Note 2: All limits guaranteed at room temperature (standard type face) and at temperature extremes (**bold type face**). All limits are used to calculate Average Outgoing Quality Leei, and all are 100% production tested.

Note 3: All limits guaranteed at room temperature (standard type face) and at temperature extremes (**bold type face**). All room temperature limits are 100% production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods.

Note 4: External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance. When the LM1575/LM2575 is used as shown in the *Figure 2* test circuit, system performance will be as shown in system parameters section of Electrical Characteristics. Note 5: Output (pin 2) sourcing current. No diode, inductor or capacitor connected to output pin.

Note 6: Feedback (pin 4) removed from output and connected to 0V.

Note 7: Feedback (pin 4) removed from output and connected to +12V for the Adjustable, 3.3V, and 5V versions, and +25V for the 12V and 15V versions, to force the output transistor OFF.

Note 8:  $V_{IN}\,=\,40V$  (60V for the high voltage version).

### Electrical Characteristics (Notes) (Continued)

Note 9: Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with 1/2 inch leads in a socket, or on a PC board with minimum copper area.

Note 10: Junction to ambient thermal resistance (no external heat sink) for the 5 lead TO-220 package mounted vertically, with 1/2 inch leads soldered to a PC board containing approximately 4 square inches of copper area surrounding the leads.

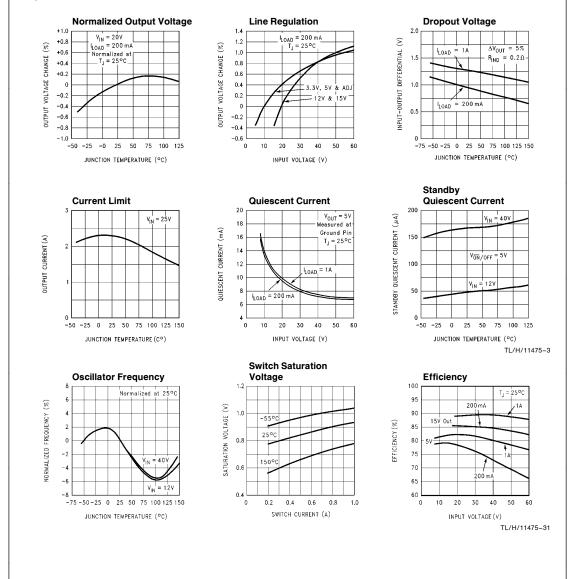
Note 11: Junction to ambient thermal resistance with approxmiately 1 square inch of pc board copper surrounding the leads. Additional copper area will lower thermal resistance further. See thermal model in Switchers made Simple software.

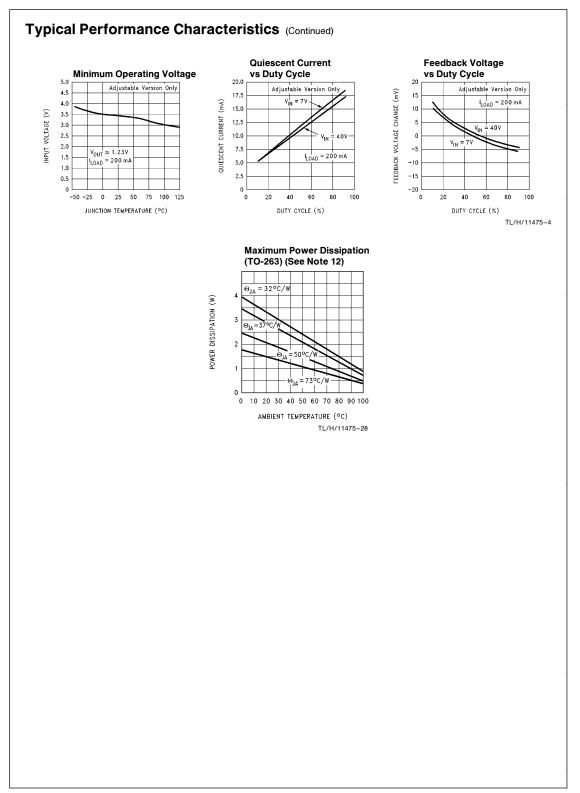
Note 12: If the TO-263 package is used, the thermal resistance can be reduced by increasing the PC board copper area thermally connected to the package: Using 0.5 square inches of copper area,  $\theta_{JA}$  is 30°C/W; with 1 square inch of copper area,  $\theta_{JA}$  is 37°C/W; and with 1.6 or more square inches of copper area,  $\theta_{JA}$  is 32°C/W.

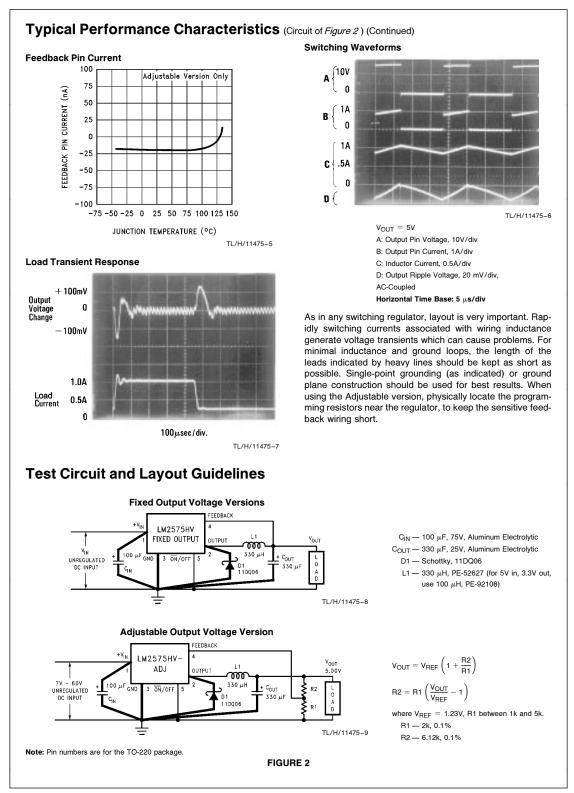
Note 13: The oscillator frequency reduces to approximately 18 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately 40% from the nominal output voltage. This self protection feature lowers the average power dissipation of the IC by lowering the minimum duty cycle from 5% down to approximately 2%.

Note 14: Refer to RETS LM1575K, LM1575HVK for current revision of military RETS/SMD.

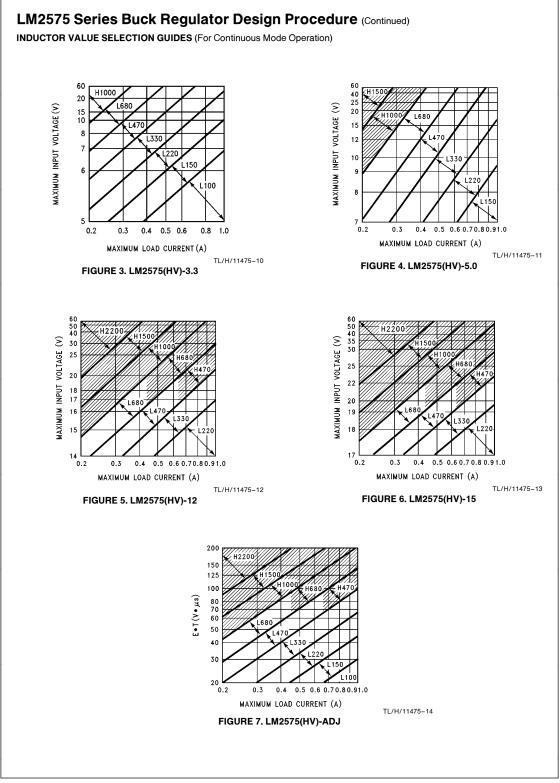
# Typical Performance Characteristics (Circuit of Figure 2)







| $_{OUT}$ = Regulated Output Voltage (3.3V, 5V, 12V, or 15V)<br>$_{IN}(Max)$ = Maximum Input Voltage<br>$_{OAD}(Max)$ = Maximum Load Current<br><b>ductor Selection (L1)</b><br>$_{Select}$ the correct Inductor value selection guide from<br><i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the | Giv<br>1.   | en:<br>$V_{OUT} = 5V$<br>$V_{IN}(Max) = 20V$<br>$I_{LOAD}(Max) = 0.8A$<br>Inductor Selection (L1)<br><b>A.</b> Use the selection guide shown in <i>Figure 4</i> .   |
|--|---|---|
| N(Max) = Maximum Input Voltage<br>OAD(Max) = Maximum Load Current<br><b>iductor Selection (L1)</b><br>Select the correct Inductor value selection guide from<br><i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the   | 1.  | $\begin{split} V_{IN}(Max) &= 20V \\ I_{LOAD}(Max) &= 0.8A \\ \textbf{Inductor Selection (L1)} \end{split}$   |
| $C_{OAD}(Max) = Maximum Load Current$<br><b>iductor Selection (L1)</b><br><b>i.</b> Select the correct Inductor value selection guide from<br><i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the   | 1.  | I <sub>LOAD</sub> (Max) = 0.8A<br>Inductor Selection (L1)   |
| <b>iductor Selection (L1)</b><br><b>a.</b> Select the correct Inductor value selection guide from<br><i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the  | 1.  | Inductor Selection (L1)   |
| <b>iductor Selection (L1)</b><br><b>a.</b> Select the correct Inductor value selection guide from<br><i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the  | 1.  | Inductor Selection (L1)   |
| Select the correct Inductor value selection guide from<br>igures 3, 4, 5, or 6. (Output voltages of 3.3V, 5V, 12V or<br>5V respectively). For other output voltages, see the   |   | . ,   |
| <i>igures 3, 4, 5,</i> or <i>6.</i> (Output voltages of $3.3V, 5V, 12V$ or 5V respectively). For other output voltages, see the  |   |   |
| 5V respectively). For other output voltages, see the   |   | <b>B.</b> From the selection guide, the inductance area   |
|  |   | intersected by the 20V line and 0.8A line is L330.  |
| esign procedure for the adjustable version.  |   | <b>C.</b> Inductor value required is $330 \ \mu\text{H}$ . From the table in  |
| From the inductor value selection guide, identify the  |   | <i>Figure 9</i> , choose AIE 415-0926, Pulse Engineering  |
| ductance region intersected by VIN(Max) and  |   | PE-52627, or RL1952.  |
| OAD (Max), and note the inductor code for that region.   |   | 1 2-02027, 01 1121002.  |
|  |   |   |
|  |   |   |
| iqure 9. Part numbers are listed for three inductor  |   |   |
| anufacturers. The inductor chosen must be rated for  |   |   |
| peration at the LM2575 switching frequency (52 kHz) and  |   |   |
|  |   |   |
| ductor information, see the inductor section in the  |   |   |
| pplication Hints section of this data sheet.   |   |   |
| utput Capacitor Selection (C <sub>OUT</sub> )  | 2.  | Output Capacitor Selection (C <sub>OUT</sub> )  |
| . The value of the output capacitor together with the  |   | <b>A.</b> $C_{OUT} = 100 \ \mu F$ to 470 $\mu F$ standard aluminum  |
| ductor defines the dominate pole-pair of the switching   |   | electrolytic.   |
| egulator loop. For stable operation and an acceptable  |   | <b>B.</b> Capacitor voltage rating $=$ 20V.   |
| utput ripple voltage, (approximately 1% of the output  |   |   |
|  |   |   |
| ecommended.  |   |   |
| . The capacitor's voltage rating should be at least 1.5  |   |   |
|  |   |   |
| rating of at least 8V is appropriate, and a 10V or 15V   |   |   |
| ating is recommended.  |   |   |
|  |   |   |
|  |   |   |
|  |   |   |
| -  |   |   |
|  | 3.  | Catch Diode Selection (D1)  |
| . The catch-diode current rating must be at least 1.2  |   | A. For this example, a 1A current rating is adequate.   |
|  |   | B. Use a 30V 1N5818 or SR103 Schottky diode, or any   |
|  |   | the suggested fast-recovery diodes shown in Figure 8.   |
|  |   |   |
|  |   |   |
|  |   |   |
|  |   |   |
|  |   |   |
| 1 5  |   | Innut Consolitor (C   |
|  | 4.  | Input Capacitor (C <sub>IN</sub> )  |
|  |   | A 47 $\mu\text{F},$ 25V aluminum electrolytic capacitor located n   |
| 5  |   | the input and ground pins provides sufficient bypassing   |
| peralion.  |   |   |
|  | Identify the inductor value from the inductor code, and<br>elect an appropriate inductor from the table shown in<br><i>gure 9.</i> Part numbers are listed for three inductor<br>anufacturers. The inductor chosen must be rated for<br>beration at the LM2575 switching frequency (52 kHz) and<br>or a current rating of $1.15 \times I_{LOAD}$ . For additional<br>ductor information, see the inductor section in the<br>pplication Hints section of this data sheet.<br><b>utput Capacitor Selection (C</b> <sub>OUT</sub> )<br>. The value of the output capacitor together with the<br>ductor defines the dominate pole-pair of the switching<br>egulator loop. For stable operation and an acceptable<br>utput ripple voltage, (approximately 1% of the output<br>oblage) a value between 100 $\mu$ F and 470 $\mu$ F is<br>ecommended.<br>. The capacitor's voltage rating should be at least 1.5<br>mes greater than the output voltage. For a 5V regulator,<br>rating of at least 8V is appropriate, and a 10V or 15V<br>titing is recommended.<br>BR numbers, and for this reasion it may be necessary to<br>elect a capacitor rated for a higher voltage than would<br>bormally be needed.<br>atch Diode Selection (D1) | . Identify the inductor value from the inductor code, and elect an appropriate inductor from the table shown in <i>gure 9.</i> Part numbers are listed for three inductor anufacturers. The inductor chosen must be rated for operation at the LM2575 switching frequency (52 kHz) and or a current rating of $1.15 \times I_{LOAD}$ . For additional ductor information, see the inductor section in the pplication Hints section of this data sheet.<br><b>utput Capacitor Selection (Cour)</b> The value of the output capacitor together with the ductor defines the dominate pole-pair of the switching egulator loop. For stable operation and an acceptable utput ripple voltage, (approximately 1% of the output obtage), avalue between 100 $\mu$ F and 470 $\mu$ F is seconmended.<br>The capacitor's voltage rating should be at least 1.5 mes greater than the output voltage. For a 5V regulator, rating of at least 8V is appropriate, and a 10V or 15V ting is recommended.<br>igher voltage electrolytic capacitors generally have lower SR numbers, and for this reasion it may be necessary to elect a capacitor rated for a higher voltage than would ormally be needed.<br><b>atch Diode Selection (D1)</b> The catch-diode current rating must be at least 1.2 mes greater than the maximum load current. Also, if the power supply design must withstand a continuous output nort, the diode should have a current rating equal to the aximum current limit of the LM2575. The most stressful ondition.<br>The reverse voltage rating of the diode should be at ast 1.25 times the maximum input voltage.<br><b>put Capacitor (CIN)</b> A naluminum or tantalum electrolytic bypass capacitor cated close to the regulator is needed for stable |





|    | PROCEDURE (Adjustable Output Voltage Versions)  | EXAMPLE (Adjustable Output Voltage Versions)  |
|----|---|---|
|    | Ven: $V_{OUT} =$ Regulated Output Voltage $V_{IN}(Max) =$ Maximum Input Voltage $I_{LOAD}(Max) =$ Maximum Load Current $F =$ Switching Frequency ( <i>Fixed at 52 kHz</i> ) <b>Programming Output Voltage</b> (Selecting R1 and R2, as<br>shown in Figure 2)Use the following formula to select the appropriate<br>resistor values. $V_{OUT} = V_{REF} \left(1 + \frac{R2}{R1}\right)$ where $V_{REF} = 1.23V$ R1 can be between 1k and 5k. (For best temperature<br>coefficient and stability with time, use 1% metal film<br>resistors) $R2 = R1 \left(\frac{V_{OUT}}{V_{REF}} - 1\right)$  | Given:<br>$V_{OUT} = 10V$ $V_{IN}(Max) = 25V$ $I_{LOAD}(Max) = 1A$ $F = 52 \text{ kHz}$ 1. Programming Output Voltage <i>(Selecting R1 and R2)</i><br>$V_{OUT} = 1.23\left(1 + \frac{R2}{R1}\right) \qquad \text{Select R1} = 1k$ $R2 = R1\left(\frac{V_{OUT}}{V_{REF}} - 1\right) = 1k\left(\frac{10V}{1.23V} - 1\right)$ $R2 = 1k (8.13 - 1) = 7.13k, \text{ closest 1% value is 7.15k}$  |
| 2. | Inductor Selection (L1)<br>A. Calculate the inductor Volt • microsecond constant,<br>$E • T (V • \mu s)$ , from the following formula:<br>$E • T = (V_{IN} - V_{OUT}) \frac{V_{OUT}}{V_{IN}} • \frac{1000}{F(in  kHz)} (V • \mu s)$<br>B. Use the E • T value from the previous formula and<br>match it with the E • T number on the vertical axis of the<br>Inductor Value Selection Guide shown in <i>Figure 7</i> .<br>C. On the horizontal axis, select the maximum load<br>current.<br>D. Identify the inductance region intersected by the E • T<br>value and the maximum load current value, and note the<br>inductor code for that region.<br>E. Identify the inductor value from the inductor code, and<br>select an appropriate inductor from the table shown in<br><i>Figure 9</i> . Part numbers are listed for three inductor<br>manufacturers. The inductor chosen must be rated for<br>operation at the LM2575 switching frequency (52 kHz)<br>and for a current rating of $1.15 \times I_{LOAD}$ . For additional<br>inductor information, see the inductor section in the<br>application hints section of this data sheet. | 2. Inductor Selection (L1)<br>A. Calculate $E \bullet T (V \bullet \mu s)$<br>$E \bullet T = (25 - 10) \bullet \frac{10}{25} \bullet \frac{1000}{52} = 115 V \bullet \mu s$<br>B. $E \bullet T = 115 V \bullet \mu s$<br>C. I <sub>LOAD</sub> (Max) = 1A<br>D. Inductance Region = H470<br>E. Inductor Value = 470 $\mu$ H <i>Choose from AIE</i><br><i>part #430-0634</i> , <i>Pulse Engineering</i><br><i>part #PE-53118, or Renco part #RL-1961.</i> |
| 3. | Output Capacitor Selection (C <sub>OUT</sub> )<br>A. The value of the output capacitor together with the inductor defines the dominate pole-pair of the switching regulator loop. For stable operation, the capacitor must satisfy the following requirement:<br>$C_{OUT} \ge 7.785 \frac{V_{IN}(Max)}{V_{OUT} \bullet L(\mu H)} (\mu F)$ The above formula yields capacitor values between 10 $\mu$ F and 2000 $\mu$ F that will satisfy the loop requirements for stable operation. But to achieve an acceptable output ripple voltage, (approximately 1% of the output voltage) and transient response, the output capacitor may need to be several times larger than the above formula yields.<br>B. The capacitor's voltage rating should be at last 1.5 times greater than the output voltage. For a 10V regulator, a rating of at least 15V or more is recommended.<br>Higher voltage electrolytic capacitor sgenerally have lower ESR numbers, and for this reasion it may be necessary to select a capacitor rate for a higher voltage than would normally be needed.  | 3. Output Capacitor Selection (C <sub>OUT</sub> )<br>A. $C_{OUT} > 7,785 \frac{25}{10 \cdot 150} = 130 \mu\text{F}$<br>However, for acceptable output ripple voltage select<br>$C_{OUT} \ge 220 \mu\text{F}$<br>$C_{OUT} = 220 \mu\text{F}$ electrolytic capacitor  |

| PROCEDURE (Adju   | ustable Output Voltag   | e Version   | s) E   | EXAMPLE (Adjustable Output Voltage Versions)                                   |   |                                   |  |  |  |
|---|---|---|--|--|---|-----------------------------------|--|--|--|
| times greater than ti<br>power supply design<br>short, the diode sho<br>maximum current lin<br>condition for this dio<br>See diode selection<br><b>B.</b> The reverse volta<br>least 1.25 times the<br><b>Input Capacitor (C</b><br>An aluminum or tan  | current rating must be<br>he maximum load current<br>in must withstand a con-<br>uld have a current ratin<br>nit of the LM2575. The<br>ode is an overload or sh<br>guide in <i>Figure 8.</i><br>guide in <i>Figure 8.</i><br>ge rating of the diode<br>maximum input voltag | ent. Also, if<br>tinuous ou<br>ng equal to<br>most stres<br>norted outp<br>should be<br>e.<br>es capacito | 2     A. For       the     B. Use       tput     sugges       the     sugges       ssful     nut.       at     5. Input       or     A 100 | e a 40V MBR340<br>sted fast-recove<br><b>Capacitor (CIN)</b><br>μF aluminum el | 3A current rating is a<br>) or 31DQ04 Schottk<br>ry diodes in <i>Figure 8</i> | y diode, or any of t              |  |  |  |
| To further simplify th  | ne buck regulator   |   | Scho   |  | East B  | ecovery                           |  |  |  |
| design procedure, National Semicon-<br>ductor is making available computer<br>design software to be used with the<br>Simple Switcher line of switching regu-<br>lators. <b>Switchers Made Simple</b> (ver-<br>sion 3.3) is available on a (3½") disk-<br>ette for IBM compatible computers<br>from a National Semiconductor sales<br>office in your area. |   | VR  | 1A   | 3A   | 14  | 3A                                |  |  |  |
|   |   | 20V<br>30V  | 1N5817<br>MBR120P<br>SR102<br>1N5818   | 1N5820<br>MBR320<br>SR302<br>1N5821  |   |                                   |  |  |  |
|   |   |   | MBR130P<br>11DQ03<br>SR103   | MBR330<br>31DQ03<br>SR303  | The following diodes are all  | The following diodes are all      |  |  |  |
|   |   | 40V   | 1N5819<br>MBR140P<br>11DQ04<br>SR104   | IN5822<br>MBR340<br>31DQ04<br>SR304  | rated to 100V<br>11DF1<br>MUR110  | rated to 100V<br>31DF1<br>MURD310 |  |  |  |
|   |   | 50V   | MBR150<br>11DQ05<br>SR105  | MBR350<br>31DQ05<br>SR305  | HER102  | HER302                            |  |  |  |
|   |   | 60V   | MBR160<br>11DQ06<br>SR106  | MBR360<br>31DQ06<br>SR306  |   |                                   |  |  |  |
|   |   |   | FIC  | URE 8. Diode   | Selection Guide   |                                   |  |  |  |
| Inductor<br>Code  | Inductor<br>Value   |   | Schott<br>(Note 1)   |  | ulse Eng.<br>(Note 2)   | Renco<br>(Note 3)                 |  |  |  |
| L100  | 100 μH  | 100 μH  |  | F  | PE-92108  | RL2444                            |  |  |  |
| L150  | 150 μH  |   | 67127010   |  | PE-53113  | RL1954                            |  |  |  |
| L220  | 220 µH  |   | 67127020   |  | PE-52626  | RL1953                            |  |  |  |
| L330  | 330 µH  |   | 67127030   |  | PE-52627  | RL1952                            |  |  |  |
| L470  | 470 μH  |   | 67127040   |  | PE-53114  | RL1951                            |  |  |  |
| L680  | 680 μH  |   | 67127050   |  | PE-52629  | RL1950                            |  |  |  |
| H150  | 150 μH  |   | 67127060   |  | PE-53115  | RL2445                            |  |  |  |
| H220  | 220 μH  |   | 67127070   |  | PE-53116<br>PE-53117  | RL2446                            |  |  |  |
| H330<br>H470  | 330 μH<br>470 μH  |   | 67127080<br>67127090   |  | PE-53117  | RL2447<br>RL1961                  |  |  |  |
| H470<br>H680  | 680 μH  |   | 67127090   |  | PE-53119  | RL1961                            |  |  |  |
| H1000   | 1000 μH   |   | 67127100   |  | PE-53120  | RL1959                            |  |  |  |
| 111000  |   |   |  |  |   | RL1958                            |  |  |  |
| H1500   |   | 1500 μH 67127120 PE-53121   |  |  |   |                                   |  |  |  |

FIGURE 9. Inductor Selection by Manufacturer's Part Number

# **Application Hints**

# INPUT CAPACITOR (CIN)

To maintain stability, the regulator input pin must be bypassed with at least a 47  $\mu F$  electrolytic capacitor. The capacitor's leads must be kept short, and located near the regulator.

If the operating temperature range includes temperatures below  $-25^{\circ}$ C, the input capacitor value may need to be larger. With most electrolytic capacitors, the capacitance value decreases and the ESR increases with lower temperatures and age. Paralleling a ceramic or solid tantalum capacitor will increase the regulator stability at cold temperatures. For maximum capacitor operating lifetime, the capacitor's RMS ripple current rating should be greater than

$$\begin{split} 1.2 \times \left(\frac{ION}{T}\right) \times I_{LOAD} \\ \text{where } \ \frac{t_{ON}}{T} &= \frac{V_{OUT}}{V_{IN}} \text{ for a buck regulator} \\ \text{and } \ \frac{t_{ON}}{T} &= \frac{|V_{OUT}|}{|V_{OUT}| + V_{IN}} \text{ for a buck-boost regulator.} \end{split}$$

#### INDUCTOR SELECTION

/+ \

All switching regulators have two basic modes of operation: continuous and discontinuous. The difference between the two types relates to the inductor current, whether it is flowing continuously, or if it drops to zero for a period of time in the normal switching cycle. Each mode has distinctively different operating characteristics, which can affect the regulator performance and requirements.

The LM2575 (or any of the Simple Switcher family) can be used for both continuous and discontinuous modes of operation.

The inductor value selection guides in *Figures 3* through 7 were designed for buck regulator designs of the continuous inductor current type. When using inductor values shown in the inductor selection guide, the peak-to-peak inductor ripple current will be approximately 20% to 30% of the maximum DC current. With relatively heavy load currents, the circuit operates in the continuous mode (inductor current always flowing), but under light load conditions, the circuit will be forced to the discontinuous mode (inductor current falls to zero for a period of time). This discontinuous mode of operation is perfectly acceptable. For light loads (less than approximately 200 mA) it may be desirable to operate the regulator in the discontinuous mode, primarily because of the lower inductor values required for the discontinuous mode.

The selection guide chooses inductor values suitable for continuous mode operation, but if the inductor value chosen is prohibitively high, the designer should investigate the possibility of discontinuous operation. The computer design software *Switchers Made Simple* will provide all component values for discontinuous (as well as continuous) mode of operation.

Inductors are available in different styles such as pot core, toriod, E-frame, bobbin core, etc., as well as different core materials, such as ferrites and powdered iron. The least expensive, the bobbin core type, consists of wire wrapped on a ferrite rod core. This type of construction makes for an inexpensive inductor, but since the magnetic flux is not completely contained within the core, it generates more electromagnetic interference (EMI). This EMI can cause problems in sensitive circuits, or can give incorrect scope readings because of induced voltages in the scope probe.

The inductors listed in the selection chart include ferrite pot core construction for AIE, powdered iron toroid for Pulse Engineering, and ferrite bobbin core for Renco.

An inductor should not be operated beyond its maximum rated current because it may saturate. When an inductor begins to saturate, the inductance decreases rapidly and the inductor begins to look mainly resistive (the DC resistance of the winding). This will cause the switch current to rise very rapidly. Different inductor types have different saturation characteristics, and this should be kept in mind when selecting an inductor.

The inductor manufacturer's data sheets include current and energy limits to avoid inductor saturation.

### INDUCTOR RIPPLE CURRENT

When the switcher is operating in the continuous mode, the inductor current waveform ranges from a triangular to a sawtooth type of waveform (depending on the input voltage). For a given input voltage and output voltage, the peak-to-peak amplitude of this inductor current waveform remains constant. As the load current rises or falls, the entire sawtooth current waveform also rises or falls. The average DC value of this waveform is equal to the DC load current (in the buck regulator configuration).

If the load current drops to a low enough level, the bottom of the sawtooth current waveform will reach zero, and the switcher will change to a discontinuous mode of operation. This is a perfectly acceptable mode of operation. Any buck switching regulator (no matter how large the inductor value is) will be forced to run discontinuous if the load current is light enough.

#### OUTPUT CAPACITOR

An output capacitor is required to filter the output voltage and is needed for loop stability. The capacitor should be located near the LM2575 using short pc board traces. Standard aluminum electrolytics are usually adequate, but low ESR types are recommended for low output ripple voltage and good stability. The ESR of a capacitor depends on many factors, some which are: the value, the voltage rating, physical size and the type of construction. In general, low value or low voltage (less than 12V) electrolytic capacitors usually have higher ESR numbers.

The amount of output ripple voltage is primarily a function of the ESR (Equivalent Series Resistance) of the output capacitor and the amplitude of the inductor ripple current ( $\Delta I_{IND}$ ). See the section on inductor ripple current in Application Hints.

The lower capacitor values (220  $\mu$ F-680  $\mu$ F) will allow typically 50 mV to 150 mV of output ripple voltage, while larger-value capacitors will reduce the ripple to approximately 20 mV to 50 mV.

Output Ripple Voltage =  $(\Delta I_{IND})$  (ESR of C<sub>OUT</sub>)

# Application Hints (Continued)

To further reduce the output ripple voltage, several standard electrolytic capacitors may be paralleled, or a higher-grade capacitor may be used. Such capacitors are often called "high-frequency," "low-inductance," or "low-ESR." These will reduce the output ripple to 10 mV or 20 mV. However, when operating in the continuous mode, reducing the ESR below  $0.05\Omega$  can cause instability in the regulator.

Tantalum capacitors can have a very low ESR, and should be carefully evaluated if it is the only output capacitor. Because of their good low temperature characteristics, a tantalum can be used in parallel with aluminum electrolytics, with the tantalum making up 10% or 20% of the total capacitance.

The capacitor's ripple current rating at 52 kHz should be at least 50% higher than the peak-to-peak inductor ripple current.

### CATCH DIODE

Buck regulators require a diode to provide a return path for the inductor current when the switch is off. This diode should be located close to the LM2575 using short leads and short printed circuit traces.

Because of their fast switching speed and low forward voltage drop, Schottky diodes provide the best efficiency, especially in low output voltage switching regulators (less than 5V). Fast-Recovery, High-Efficiency, or Ultra-Fast Recovery diodes are also suitable, but some types with an abrupt turnoff characteristic may cause instability and EMI problems. A fast-recovery diode with soft recovery characteristics is a better choice. Standard 60 Hz diodes (e.g., 1N4001 or 1N5400, etc.) are also **not suitable**. See *Figure 8* for Schottky and "soft" fast-recovery diode selection guide.

#### **OUTPUT VOLTAGE RIPPLE AND TRANSIENTS**

The output voltage of a switching power supply will contain a sawtooth ripple voltage at the switcher frequency, typically about 1% of the output voltage, and may also contain short voltage spikes at the peaks of the sawtooth waveform.

The output ripple voltage is due mainly to the inductor sawtooth ripple current multiplied by the ESR of the output capacitor. (See the inductor selection in the application hints.) The voltage spikes are present because of the the fast switching action of the output switch, and the parasitic inductance of the output filter capacitor. To minimize these voltage spikes, special low inductance capacitors can be used, and their lead lengths must be kept short. Wiring inductance, stray capacitance, as well as the scope probe used to evaluate these transients, all contribute to the amplitude of these spikes.

An additional small LC filter (20  $\mu$ H & 100  $\mu$ F) can be added to the output (as shown in *Figure 15*) to further reduce the amount of output ripple and transients. A 10  $\times$  reduction in output ripple voltage and transients is possible with this filter.

### FEEDBACK CONNECTION

The LM2575 (fixed voltage versions) feedback pin must be wired to the output voltage point of the switching power supply. When using the adjustable version, physically locate both output voltage programming resistors near the LM2575 to avoid picking up unwanted noise. Avoid using resistors greater than 100 k $\Omega$  because of the increased chance of noise pickup.

#### **ON/OFF INPUT**

For normal operation, the  $\overline{ON}/OFF$  pin should be grounded or driven with a low-level TTL voltage (typically below 1.6V). To put the regulator into standby mode, drive this pin with a high-level TTL or CMOS signal. The  $\overline{ON}/OFF$  pin can be safely pulled up to  $+V_{IN}$  without a resistor in series with it. The  $\overline{ON}/OFF$  pin should not be left open.

#### GROUNDING

To maintain output voltage stability, the power ground connections must be low-impedance (see *Figure 2*). For the TO-3 style package, the case is ground. For the 5-lead TO-220 style package, both the tab and pin 3 are ground and either connection may be used, as they are both part of the same copper lead frame.

With the N or M packages, all the pins labeled ground, power ground, or signal ground should be soldered directly to wide printed circuit board copper traces. This assures both low inductance connections and good thermal properties.

#### HEAT SINK/THERMAL CONSIDERATIONS

In many cases, no heat sink is required to keep the LM2575 junction temperature within the allowed operating range. For each application, to determine whether or not a heat sink will be required, the following must be identified:

- 1. Maximum ambient temperature (in the application).
- 2. Maximum regulator power dissipation (in application).
- Maximum allowed junction temperature (150°C for the LM1575 or 125°C for the LM2575). For a safe, conservative design, a temperature approximately 15°C cooler than the maximum temperature should be selected.
- 4. LM2575 package thermal resistances  $\theta_{JA}$  and  $\theta_{JC}$ .

Total power dissipated by the LM2575 can be estimated as follows:

### $P_{D} = (V_{IN}) (I_{Q}) + (V_{O}/V_{IN}) (I_{LOAD}) (V_{SAT})$

where I<sub>Q</sub> (quiescent current) and V<sub>SAT</sub> can be found in the Characteristic Curves shown previously, V<sub>IN</sub> is the applied minimum input voltage, V<sub>O</sub> is the regulated output voltage, and I<sub>LOAD</sub> is the load current. The dynamic losses during turn-on and turn-off are negligible if a Schottky type catch diode is used.

### Application Hints (Continued)

When no heat sink is used, the junction temperature rise can be determined by the following:

$$\Delta T_{J} = (P_{D}) (\theta_{JA})$$

To arrive at the actual operating junction temperature, add the junction temperature rise to the maximum ambient temperature.

$$T_J = \Delta T_J + T_A$$

If the actual operating junction temperature is greater than the selected safe operating junction temperature determined in step 3, then a heat sink is required.

When using a heat sink, the junction temperature rise can be determined by the following:

 $\Delta T_{J} = (P_{D}) (\theta_{JC} + \theta_{interface} + \theta_{Heat sink})$ The operating junction temperature will be:

 $T_J = T_A + \Delta T_J$ 

As above, if the actual operating junction temperature is greater than the selected safe operating junction temperature, then a larger heat sink is required (one that has a lower thermal resistance).

When using the LM2575 in the plastic DIP (N) or surface mount (M) packages, several items about the thermal properties of the packages should be understood. The majority of the heat is conducted out of the package through the leads, with a minor portion through the plastic parts of the package. Since the lead frame is solid copper, heat from the die is readily conducted through the leads to the printed circuit board copper, which is acting as a heat sink.

For best thermal performance, the ground pins and all the unconnected pins should be soldered to generous amounts of printed circuit board copper, such as a ground plane. Large areas of copper provide the best transfer of heat to the surrounding air. Copper on both sides of the board is also helpful in getting the heat away from the package, even if there is no direct copper contact between the two sides. Thermal resistance numbers as low as 40°C/W for the SO package, and 30°C/W for the N package can be realized with a carefully engineered pc board.

Included on the *Switchers Made Simple* design software is a more precise (non-linear) thermal model that can be used to determine junction temperature with different input-output parameters or different component values. It can also calculate the heat sink thermal resistance required to maintain the regulators junction temperature below the maximum operating temperature.

# **Additional Applications**

## INVERTING REGULATOR

Figure 10 shows a LM2575-12 in a buck-boost configuration to generate a negative 12V output from a positive input voltage. This circuit bootstraps the regulator's ground pin to the negative output voltage, then by grounding the feedback pin, the regulator senses the inverted output voltage and regulates it to -12V.

For an input voltage of 12V or more, the maximum available output current in this configuration is approximately 0.35A. At lighter loads, the minimum input voltage required drops to approximately 4.7V.

The switch currents in this buck-boost configuration are higher than in the standard buck-mode design, thus lowering the available output current. Also, the start-up input current of the buck-boost converter is higher than the standard buck-mode regulator, and this may overload an input power source with a current limit less than 1.5A. Using a delayed turn-on or an undervoltage lockout circuit (described in the next section) would allow the input voltage to rise to a high enough level before the switcher would be allowed to turn on.

Because of the structural differences between the buck and the buck-boost regulator topologies, the buck regulator design procedure section can not be used to to select the inductor or the output capacitor. The recommended range of inductor values for the buck-boost design is between  $68 \ \mu\text{H}$  and  $220 \ \mu\text{H}$ , and the output capacitor values must be larger than what is normally required for buck designs. Low input voltages or high output currents require a large value output capacitor (in the thousands of micro Farads).

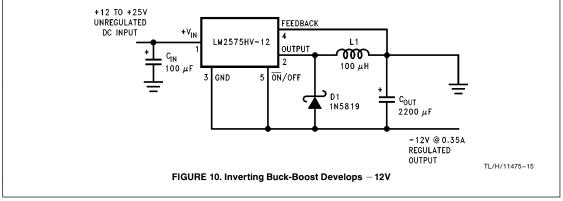
The peak inductor current, which is the same as the peak switch current, can be calculated from the following formula:

$$I_p \approx \frac{I_{\text{LOAD}} \left( V_{\text{IN}} + |V_0| \right)}{V_{\text{IN}}} + \frac{V_{\text{IN}} |V_0|}{V_{\text{IN}} + |V_0|} \times \frac{1}{2 L_1 f_{\text{os}}}$$

Where  $f_{\text{OSC}}=52$  kHz. Under normal continuous inductor current operating conditions, the minimum  $V_{IN}$  represents the worst case. Select an inductor that is rated for the peak current anticipated.

Also, the maximum voltage appearing across the regulator is the absolute sum of the input and output voltage. For a -12V output, the maximum input voltage for the LM2575 is +28V, or +48V for the LM2575HV.

The *Switchers Made Simple* (version 3.3) design software can be used to determine the feasibility of regulator designs using different topologies, different input-output parameters, different components, etc.

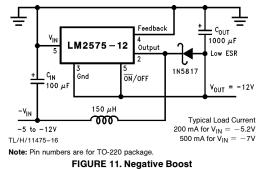


# Additional Applications (Continued)

# NEGATIVE BOOST REGULATOR

Another variation on the buck-boost topology is the negative boost configuration. The circuit in *Figure 11* accepts an input voltage ranging from -5V to -12V and provides a regulated -12V output. Input voltages greater than -12V will cause the output to rise above -12V, but will not damage the regulator.

Because of the boosting function of this type of regulator, the switch current is relatively high, especially at low input voltages. Output load current limitations are a result of the maximum current rating of the switch. Also, boost regulators can not provide current limiting load protection in the event of a shorted load, so some other means (such as a fuse) may be necessary.



#### Theorie Theogenive

### UNDERVOLTAGE LOCKOUT

In some applications it is desirable to keep the regulator off until the input voltage reaches a certain threshold. An undervoltage lockout circuit which accomplishes this task is shown in *Figure 12*, while *Figure 13* shows the same circuit applied to a buck-boost configuration. These circuits keep the regulator off until the input voltage reaches a predetermined level.

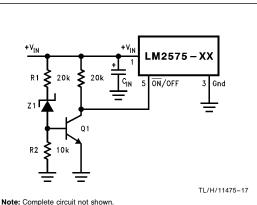
$$V_{TH} \approx V_{71} + 2V_{BF}$$
 (Q1)

### DELAYED STARTUP

The  $\overline{\text{ON}}/\text{OFF}$  pin can be used to provide a delayed startup feature as shown in *Figure 14*. With an input voltage of 20V and for the part values shown, the circuit provides approximately 10 ms of delay time before the circuit begins switching. Increasing the RC time constant can provide longer delay times. But excessively large RC time constants can cause problems with input voltages that are high in 60 Hz or 120 Hz ripple, by coupling the ripple into the  $\overline{\text{ON}}/\text{OFF}$  pin.

### ADJUSTABLE OUTPUT, LOW-RIPPLE POWER SUPPLY

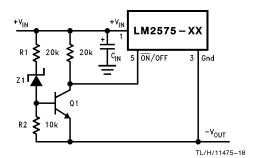
A 1A power supply that features an adjustable output voltage is shown in *Figure 15*. An additional L-C filter that reduces the output ripple by a factor of 10 or more is included in this circuit.



Note: Complete circuit not snown.

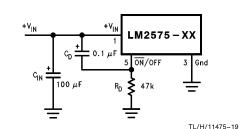
Note: Pin numbers are for the TO-220 package.

FIGURE 12. Undervoltage Lockout for Buck Circuit



**Note:** Complete circuit not shown (see *Figure 10*). **Note:** Pin numbers are for the TO-220 package.

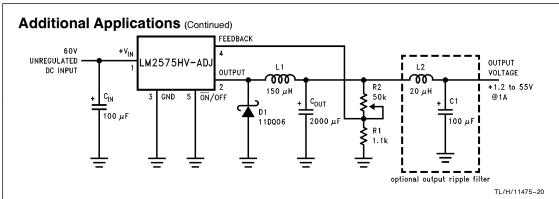
FIGURE 13. Undervoltage Lockout for Buck-Boost Circuit



Note: Complete circuit not shown.

Note: Pin numbers are for the TO-220 package.

FIGURE 14. Delayed Startup



Note: Pin numbers are for the TO-220 package

FIGURE 15. 1.2V to 55V Adjustable 1A Power Supply with Low Output Ripple

# **Definition of Terms**

### BUCK REGULATOR

A switching regulator topology in which a higher voltage is converted to a lower voltage. Also known as a step-down switching regulator.

### BUCK-BOOST REGULATOR

A switching regulator topology in which a positive voltage is converted to a negative voltage without a transformer.

### DUTY CYCLE (D)

Ratio of the output switch's on-time to the oscillator period.

$$\begin{array}{ll} \mbox{for buck regulator} & D = \frac{t_{ON}}{T} = \frac{V_{OUT}}{V_{IN}} \\ \mbox{for buck-boost regulator} & D = \frac{t_{ON}}{T} = \frac{|V_O|}{|V_O| + V_{IN}} \end{array}$$

### CATCH DIODE OR CURRENT STEERING DIODE

The diode which provides a return path for the load current when the LM2575 switch is OFF.

## EFFICIENCY ( $\eta$ )

The proportion of input power actually delivered to the load.

$$\eta = \frac{\mathsf{P}_{\mathsf{OUT}}}{\mathsf{P}_{\mathsf{IN}}} = \frac{\mathsf{P}_{\mathsf{OUT}}}{\mathsf{P}_{\mathsf{OUT}} + \mathsf{P}_{\mathsf{LOSS}}}$$

#### CAPACITOR EQUIVALENT SERIES RESISTANCE (ESR)

The purely resistive component of a real capacitor's impedance (see *Figure 16*). It causes power loss resulting in capacitor heating, which directly affects the capacitor's operating lifetime. When used as a switching regulator output filter, higher ESR values result in higher output ripple voltages.

#### FIGURE 16. Simple Model of a Real Capacitor

Most standard aluminum electrolytic capacitors in the 100  $\mu\text{F}-1000~\mu\text{F}$  range have  $0.5\Omega$  to  $0.1\Omega$  ESR. Higher-grade capacitors ("low-ESR", "high-frequency", or "low-inductance") in the 100  $\mu\text{F}-1000~\mu\text{F}$  range generally have ESR of less than  $0.15\Omega.$ 

### EQUIVALENT SERIES INDUCTANCE (ESL)

The pure inductance component of a capacitor (see *Figure 16*). The amount of inductance is determined to a large extent on the capacitor's construction. In a buck regulator, this unwanted inductance causes voltage spikes to appear on the output.

### OUTPUT RIPPLE VOLTAGE

The AC component of the switching regulator's output voltage. It is usually dominated by the output capacitor's ESR multiplied by the inductor's ripple current ( $\Delta I_{IND}$ ). The peak-to-peak value of this sawtooth ripple current can be determined by reading the Inductor Ripple Current section of the Application hints.

#### CAPACITOR RIPPLE CURRENT

RMS value of the maximum allowable alternating current at which a capacitor can be operated continuously at a specified temperature.

### STANDBY QUIESCENT CURRENT (ISTBY)

Supply current required by the LM2575 when in the standby mode ( $\overline{ON}/OFF$  pin is driven to TTL-high voltage, thus turning the output switch OFF).

# INDUCTOR RIPPLE CURRENT (AIIND)

The peak-to-peak value of the inductor current waveform, typically a sawtooth waveform when the regulator is operating in the continuous mode (vs. discontinuous mode).

## CONTINUOUS/DISCONTINUOUS MODE OPERATION

Relates to the inductor current. In the continuous mode, the inductor current is always flowing and never drops to zero, vs. the discontinuous mode, where the inductor current drops to zero for a period of time in the normal switching cycle.

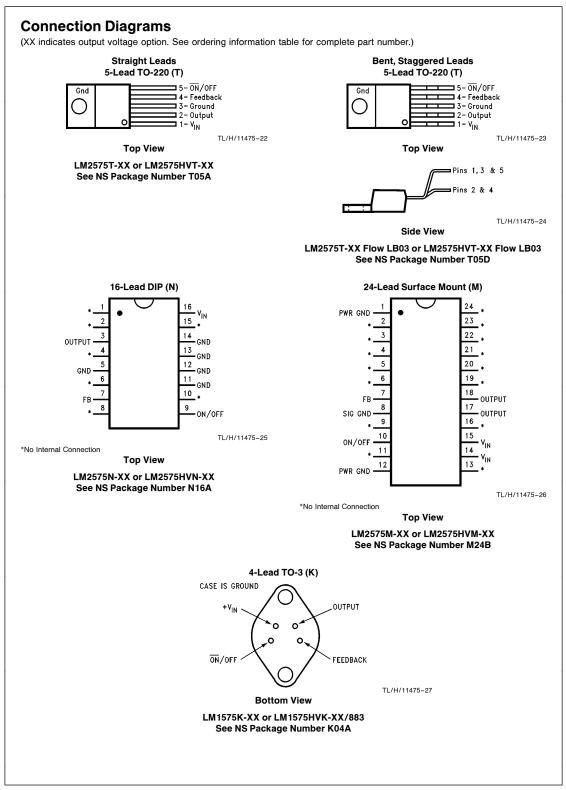
### INDUCTOR SATURATION

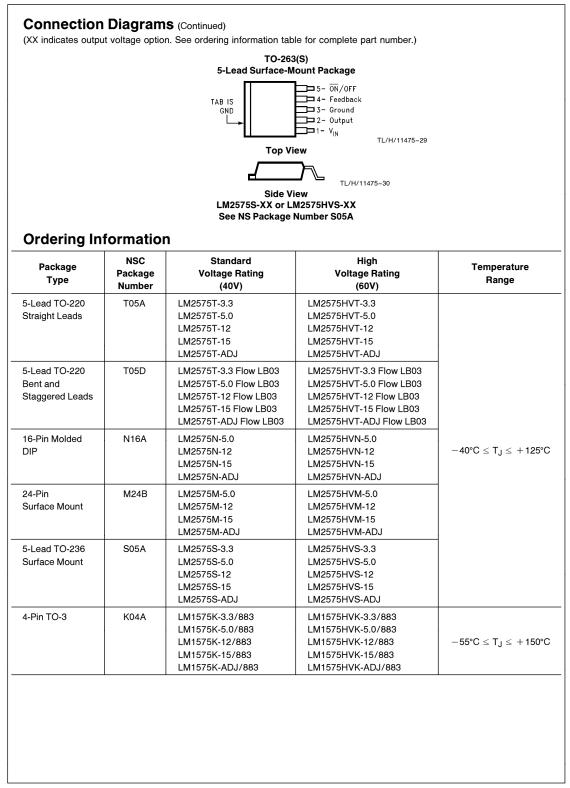
The condition which exists when an inductor cannot hold any more magnetic flux. When an inductor saturates, the inductor appears less inductive and the resistive component dominates. Inductor current is then limited only by the DC resistance of the wire and the available source current.

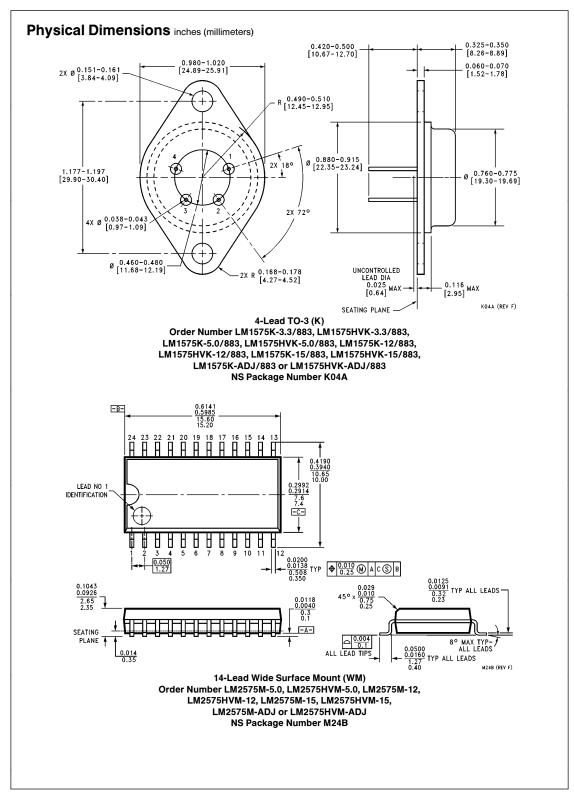
# OPERATING VOLT MICROSECOND CONSTANT (E•Top)

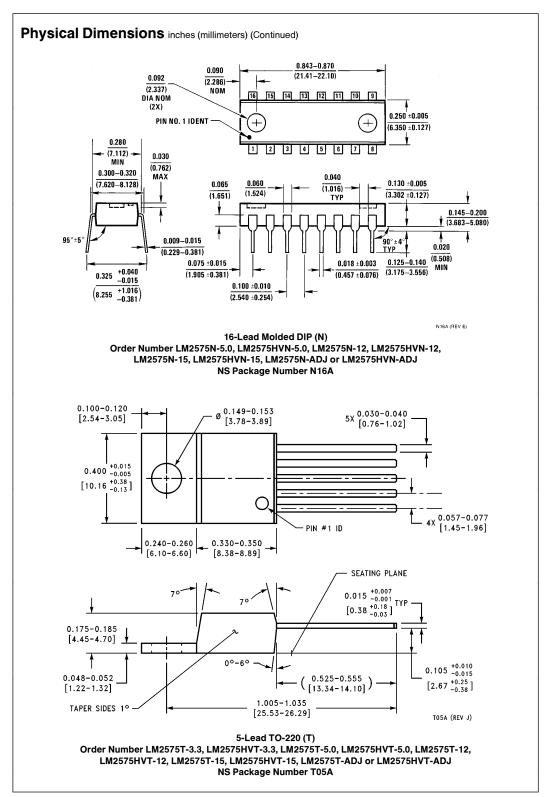
The product (in Volt•µs) of the voltage applied to the inductor and the time the voltage is applied. This E•T<sub>op</sub> constant is a measure of the energy handling capability of an inductor and is dependent upon the type of core, the core area, the number of turns, and the duty cycle.

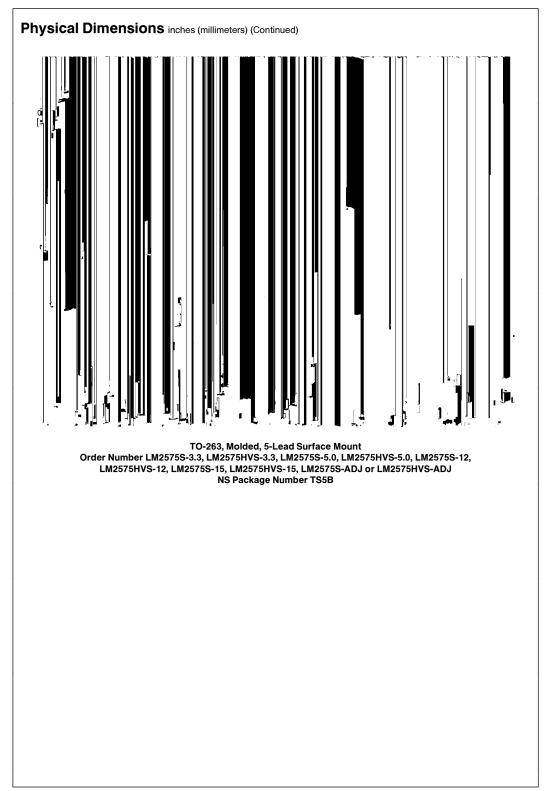
TI /H/11475-21

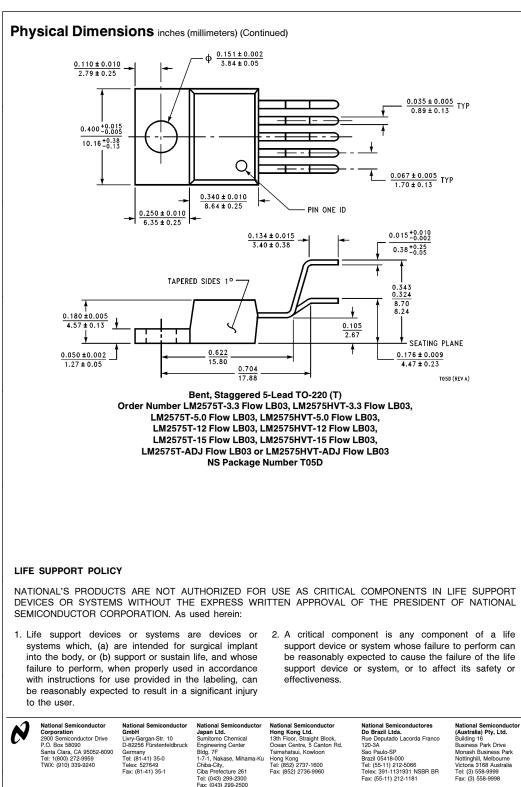












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